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The schematic diagram illustrates the internal circuitry of the V_Sensor1 and V_Sensor2 modules. Each module is a voltage divider circuit. The input voltage (V5_IN) is applied to a 10k resistor (R1, R2). The output of this resistor is connected to a 180k resistor (R20, R21), which is also connected to a 5V input (V5_NRF). A 470pF capacitor (C5, C6) is connected in parallel with the 180k resistor. The output of the 180k resistor is connected to a 60R resistor (R24, R25), which is also connected to a 5V input (V5_NRF). The output of the 60R resistor is connected to a 13k resistor (R26, R27), which is also connected to a 5V input (V5_NRF). The output of the 13k resistor is the sensor output (V_SENSOR1, V_SENSOR2). The output is also connected to a 10k resistor (R18, R19) and a 13k resistor (R28, R29).

This ensures LD01 is not supplying power while boost1 is enabled

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