

# ELC 2137 Lab 4: Subtractor

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## Summary

The Purpose of this lab was to familiarize the class with the basics of the subtractor circuit. We were introduced to Half, Full, and Two-Bit adders in the previous lab by building the individual circuits and completing the Two-Bit Circuit diagram in Pre-Lab 4. Then we furthered our understanding by building the subtractor in the lab using XOR, and AND gates.

## Q&A

1. Which gates could we use for combining the carry bits?

## Results