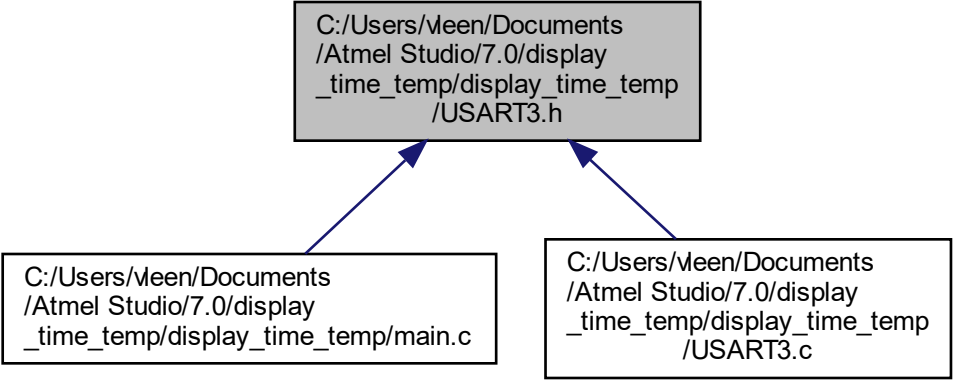


C:/Users/Meen/Documents  
/Atmel Studio/7.0/display  
\_time\_temp/display\_time\_temp  
/USART3.h



```
graph BT; A["C:/Users/Meen/Documents/Atmel Studio/7.0/display_time_temp/display_time_temp/USART3.h"] <--> B["C:/Users/Meen/Documents/Atmel Studio/7.0/display_time_temp/display_time_temp/main.c"]; A <--> C["C:/Users/Meen/Documents/Atmel Studio/7.0/display_time_temp/display_time_temp/USART3.c"]
```

C:/Users/Meen/Documents  
/Atmel Studio/7.0/display  
\_time\_temp/display\_time\_temp/main.c

C:/Users/Meen/Documents  
/Atmel Studio/7.0/display  
\_time\_temp/display\_time\_temp  
/USART3.c