

# **ENG306**

## **Power Electronics**

### **Lab 2**

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# 1 Introduction

NOTE: Mention We had issues with saving data from the scope

## 2 Diode Rectifiers

### 2.1 Half-Wave Rectifier with Resistive Load

*Plot(using data saved from the oscilloscope) or insert screenshots or sketch by hand, waveforms for  $v_s$ ,  $v_o$ ,  $i_o$  and  $v_D$  using the same time-scale x axis (so that they can be easily compared – including diode voltage), and discuss your observations briefly.*

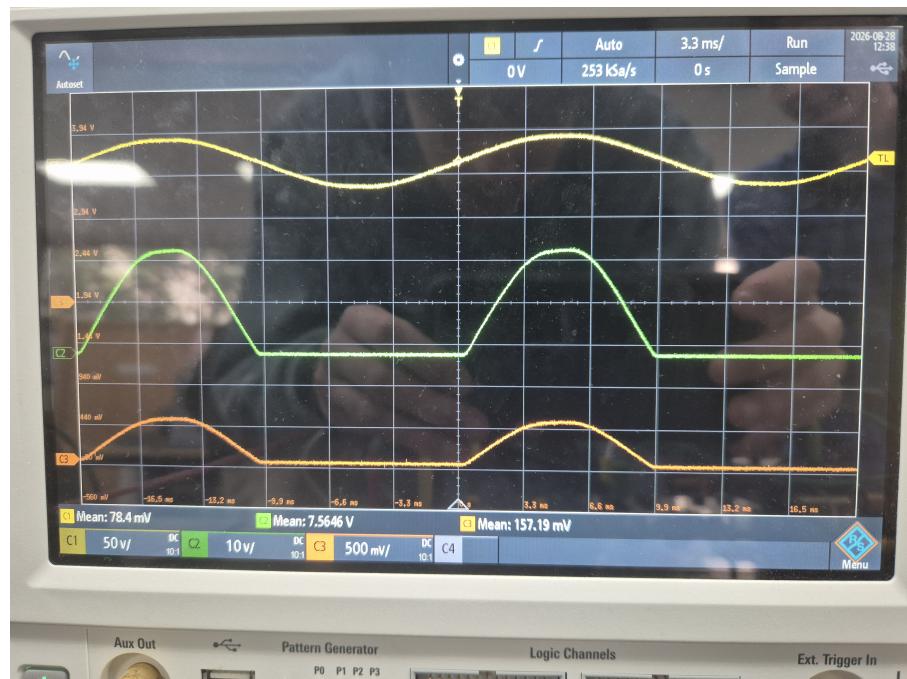


Figure 1:  $V_s$  (yellow),  $V_o$  (green) and  $I_o$  (orange) for a half-wave rectifier with resistive load

As expected this is a half wave rectifier, as seen in Figure 1. When the input voltage is positive the output voltage is positive, when the input voltage is negative the output voltage is zero. This is because the diode is blocking the negative voltages. This is also true for the current wave form.

*From your waveform measurements using oscilloscope, compare the dc output current and voltage against digital multimeter measurements and also against values determined by applying theoretical relationships for this rectifier. Present the different measurements and calculated values in table form, commenting on your observations.*

The DC voltage can be calculated as seen in the following

$$\begin{aligned} V_{DC} &= \frac{V_m}{\pi} \\ V_{DC} &= \frac{V_{rms} \cdot \sqrt{2}}{\pi} \\ V_{DC} &= \frac{20 \cdot \sqrt{2}}{\pi} \\ V_{DC} &= 9.0V \end{aligned}$$

The DC current then can simply be calculated using Ohms Law:

$$I_{DC} = \frac{V_{DC}}{R} = \frac{9}{48} = 0.188A$$

Table 1: Theory and measured DC voltage and current for half-wave rectifier

	Measured	Theory
DC Voltage (V)	9.6	9.0
DC Current (mA)	157	188

## 2.2 Full-Wave Rectifier with Resistive and Inductive Load

*Why was it necessary to measure the rectifier supply voltage waveform using the oscilloscope probes on the second of the two secondary windings or using two probes and the maths function? Describe what you think would happen if probes were still placed across the first winding (i.e. between points 1 and 2 on the circuit).*

To isolate the ground, the oscilloscope probes have common grounds. If the probes were still connected across terminals 1 and 2 and another probe was connected to the load, then the grounds shared between the probes would be located at different spots, creating a short between the input and the load. Hence connecting the probe across terminals 3 and 4 allows us to create a new ground reference that is not effected by the rest of the circuit.

*For the resistive load only set up, plot (using data saved from the oscilloscope) or insert screenshots or sketch by hand, waveforms for  $v_s$ ,  $v_o$ ,  $i_o$  using the same time-scale x axis, and discuss your observations.*

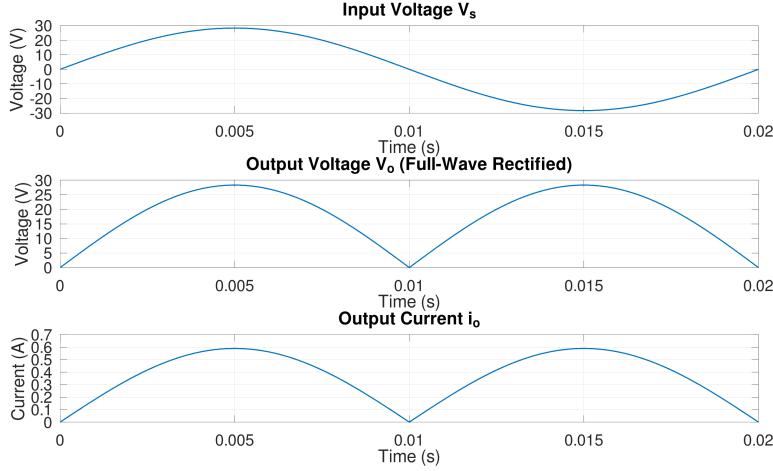


Figure 2:  $V_s$ ,  $V_o$  and  $I_o$  for a full-wave rectifier with resistive load

As expected the plot (Figure 2) shows a full-wave rectified voltage, this was also seen in the lab, however a picture was not obtained. When the voltage is positive the two diodes  $D_1$  and  $D_2$  are forward biased and the other two diodes ( $D_3$  and  $D_4$ ) are reverse biased, and vice versa for negative voltages. This results in a full-wave rectified voltage and current.

*Again for the resistive load only, from your waveform measurements using oscilloscope, compare the DC output current and voltage against digital multimeter measurements and also against values determined by applying theoretical relationships for this rectifier. Present the different measurements and calculated values in table form, commenting on your observations.*

The DC voltage and current can be seen as in the following calculations:

$$\begin{aligned} V_{DC} &= \frac{2V_m}{\pi} \\ V_{DC} &= \frac{2V_{rms} \cdot \sqrt{2}}{\pi} \\ V_{DC} &= \frac{2 \cdot 20 \cdot \sqrt{2}}{\pi} \\ V_{DC} &= 18.0V \end{aligned}$$

Then using Ohm's Law we can calculate the DC current

$$I_{DC} = \frac{V_{DC}}{R} = \frac{18}{48} = 0.375A$$

Table 2: Theory and measured DC voltage and current for full-wave rectifier with purely resistive load

	Measured	Theory
DC Voltage (V)	16.2	18.0
DC Current (mA)	338	375

Looking at Table 2 it can be seen that the theoretical results are greater than the measured results, this is probably due to measurements and setup errors. However, it could also be due

to not considering the losses in the diodes when calculating the voltage and current.

*For the resistive and inductive load, plot (using data saved from the oscilloscope) or insert screenshots or sketch by hand, waveforms for  $v_s$ ,  $v_o$ ,  $i_o$  and  $v_D$  for the two diodes measured, using the same time-scale x axis (so that they can be easily compared – including diode voltage), and discuss your observations, including comparing against what was observed for resistive only load*



Figure 3:  $V_s$ ,  $V_o$  and  $I_o$  for a full-wave rectifier with resistive and inductive load

The voltage across the load is a DC voltage with ripple, this can be seen in Figure 3. This is because the inductor in the load stores energy (from current) and outputs current when the input current is negative. This results in the output voltage rising and reaching a steady state DC voltage with ripple.

The DC ripple for the inductive case is much less than the DC ripple in the purely resistive case. As mentioned this is because the inductor stores energy and releases it when necessary, creating a DC voltage.

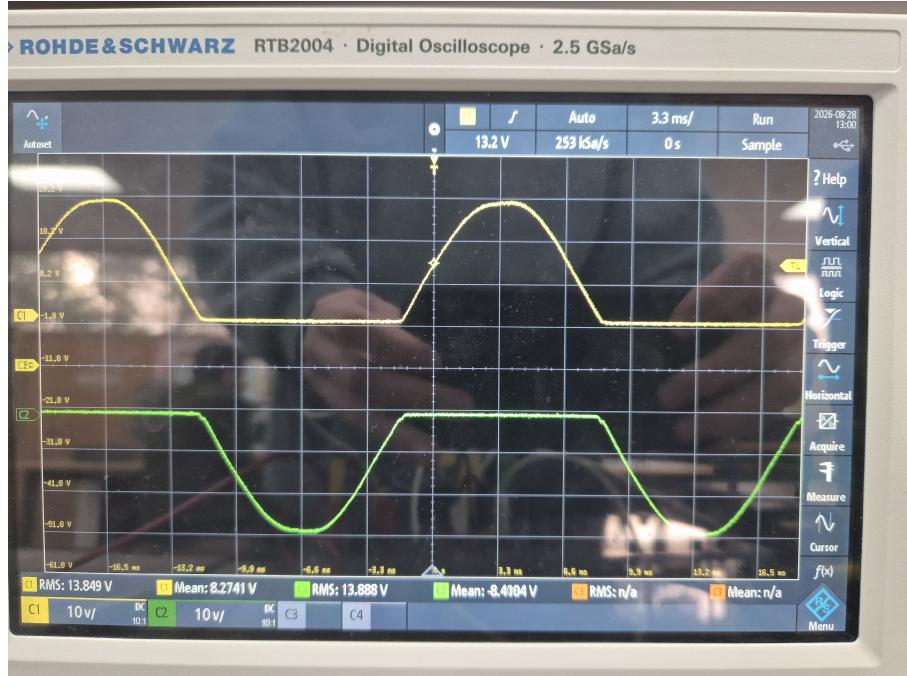


Figure 4: Voltage across diodes  $D_2$  and  $D_3$ . Note:  $D_3$  is the negative voltage

In Figure 4 it can be seen how the rectifier works. When the source voltage is positive the voltage across  $D_3$  is zero and the voltage across  $D_2$  is the source voltage. And vice versa for negative voltages, this results in a full-wave rectified voltage at the output. If the voltage across  $D_1$  and  $D_4$  were recorded we would expect that  $D_1 = D_2$  and  $D_3 = D_4$ .

*Present your measured average (dc) and rms values of output voltage and current (for RL load). How do they compare to the values measured for the resistive load only and why?*

Table 3: Measured DC and rms voltage and DC current for full-wave rectifier with resistive and inductive load

DC Voltage (V)	rms Voltage (V)	DC Current (mA)
11.4	15.1	332

Comparing Table 2 and 3 it can be seen that the DC voltage is lower, and the DC current is the same, when there is an inductor in the load. This is because the inductor stores energy when the input voltage is high and releases it when it is low, reducing the output voltage. As for the equal current, this is because the inductor has no effect on the average current, as seen by the differential equation  $v = L \frac{di}{dt}$ , the voltage is dependent on the change in current.

*By considering losses in the four diodes, estimate your overall rectifier circuit efficiency. Note: there are a few approaches you can take here, some which may require you to think about and perform some more measurements in the lab.*

Diodes experience three types of losses, forward power loss  $P_F$ , reverse power loss  $P_R$  and reverse recovery loss  $P_{RR}$  (ROHM, 2023). The losses are equal for each diode, so we only need to calculate the loss for one cycle and then multiply the result by four.

First let's calculate the forward losses, using data from (Incorporated, N/A) and Table 3.

$$\begin{aligned}
P_F &= V_F \cdot I_F \\
P_F &= V_F \cdot \frac{I_{DC}}{2} \\
P_F &= 0.95 \cdot \frac{332 \cdot 10^{-3}}{2} \\
P_F &= 0.1577 \text{ W}
\end{aligned}$$

Next lets calculate the reverse losses, using data from Table 3.

$$\begin{aligned}
P_R &= V_R \cdot I_R \\
P_R &= \frac{V_D}{2} \cdot \frac{I_{DC}}{2} \\
P_R &= \frac{13.9}{2} \cdot \frac{332 \cdot 10^{-3}}{2} \\
P_R &= 1.1537 \text{ W}
\end{aligned}$$

Lastly the reverse recovery loss, which would be calculated using  $P_{RR} = \frac{1}{6}V_R I_{RM} t_{rrf}$ , however we did not find  $I_{RM}$ . Although  $P_{RR}$  would be minimal because  $t_{rr} \approx 1 \mu s$  and  $I_{RM}$  would be measured in micro amps. Hence  $P_{RR}$  would have very little effect on the overall power loss of the rectifier.

Now we can calculate the efficiency of the rectifier, using data from the previous calculations and from Table 3.

$$\begin{aligned}
P_L &= 4 \cdot P_F + 4 \cdot P_R \\
P_L &= 4 \cdot 0.16 + 4 \cdot 1.15 \\
P_L &= 5.25 \text{ W}
\end{aligned}$$

$$\begin{aligned}
P_T &= P_L + P_{load} \\
P_T &= 5.25 + \frac{11.4^2}{48} \\
P_T &= 8.0 \text{ W}
\end{aligned}$$

$$\begin{aligned}
\eta &= \frac{5.25}{8.0} \\
\eta &= 65\%
\end{aligned}$$

### 2.3 Full-Wave Rectifier with Capacitive Output Filter and Resistive Load

*Tabulate, to allow for easy comparison, your measured dc output voltage and peak-to-peak ripple for each of the three capacitors values*

Capacitor Size	DC Output Voltage $V_{dc}$	Peak-to-Peak Ripple $V_{pp}$
470F	2.1V	7V
1000F	1.2V	4.4V
2000F	0.5V	2.35V

Carefully plot (using data saved from the oscilloscope) or sketch by hand on one graph the output voltage waveform observed for each of the three capacitor values. Comment on how and why the waveforms and measurements differ with changing capacitor value.

These plots show how the capacitive output filter effects the output voltage and current waveforms. As the capacitor size increases the smoothing effect also increases. This is because the time for the capacitor to charge and discharge also changes. As the AC input increases the capacitor charges, then as the input is negative the capacitor discharges. The time constant  $\tau = R_L C$  effects the charging time of the capacitor, appearing in the plots as more ripple in the output voltage with the smaller capacitor values.

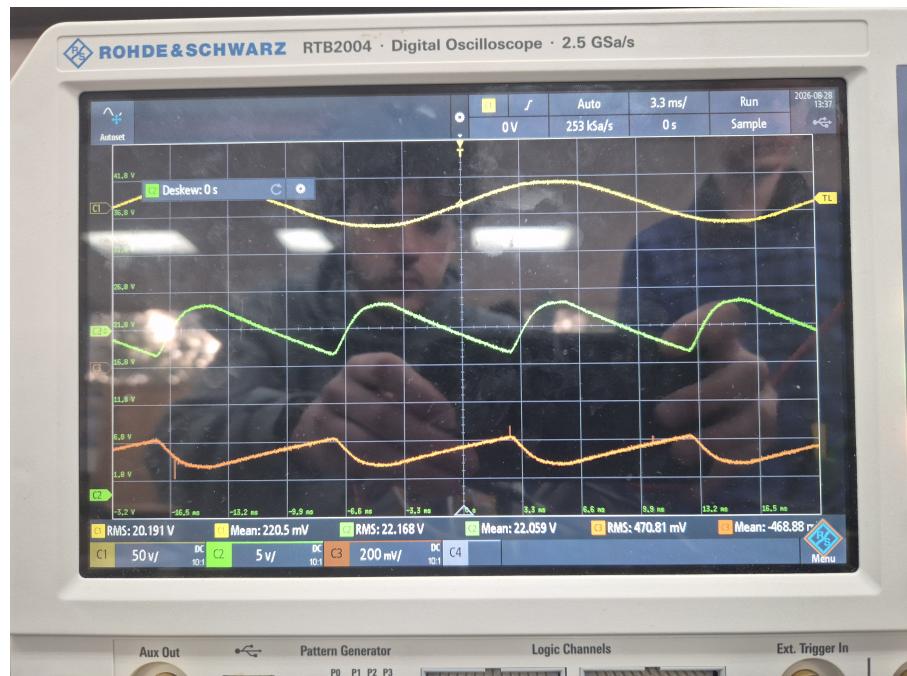


Figure 5:  $470 \mu F$  Capacitor Plot



Figure 6:  $1000 \mu F$  Capacitor Plot



Figure 7:  $2000 \mu F$  Capacitor Plot

*Develop an approximate expression for percentage peak-to-peak ripple as a function of  $R$ ,  $C$  and frequency  $f$ , stating carefully any assumptions. How do your calculated values, using your derived expression, compare to your measurements?*

Using the capacitor discharge over one ripple period the percentage peak-to-peak ripple can be calculated.

$$\Delta \approx \frac{V_{DC}}{R_L C} \cdot \frac{1}{2f}$$

$$r_{pp}(\%) \approx \frac{100}{2fR_L C}$$

Comparing this to the measured results with the  $1000 \mu F$  capacitor.

$$r_{pp,calc}(\%) \approx \frac{100}{2 * 50 * 48 * 1000 * 10^{-6}} = 20.83\%$$

$$r_{pp,measured}(\%) \approx 18.2\%$$

This expression is a good approximation to the peak-to-peak ripple measured on the plot.

*How would you expect the value of the capacitor to impact upon supply current waveform, in particular on the harmonic content? Describe why you expect this? (Note: if you have time you may wish to think of a way to measure and display the Fourier components of supply current for your circuit, thus gaining extra insight)*

Increasing the capacitance makes the rectifier's output ripple smaller, but it also makes the supply current more impulsive. As the capacitor size increases the Total Harmonic Distortion also increase. This leads to the distortion power factor deteriorating, also worsening the overall power factor. This also means there are more harmonics. The harmonic currents only affect the RMS current without increasing the real power so the power factor decreases.

### 3 Thyristor Controller Rectifiers

#### 3.1 Semi-Converter with Resistive and Inductive Load

Firing angle	Digital multimeter	Calculated	
$\alpha$	$V_o(V)$	$I_o(mA)$	$V_o(V)$
0	0	0	0
20	0.8	0.8	0.384
45	3.4	20	1.865
60	5.6	63	3.183
90	9.7	159	6.366
100	10.2	184	7.471
120	10.9	247	9.549
140	10.6	285	11.243
160	9.5	317	12.348
180	8.8	333	12.732

To calculate  $V_o$  use the formula,

$$V_{o,theory} = K(1 - \cos(\alpha))$$

where,

$$K = \frac{V_m}{\pi} = 6.366V$$

These results can be seen in the table above.

*Detail the way you connected your oscilloscope probes and configured the oscilloscope to record simultaneously the source voltage, trigger signal, output voltage and current waveforms.*

The common ground is at  $+V_o$  to allow for common measurements. Channel 1 measures the source voltage using the secondary transformer winding. This is acceptable as it is electrically isolated from the remainder of the circuit so multiple grounds cannot cause a short circuit. Channel 2 measure the output voltage, with its positive probe placed at  $-V_o$ . Channel 3 measures  $I_o$ , with its probe placed between the  $1\Omega$  and  $47\Omega$  resistors. Measuring the voltage over the  $1\Omega$  resistor is equivalent to measuring current as  $V = IR$ . Channel 4 measures the positive trigger input of  $T_1$ .

*Include plot from oscilloscope of all waveforms for a firing angle of  $45^\circ$ .*

Channel 2 and Channel 3 show the negative output voltage and output current respectively as their ground and positive connections are inverted due to needing a common ground point for all measurements.

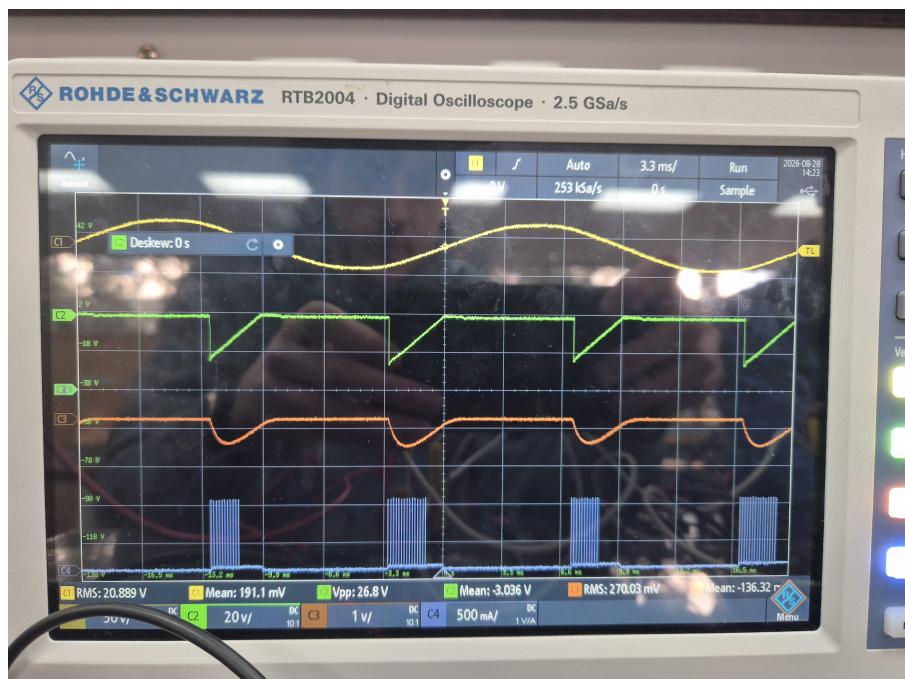


Figure 8:  $45^\circ$  firing angle

*Comment on the observed waveforms and on how they changed with firing angle, describing your observations by considering theory of the circuit operation.*

As the firing angle is increased to  $180^\circ$  the output voltage and current approach the full wave rectified input waveform shown by Channel 1. This is because at a firing angle of  $180^\circ$  the thyristor acts the same as a typical diode used in a full wave rectifier.

*From your measured data, create a plot of DC output voltage vs firing angle. Also include in the plot a curve based on theoretical considerations for this circuit. Discuss your findings.*

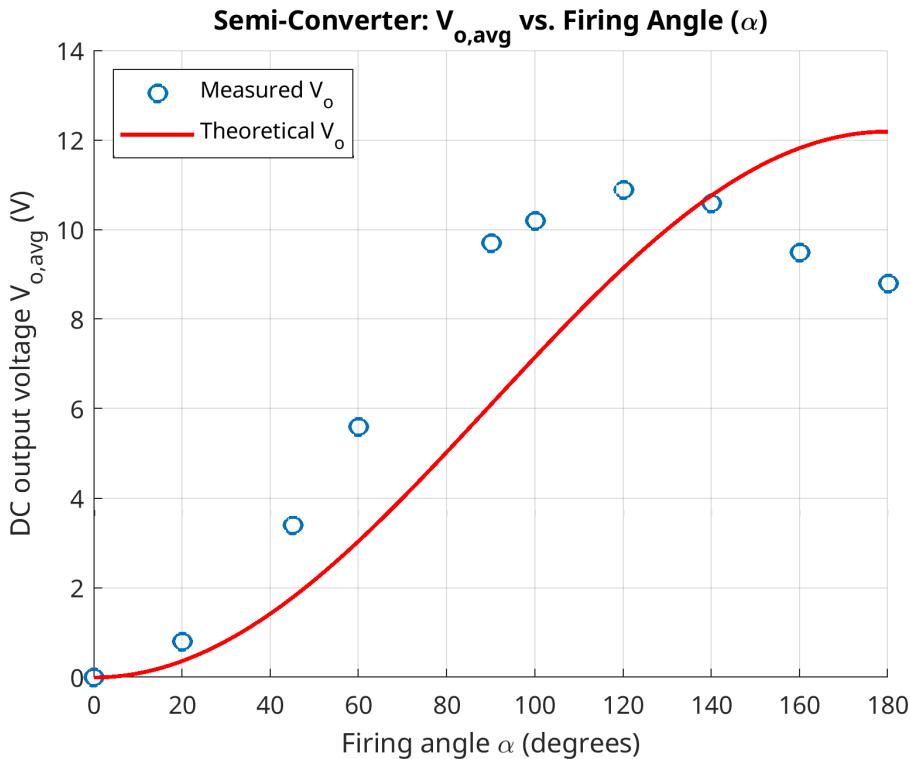


Figure 9: Output Voltage vs Firing Angle

The theoretical curve  $V_{dc} = K(1 - \cos \alpha)$  matches the measured results with an acceptable error. At a small firing angle the deviations are caused by discontinuous current. The error at higher firing angles may be attributed to residual conduction in the load. There may also be isomer error caused by the non-idealities in the lab configurations.

*Based on your observation, at what firing angle did you observe there to first be a boundary between continuous and discontinuous conduction (of current in the load). Compare to what you might expect from a theoretical view point?*

The data suggests that the rectifier is in discontinuous conduction mode until approximately 50° as the average output current is very low suggesting the current drops to zero for parts of the cycle. Above 60° the output current increases significantly as  $\alpha$  increases confirming the current is in continuous conduction mode.

Theoretically current should be continuous as long as long as the firing time is long enough for the inductor to store energy to prevent zero current between firing pulses. This means that discontinuous current is expected for low values of  $\alpha$  as seen through the measurements.

*Describe the purpose of the freewheeling diode in this circuit and what you think might happen if it were removed.*

The freewheeling diode provided a safe path for current to flow when the AC input voltage reverse polarity or if the thyristors are turned off. The inductor inherently opposes sudden current change so a path is required to allow for a gradual decay. This prevents damage to components due to large voltage spikes across the thyristors. It also improves the rectifiers performance by reducing output voltage ripple and smoothing the load current.

If the freewheeling diode were to be removed the inductor current would have no alternate discharge paths, leading to discontinuous conduction, higher output ripple and potentially cause damage to the thyristors due to the inductor's stored energy.

## 4 Reflection

TODO:

## References

- Incorporated, D. (N/A). *6a05 - 6a10 6.0a silicon rectifier* [Revision: DS28009 Rev. F-2, Accessed: 2025-09-10]. <https://www.alldatasheet.com/datasheet-pdf/pdf/58864/DIODES/6A10.html>
- ROHM. (2023). *Power loss and thermal design of diodes* [Revision: R2043A, Accessed: 2025-09-10]. [https://fscdn.rohm.com/en/products/databook/applinote/discrete/diodes/power\\_loss\\_and\\_thermal\\_design\\_of\\_diodes\\_an-e.pdf](https://fscdn.rohm.com/en/products/databook/applinote/discrete/diodes/power_loss_and_thermal_design_of_diodes_an-e.pdf)

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*End of Report*