

ENG306

Power Electronics

Lab 2

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1 Introduction

NOTE: Mention We had issues with saving data from the scope

2 Diode Rectifiers

2.1 Half-Wave Rectifier with Resistive Load

Plot (using data saved from the oscilloscope) or insert screenshots or sketch by hand, waveforms for v_s , v_o , i_o and v_D using the same time-scale x axis (so that they can be easily compared – including diode voltage), and discuss your observations briefly.

From your waveform measurements using oscilloscope, compare the dc output current and voltage against digital multimeter measurements and also against values determined by applying theoretical relationships for this rectifier. Present the different measurements and calculated values in table form, commenting on your observations.

2.2 Full-Wave Rectifier with Resistive and Inductive Load

Why was it necessary to measure the rectifier supply voltage waveform using the oscilloscope probes on the second of the two secondary windings or using two probes and the maths function? Describe what you think would happen if probes were still placed across the first winding (i.e. between points 1 and 2 on the circuit).

For the resistive load only set up, plot (using data saved from the oscilloscope) or insert screenshots or sketch by hand, waveforms for v_s , v_o , i_o using the same time-scale x axis, and discuss your observations.

Again for the resistive load only, from your waveform measurements using oscilloscope, compare the dc output current and voltage against digital multimeter measurements and also against values determined by applying theoretical relationships for this rectifier. Present the different measurements and calculated values in table form, commenting on your observations.

For the resistive and inductive load, plot (using data saved from the oscilloscope) or insert screenshots or sketch by hand, waveforms for v_s , v_o , i_o and v_D for the two diodes measured, using the same time-scale x axis (so that they can be easily compared – including diode voltage), and discuss your observations, including comparing against what was observed for resistive only load

Present your measured average (dc) and rms values of output voltage and current (for RL load). How do they compare to the values measured for the resistive load only and why?

By considering losses in the four diodes, estimate your overall rectifier circuit efficiency. Note: there are a few approaches you can take here, some which may require you to think about and perform some more measurements in the lab.

2.3 Full-Wave Rectifier with Capacitive Output Filter and Resistive Load

Tabulate, to allow for easy comparison, your measured dc output voltage and peak-to-peak ripple for each of the three capacitors values

Capacitor Size	DC Output Voltage V_{dc}	Peak-to-Peak Ripple V_{pp}
470F	2.1V	7V
1000F	1.2V	4.4V
2000F	0.5V	2.35V

Carefully plot (using data saved from the oscilloscope) or sketch by hand on one graph the output voltage waveform observed for each of the three capacitor values. Comment on how and why the waveforms and measurements differ with changing capacitor value.

These plots show how the capacitive output filter effects the output voltage and current waveforms. As the capacitor size increases the smoothing effect also increases. This is because the time for the capacitor to charge and discharge also changes. As the AC input increases the capacitor charges, then as the input is negative the capacitor discharges. The time constant $\tau = R_L C$ effects the charging time of the capacitor, appearing in the plots as more ripple in the output voltage with the smaller capacitor values.



Figure 1: $470 \mu F$ Capacitor Plot



Figure 2: $1000 \mu\text{F}$ Capacitor Plot



Figure 3: $2000 \mu\text{F}$ Capacitor Plot

Develop an approximate expression for percentage peak-to-peak ripple as a function of R , C and frequency f , stating carefully any assumptions. How do your calculated values, using your derived expression, compare to your measurements?

Using the capacitor discharge over one ripple period the percentage peak-to-peak ripple can be calculated.

$$\Delta \approx \frac{V_{DC}}{R_L C} \cdot \frac{1}{2f}$$

$$r_{pp}(\%) \approx \frac{100}{2fR_L C}$$

Comparing this to the measured results with the $1000 \mu F$ capacitor.

$$r_{pp,calc}(\%) \approx \frac{100}{2 * 50 * 48 * 1000 * 10^{-6}} = 20.83\%$$

$$r_{pp,measured}(\%) \approx 18.2\%$$

This expression is a good approximation to the peak-to-peak ripple measured on the plot.

How would you expect the value of the capacitor to impact upon supply current waveform, in particular on the harmonic content? Describe why you expect this? (Note: if you have time you may wish to think of a way to measure and display the Fourier components of supply current for your circuit, thus gaining extra insight)

Increasing the capacitance makes the rectifier's output ripple smaller, but it also makes the supply current more impulsive. As the capacitor size increases the Total Harmonic Distortion also increase. This leads to the distortion power factor deteriorating, also worsening the overall power factor. This also means there are more harmonics. The harmonic currents only affect the RMS current without increasing the real power so the power factor decreases.

3 Thyristor Controller Rectifiers

3.1 Semi-Converter with Resistive and Inductive Load

Firing angle	Digital multimeter	Calculated	
α	$V_o(V)$	$I_o(mA)$	$V_o(V)$
0	0	0	0
20	0.8	0.8	0.384
45	3.4	20	1.865
60	5.6	63	3.183
90	9.7	159	6.366
100	10.2	184	7.471
120	10.9	247	9.549
140	10.6	285	11.243
160	9.5	317	12.348
180	8.8	333	12.732

To calculate V_o use the formula,

$$V_{o,theory} = K(1 - \cos(\alpha))$$

where,

$$K = \frac{V_m}{\pi} = 6.366V$$

These results can be seen in the table above.

Detail the way you connected your oscilloscope probes and configured the oscilloscope to record simultaneously the source voltage, trigger signal, output voltage and current waveforms.

The common ground is at $+V_o$ to allow for common measurements. Channel 1 measures the source voltage using the secondary transformer winding. This is acceptable as it is electrically isolated from the remainder of the circuit so multiple grounds cannot cause a short circuit. Channel 2 measure the output voltage, with its positive probe placed at $-V_o$. Channel 3 measures I_o , with its probe placed between the 1Ω and 47Ω resistors. Measuring the voltage over the 1Ω resistor is equivalent to measuring current as $V = IR$. Channel 4 measures the positive trigger input of T_1 .

Include plot from oscilloscope of all waveforms for a firing angle of 45° .

Channel 2 and Channel 3 show the negative output voltage and output current respectively as their ground and positive connections are inverted due to needing a common ground point for all measurements.

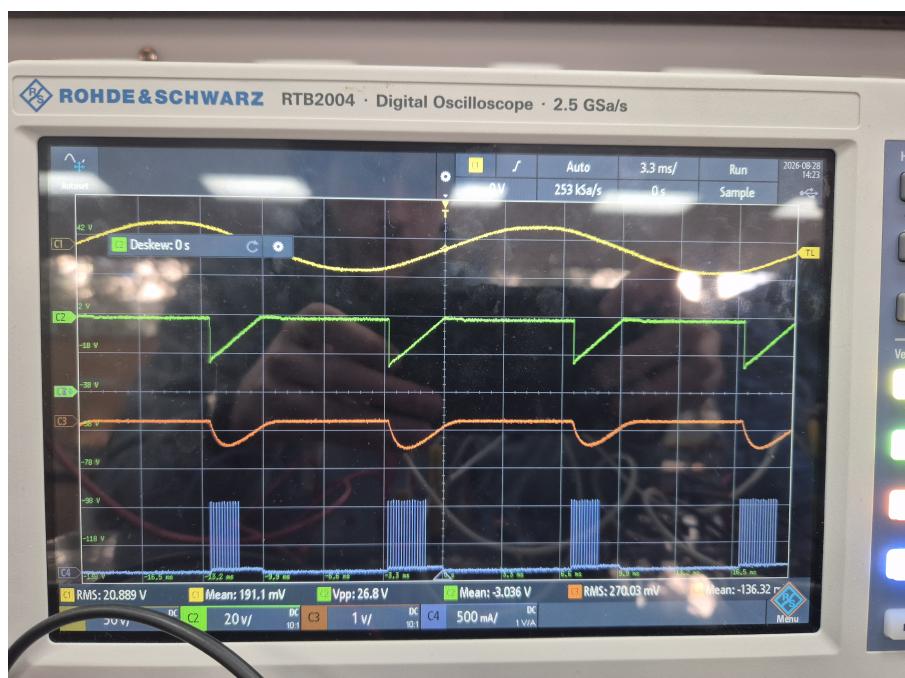


Figure 4: 45° firing angle

Comment on the observed waveforms and on how they changed with firing angle, describing your observations by considering theory of the circuit operation.

As the firing angle is increased to 180° the output voltage and current approach the full wave rectified input waveform shown by Channel 1. This is because at a firing angle of 180° the thyristor acts the same as a typical diode used in a full wave rectifier.

From your measured data, create a plot of DC output voltage vs firing angle. Also include in the plot a curve based on theoretical considerations for this circuit. Discuss your findings.

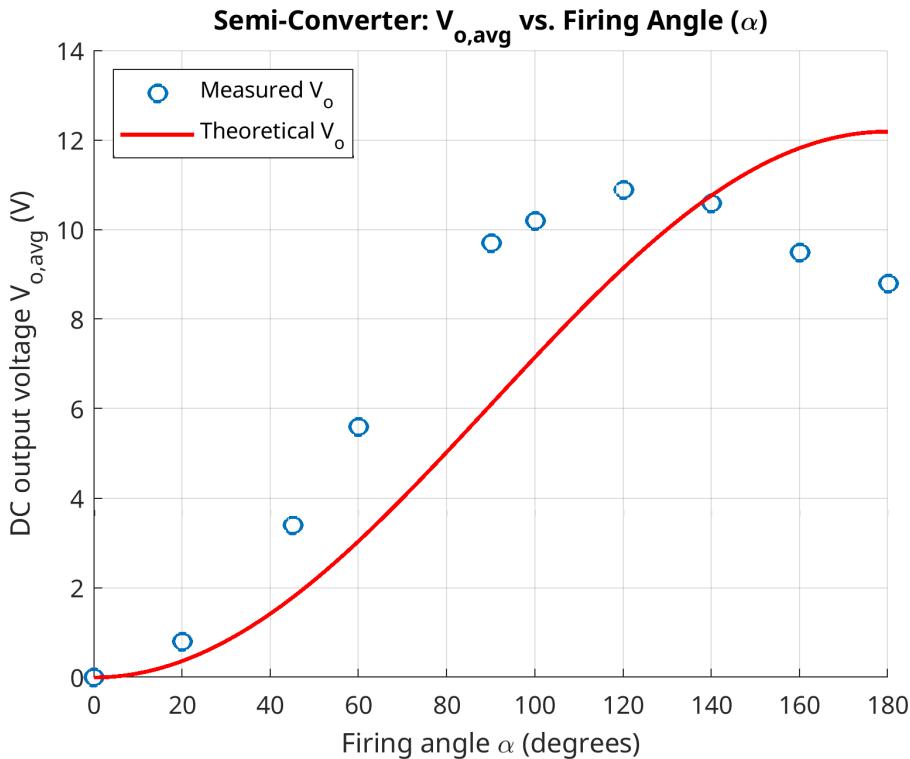


Figure 5: Output Voltage vs Firing Angle

The theoretical curve $V_{dc} = K(1 - \cos \alpha)$ matches the measured results with an acceptable error. At a small firing angle the deviations are caused by discontinuous current. The error at higher firing angles may be attributed to residual conduction in the load. There may also be isomer error caused by the non-idealities in the lab configurations.

Based on your observation, at what firing angle did you observe there to first be a boundary between continuous and discontinuous conduction (of current in the load). Compare to what you might expect from a theoretical view point?

The data suggests that the rectifier is in discontinuous conduction mode until approximately 50° as the average output current is very low suggesting the current drops to zero for parts of the cycle. Above 60° the output current increases significantly as α increases confirming the current is in continuous conduction mode.

Theoretically current should be continuous as long as long as the firing time is long enough for the inductor to store energy to prevent zero current between firing pulses. This means that discontinuous current is expected for low values of α as seen through the measurements.

Describe the purpose of the freewheeling diode in this circuit and what you think might happen if it were removed.

The freewheeling diode provided a safe path for current to flow when the AC input voltage reverse polarity or if the thyristors are turned off. The inductor inherently opposes sudden current change so a path is required to allow for a gradual decay. This prevents damage to components due to large voltage spikes across the thyristors. It also improves the rectifiers performance by reducing output voltage ripple and smoothing the load current.

If the freewheeling diode were to be removed the inductor current would have no alternate discharge paths, leading to discontinuous conduction, higher output ripple and potentially cause damage to the thyristors due to the inductor's stored energy.

4 Reflection

TODO:

End of Report