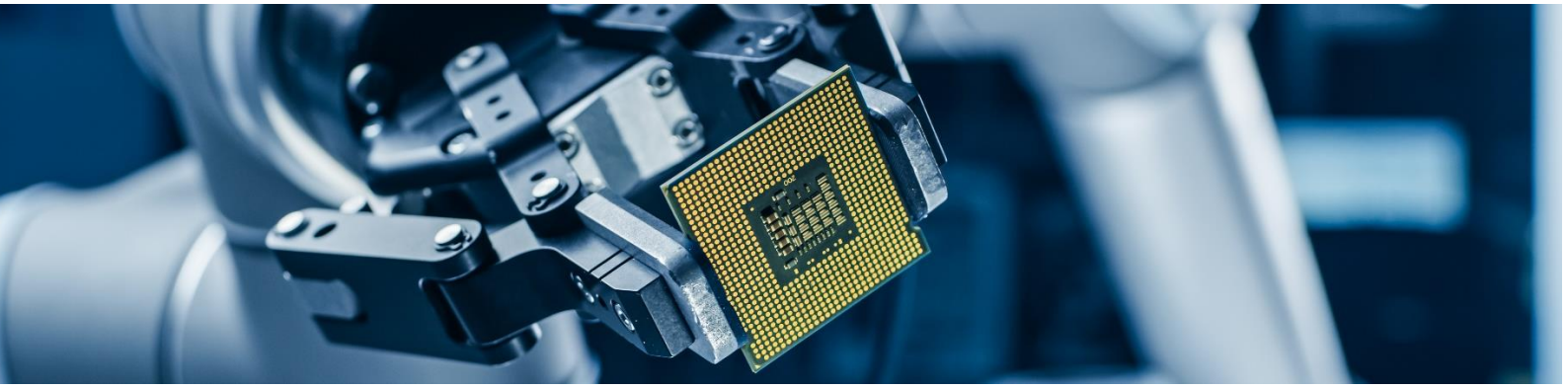
Event Invitation



# Intel FPGA as µC Companion – part 1

**23rd June 2021 – online**

In times of fast evolution, staying at the cutting edge of technology is only achievable by continuous learning. With this seminar, Arrow provides you the chance for a job specific training.

The ability to Interface with a diverse range of sensors and actuators is critical for embedded systems. Project risk increases significantly if available interfaces limit system choices or – even worse – a single interface drives the microcontroller selection.

This webinar will demonstrate how unique, legacy, and standard interfaces can be implemented using small and low cost, low power Intel FPGA in combination with your preferred microcontroller using a I2C or SPI bus. This approach enables system developers to use their preferred microcontroller while still being able to expand the interface as desired Thus supporting tailor made and specific or standardized legacy interfaces, which are not natively supported by the MCU of choice.

Implementation of this companion design does not require any FPGA design skills.
The following two boards get used for the demonstration: MAX1000 + STM32 Discovery Kit

Part Two of the webinar, scheduled for September 2021, will focus on the use of high throughput protocols for higher bandwidth applications.

**Speakers:** Adam Taylor
**Language:** English
**Prerequisites:** None
**Seminar Actions:** Presentation
**Contact Person:** Gerhard Nedok, gerhard.nedok@arroweurope.com, +43 664 - 2 45 56 01

**Agenda** (Time zone: CEST)

| | |
|---|---|
| 10:00 – 10:10 | Introduction to the presenter and the topic |
| 10:10 – 10:45 | Presentation and Demonstration of the Reference Designs |
| 10.45– 11:00 | Questions & Answers |

**Register**