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Automatisch generierte Beschreibung

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| **MAX 10 Webinar Labs Summary** | |
| Lab1 - First MAX1000 Design:  This tutorial provides comprehensive information to help you understand how to create a simple Intel FPGA Design and run it on your MAX1000 board. This lab will not make you an expert, but at the end, you will understand basic concepts about Quartus Prime projects, such as entering a design using a schematic editor, compiling your design, and downloading it into the FPGA on your development board.  Lab 2 – RTL Simulation  This tutorial provides comprehensive information to help you understand how to simulate your FPGA design in the ModelSim – Intel FPGA Edition simulator. Design simulation verifies your design before programming.  Lab 3 – Simple NIOS  This tutorial provides comprehensive information to help you understand how to create a simple Nios soft processor system in an Intel FPGA and run it on your MAX1000 board. The Nios II processor is a soft intellectual property (IP) processor that you download (along with other hardware components that comprise the Nios II system) onto an Intel FPGA. At the end, you will understand basic concepts about Quartus Platform Designer (formerly Qsys), Software Build Tool and the basic development flow for the Nios II design flow.  Lab 4 – Embedded System  This tutorial provides comprehensive information to help you understand how to create a software project for a Nios II processor system in an Intel FPGA and run the software project on your MAX1000 board. The Nios II processor core is a soft intellectual property (IP) processor that you download (along with other hardware components that comprise the Nios II system) onto an Intel FPGA. This tutorial introduces you to the basic software development flow for the Nios II processor. |  |
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