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Перв. примен.

Справ. №

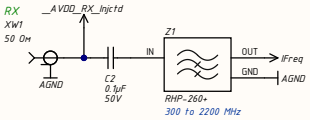
Подп. и дата

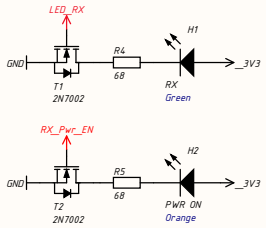
Инв. № дубл.

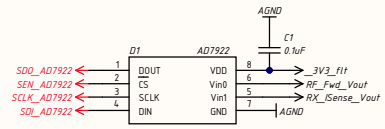
Взам. инв. №

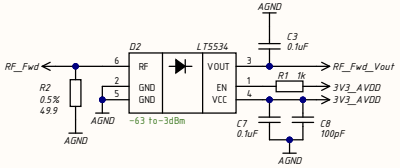
Подп. и дата

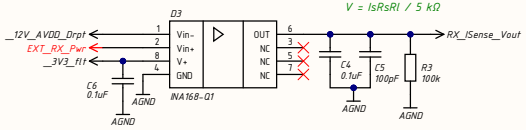
Инв. № подл.

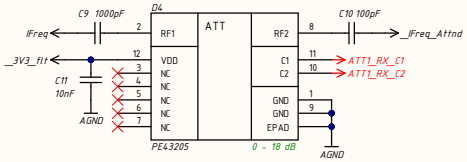


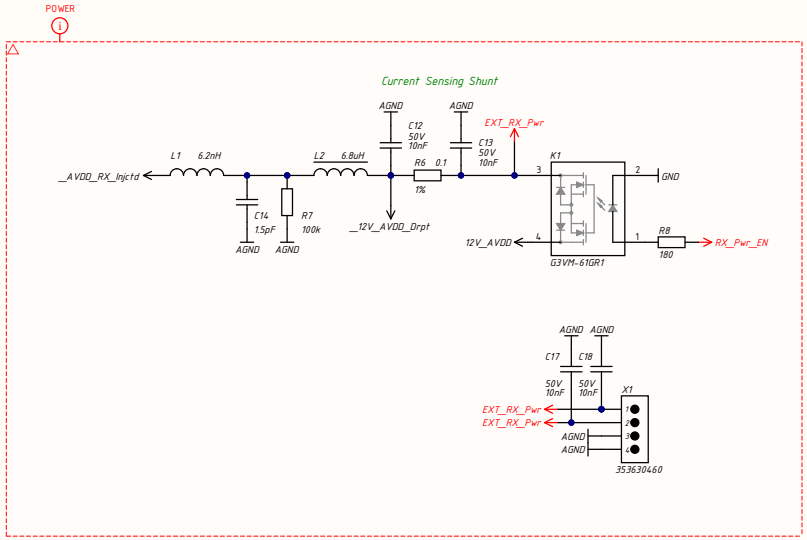


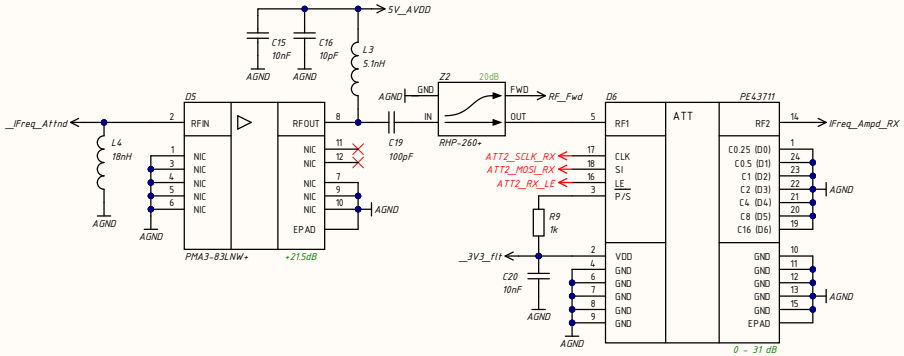












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Изм.	Лист	№ докум.	Подп.	Дата		
Разраб.		{Разраб.}			{Наименование} {Документ}	Лит. {} {} {} / Лист {} / Листов {}
Пров.		{Проверил}				
Т.контр.		{Т.контр.}				
Н.контр.		{Н.контр.}				
Утв.		{Утвердил}				

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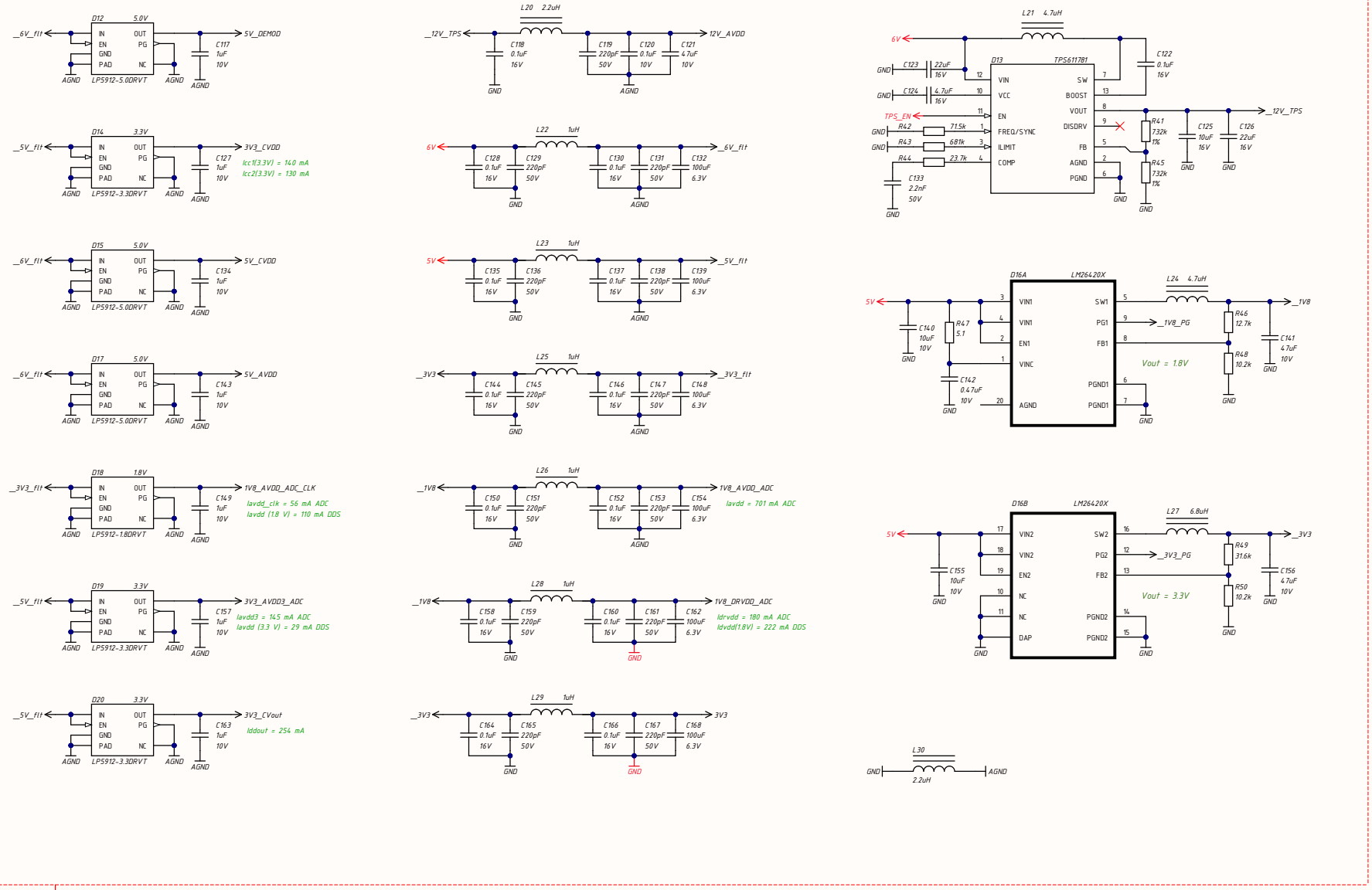
Формат А3



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Формат А3

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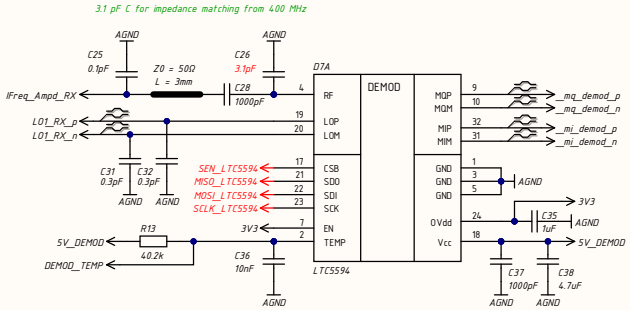
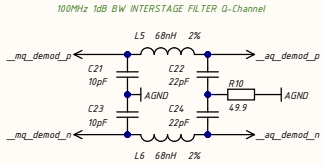
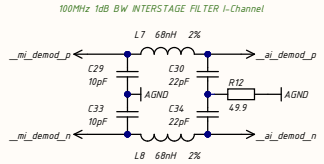
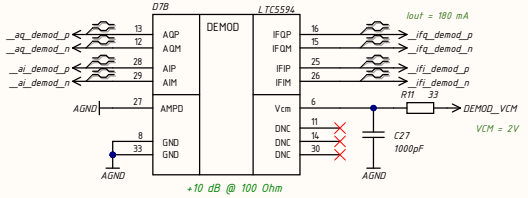
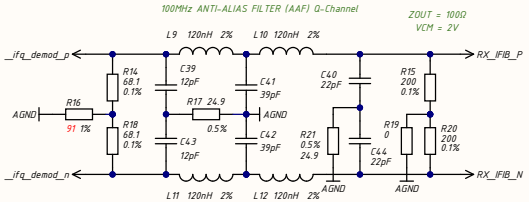
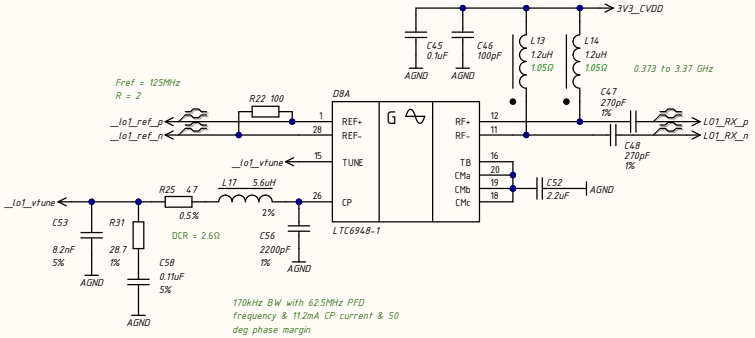


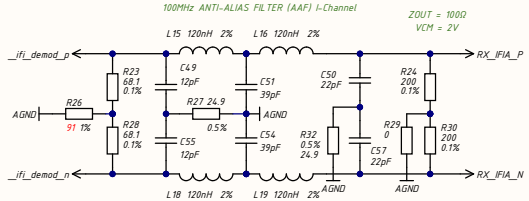


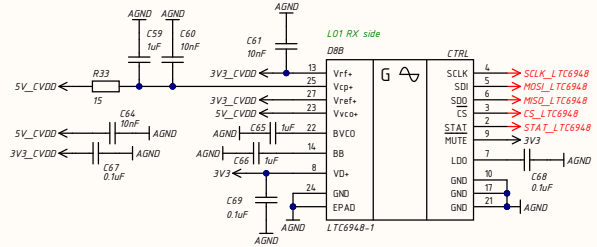




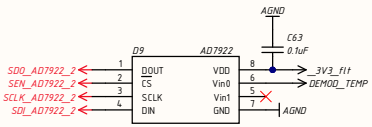


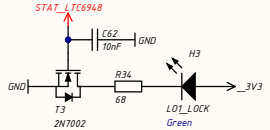






LTC6948-1 Fequency Table:
2.240 to 3.740 GHz
1.120 to 1.870 GHz
0.747 to 1.247 GHz
0.560 to 0.935 GHz
0.448 to 0.748 GHz
0.373 to 0.623 GHz





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Формат А3

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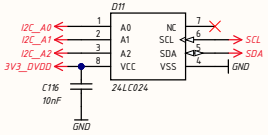
DBoard ID & Parameter's



X2A

Конт.	Цепь	Адрес
A1	ADC_0_p	
A2	ADC_0_n	
A3	GND	
A4	GND	
A5	ADC_6_p	
A6	ADC_6_n	
A7	GND	
A8	GND	
A9	ADC_12_p	
A10	ADC_12_n	
A11	GND	
A12	GND	
A13	dds_sync_p	DDS_sync_p
A14	dds_sync_n	DDS_sync_n
A15	GND	
A16	GND	
A17	GND	
A18	dds_pdclk	DDS_pdclk
A19	GND	
A20	dds_d15	DDS_p15
A21	GND	
A22	dds_d12	DDS_p12
A23	GND	
A24	dds_d09	DDS_p9
A25	GND	
A26	dds_d06	DDS_p6
A27	GND	
A28	dds_d03	DDS_p3
A29	GND	
A30	dds_d00	DDS_p0

SEARAY Plug, 240 Circuits

X2B

Конт.	Цепь	Адрес
B1	GND	
B2	GND	
B3	ADC_1_p	
B4	ADC_1_n	
B5	GND	
B6	GND	
B7	ADC_7_p	
B8	ADC_7_n	
B9	GND	
B10	GND	
B11	ADC_13_p	
B12	ADC_13_n	
B13	GND	
B14	GND	
B15	ref_clk_p	
B16	ref_clk_n	
B17	GND	
B18	GND	
B19	dds_d14	DDS_p14
B20	GND	
B21	dds_d11	DDS_p11
B22	GND	
B23	dds_d08	DDS_p8
B24	GND	
B25	dds_d05	DDS_p5
B26	GND	
B27	dds_d02	DDS_p2
B28	GND	
B29	dds_f0	DDS_f0
B30	GND	

SEARAY Plug, 240 Circuits

X2C

Конт.	Цепь	Адрес
C1	ADC_2_p	
C2	ADC_2_n	
C3	GND	
C4	GND	
C5	ADC_8_p	
C6	ADC_8_n	
C7	GND	
C8	GND	
C9	ADC_CLKOUT_p	
C10	ADC_CLKOUT_n	
C11	GND	
C12	GND	
C13	ref_freq_rx_p	clock_p
C14	ref_freq_rx_n	clock_n
C15	GND	
C16	GND	
C17	SDA	SDA
C18	SCL	SCL
C19	GND	
C20	dds_d13	DDS_p13
C21	GND	
C22	dds_d10	DDS_p10
C23	GND	
C24	dds_d07	DDS_p7
C25	GND	
C26	dds_d04	DDS_p4
C27	GND	
C28	dds_d01	DDS_p1
C29	GND	
C30	dds_f1	DDS_f1

SEARAY Plug, 240 Circuits

X2D

Конт.	Цепь	Адрес
D1	GND	
D2	GND	
D3	ADC_3_p	
D4	ADC_3_n	
D5	GND	
D6	GND	
D7	ADC_9_p	
D8	ADC_9_n	
D9	GND	
D10	GND	
D11	ADC_14_p	
D12	ADC_14_n	
D13	GND	
D14	GND	
D15		
D16	I2C_A2	I2C_A3
D17	I2C_A1	I2C_A2
D18	I2C_A0	I2C_A1
D19	GND	
D20	cpld_io_01	io_01
D21	cpld_io_03	io_03
D22	GND	
D23	cpld_io_05	io_05
D24	cpld_io_07	io_07
D25	GND	
D26	cpld_io_09	io_09
D27	cpld_io_13	io_11
D28	GND	
D29	cpld_io_11	io_13
D30	cpld_io_15	io_15

SEARAY Plug, 240 Circuits

X2E

Конт.	Цепь	Адрес
E1	ADC_4_p	
E2	ADC_4_n	
E3	GND	
E4	GND	
E5	ADC_10_p	
E6	ADC_10_n	
E7	GND	
E8	GND	
E9	ADC_15_p	
E10	ADC_15_n	
E11	GND	
E12	GND	
E13		
E14	SEN	SEN
E15	SCLK	SCLK
E16	SDO	SDO
E17	SDI	SDI
E18	GND	
E19	cpld_io_02	io_02
E20	cpld_io_04	io_04
E21	GND	
E22	cpld_io_06	io_06
E23	cpld_io_08	io_08
E24	GND	
E25	cpld_io_10	io_10
E26	cpld_io_12	io_12
E27	GND	
E28	cpld_io_14	io_14
E29	cpld_io_16	io_16
E30	GND	

SEARAY Plug, 240 Circuits

X2F

Конт.	Цепь	Адрес
F1	GND	
F2	GND	
F3	ADC_5_p	
F4	ADC_5_n	
F5	GND	
F6	GND	
F7	ADC_11_p	
F8	ADC_11_n	
F9	GND	
F10	GND	
F11	ADC_OR_p	
F12	ADC_OR_n	
F13	GND	
F14	GND	
F15	3V3_DVDD	DVDD
F16	3V3_DVDD	DVDD
F17	3V3_DVDD	DVDD
F18	GND	
F19	3V3_DVDD	DVDD
F20	3V3_DVDD	DVDD
F21	3V3_DVDD	DVDD
F22	GND	
F23	GND	
F24	GND	
F25	GND	
F26	GND	
F27	GND	
F28	GND	
F29	GND	
F30	GND	

SEARAY Plug, 240 Circuits

X2G

Конт.	Цепь	Адрес
G1		
G2		
G3	GND	
G4	GND	
G5		
G6		
G7	GND	
G8	GND	
G9		
G10		
G11	GND	
G12	GND	
G13	SDA	
G14	SCL	
G15	GND	
G16	TxFault	
G17	ModAbs	
G18	RS0	
G19	RxLOS	
G20	RS1	
G21	TxDisable	
G22	GND	
G23	GND	
G24	RX_p	
G25	RX_n	
G26	GND	
G27	GND	
G28	TX_n	
G29	TX_p	
G30	GND	

SEARAY Plug, 240 Circuits

X2H

Конт.	Цепь	Адрес
H1	GND	
H2	GND	
H3	5V	5V
H4	5V	5V
H5	5V	5V
H6	5V	5V
H7	GND	
H8	5V	5V
H9	5V	5V
H10	5V	5V
H11	GND	
H12	GND	
H13	GND	
H14	GND	
H15	6V	6V
H16	6V	6V
H17	6V	6V
H18	GND	
H19	6V	6V
H20	6V	6V
H21	6V	6V
H22	GND	
H23	GND	
H24	VccRX	VccRX
H25	VccRX	VccRX
H26	GND	
H27	GND	
H28	VccTX	VccTX
H29	VccTX	VccTX
H30	GND	

SEARAY Plug, 240 Circuits

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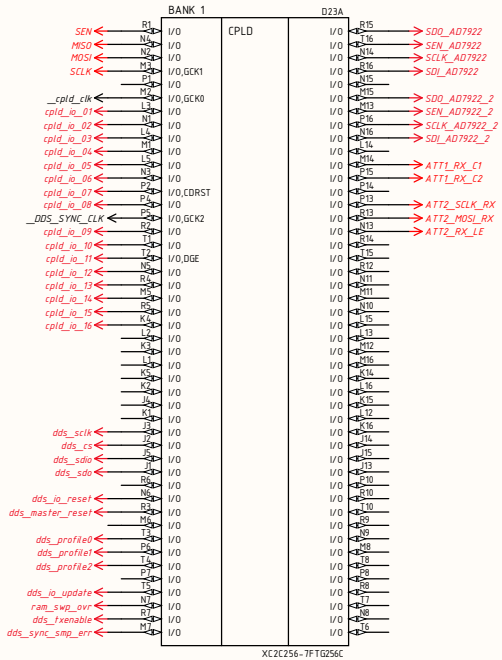

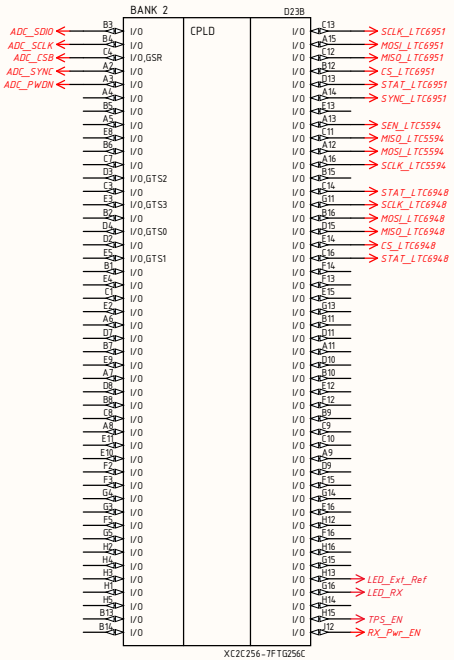

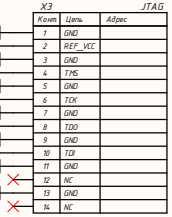

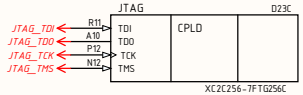

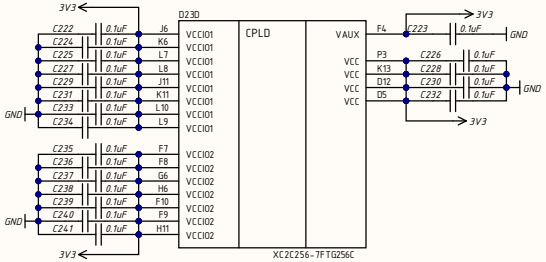

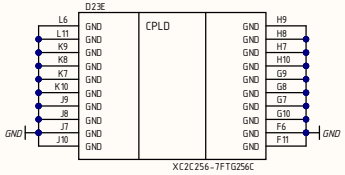

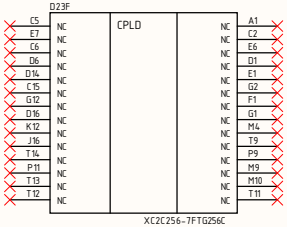

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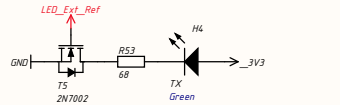
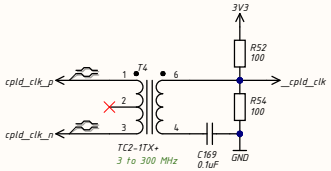
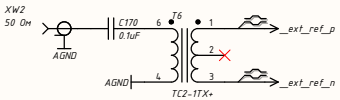
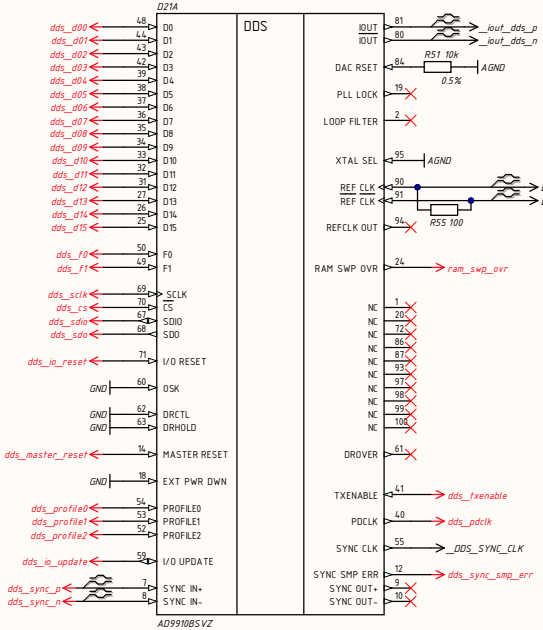
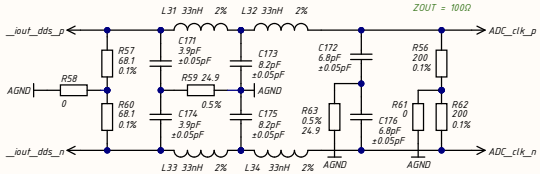


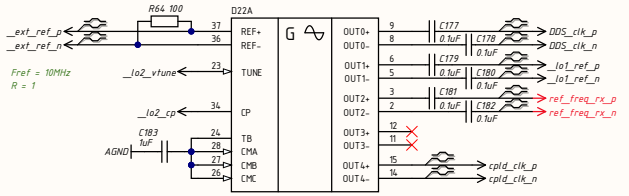




External reference for RX side 10 MHZ



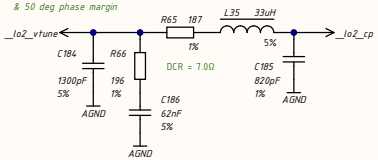


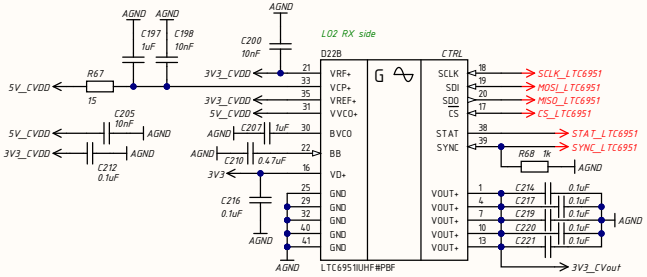


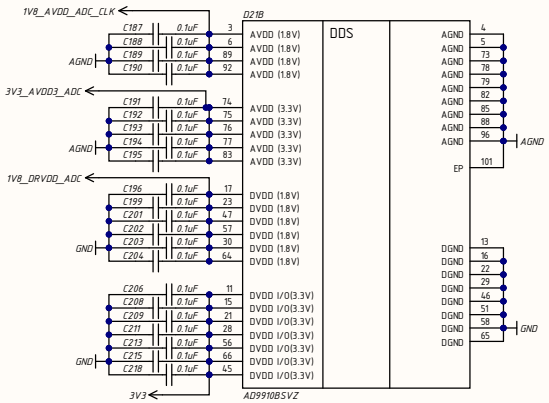
Fref = 10MHz
R = 1

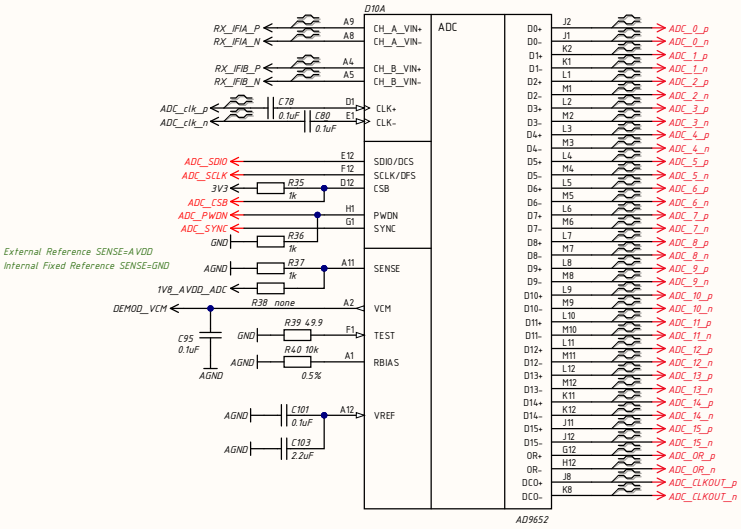
105kHz BW with 10MHz PFD frequency & 11.2mA CP current & 50 deg phase margin

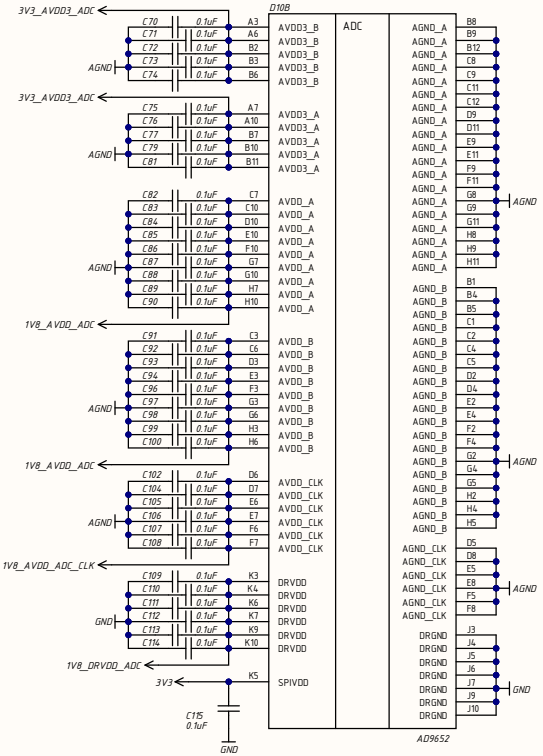
OUT0 = 1GHz CML DDS
OUT1 = 125MHz CML LO1
OUT2 = 125MHz CML FPGA
OUT3 = NC CML
OUT4 = 62.5MHz LVDS CPLD











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Копировал

Формат А3

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