DISCRETE SEMICONDUCTORS

DATA SHEET



BAT85Schottky barrier diode

Product specification Supersedes data of 1996 Mar 20 2000 May 25





Schottky barrier diode

BAT85

FEATURES

- Low forward voltage
- · Guard ring protected
- Hermetically-sealed leaded glass package.

APPLICATIONS

- Ultra high-speed switching
- Voltage clamping
- · Protection circuits
- · Blocking diodes.

DESCRIPTION

Planar Schottky barrier diode with an integrated protection ring against static discharges, encapsulated in a hermetically-sealed subminiature SOD68 (DO-34) package. The diode is suitable for mounting on a 2 E (5.08 mm) pitch.

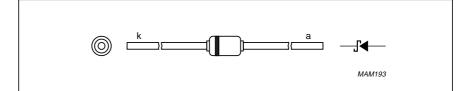


Fig.1 Simplified outline (SOD68; DO-34), pin configuration and symbol.

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _R	continuous reverse voltage		_	30	V
I _F	continuous forward current		_	200	mA
I _{F(AV)}	average forward current	PCB mounting, lead length = 4 mm; V_{RWM} = 25 V; a = 1.57; δ = 0.5; T_{amb} = 50 °C; see Fig.2	_	200	mA
I _{FRM}	repetitive peak forward current	$t_p \le 1 \text{ s}; \delta 0.5$	_	300	mA
I _{FSM}	non-repetitive peak forward current	$t_p \le 10 \text{ ms}$	_	5	Α
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	125	°C
T _{amb}	operating ambient temperature		-65	+125	°C

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ELECTRICAL CHARACTERISTICS

 T_{amb} = 25 °C; unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MAX.	UNIT
V _F	forward voltage	see Fig.3		
		I _F = 0.1 mA	240	mV
		I _F = 1 mA	320	mV
		I _F = 10 mA	400	mV
		I _F = 30 mA	500	mV
		I _F = 100 mA	800	mV
I _R	reverse current	V _R = 25 V; see Fig.4	2	μΑ
t _{rr}	reverse recovery time	when switched from I_F = 10 mA to I_R = 10 mA; R_L = 100 Ω ; measured at I_R = 1 mA; see Fig.6	4	ns
C _d	diode capacitance	f = 1 MHz; V _R = 1 V; see Fig.5	10	pF

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	320	K/W

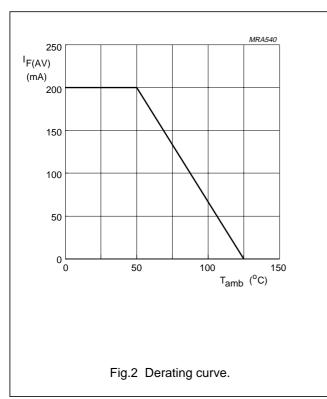
Note

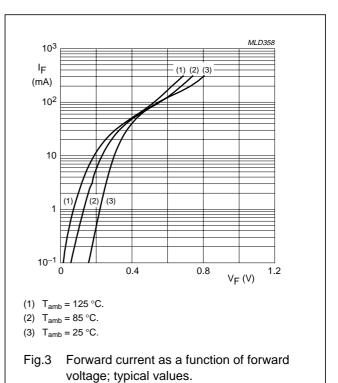
1. Refer to SOD68 standard mounting conditions.

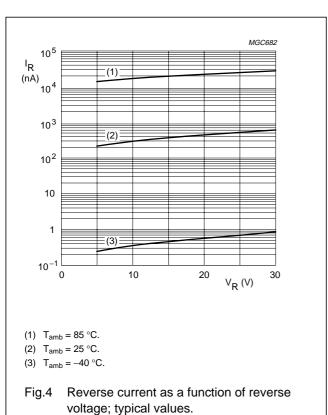
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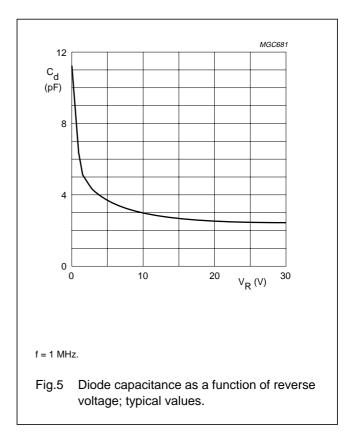
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GRAPHICAL DATA



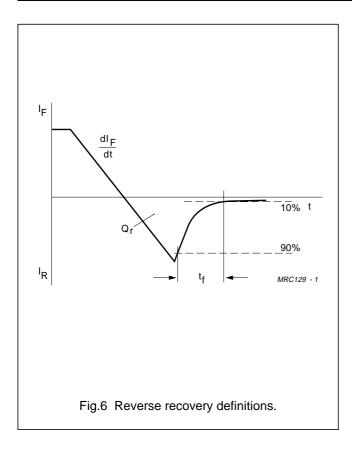






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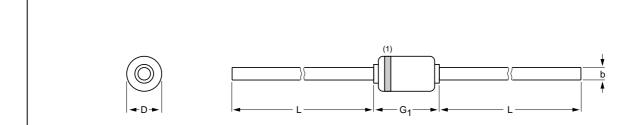
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PACKAGE OUTLINE

Hermetically sealed glass package; axial leaded; 2 leads

SOD68



DIMENSIONS (mm are the original dimensions)

UNIT	b max.	D max.	G ₁ max.	L min.	
mm	0.55	1.6	3.04	25.4	



Note

1. The marking band indicates the cathode.

OUTLINE		REFERENCES		EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOD68		DO-34				97-06-09

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DATA SHEET STATUS

DATA SHEET STATUS	PRODUCT STATUS	DEFINITIONS (1)
Objective specification	Development	This data sheet contains the design target or goal specifications for product development. Specification may change in any manner without notice.
Preliminary specification	Qualification	This data sheet contains preliminary data, and supplementary data will be published at a later date. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.
Product specification	Production	This data sheet contains final specifications. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.

Note

Please consult the most recently issued data sheet before initiating or completing a design.

DEFINITIONS

Short-form specification — The data in a short-form specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.

Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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