



The diagram shows six connections for the PWR_FLAG pin:

- PWR_FLAG is connected to VBAT.
- PWR_FLAG is connected to PGND.
- PWR_FLAG is connected to VIN_EMI.
- PWR_FLAG is connected to VIN.
- PWR_FLAG is connected to VCC.
- PWR_FLAG is connected to VOUT.

| | |
|---|------------------|
| An independent prototype design for the Super Step Down V3 By Julian Joaquin UBCO Aerospace Club | |
| Sheet: / File: 2025_SSDP3A2.kicad_sch | |
| <i>Title: Super Step Down P3A2</i> | |
| Size: A3 | Date: 2025-09-13 |
| KiCad E.D.A. | 9.0.4 |