

V_{IN} = 20~70V (50V Nom)
typ. 6/12S LiPo Battery

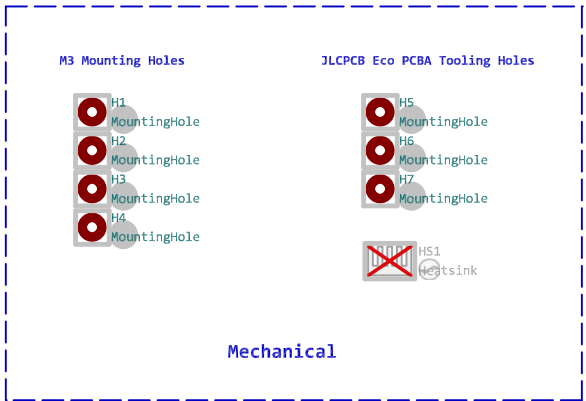
Inductor Ripple
d_I = 28% @ 5V
d_I = 55% @ 12V

Exclude from board if
low-side Q_{rr} < 40nC

I_{LIM} = 10.2A @ 5V
I_{LIM} = 11.2A @ 12V

Config 12V

5V 8A OUT



Stability Analysis of Compensation @ 5V

- PWR_FLAG → VBAT
- PWR_FLAG → PGND
- PWR_FLAG → VIN_EMI
- PWR_FLAG → VIN
- PWR_FLAG → VCC
- PWR_FLAG → VOUT

An independent prototype design for the Super Step Down V3 By Julian Joaquin UBCO Aerospace Club		
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Title: Super Step Down P3A2		
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