



Radxa E24C

Schematic

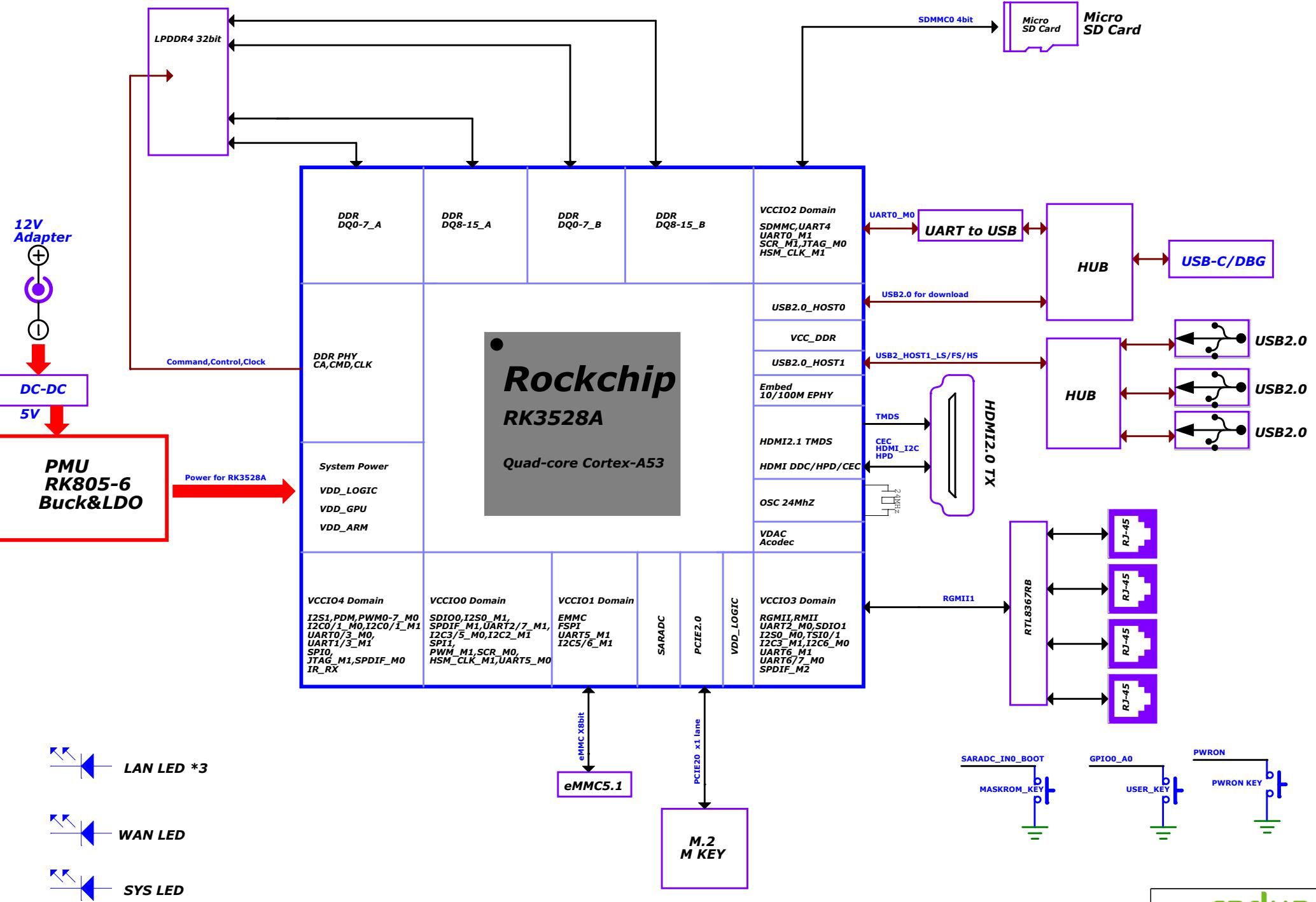
V1.2

Index

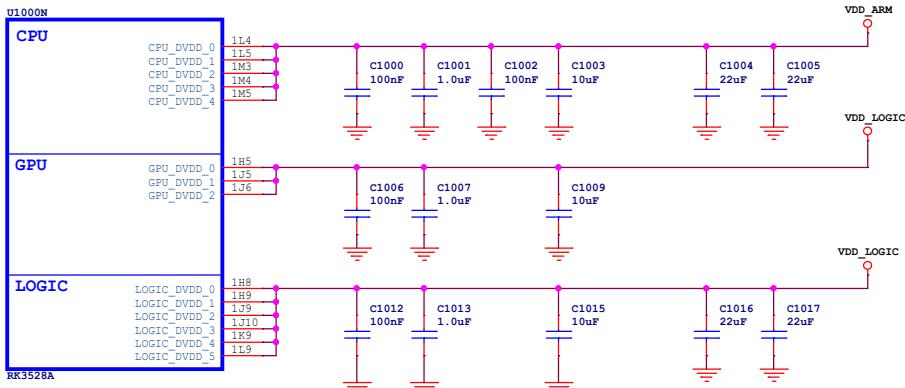
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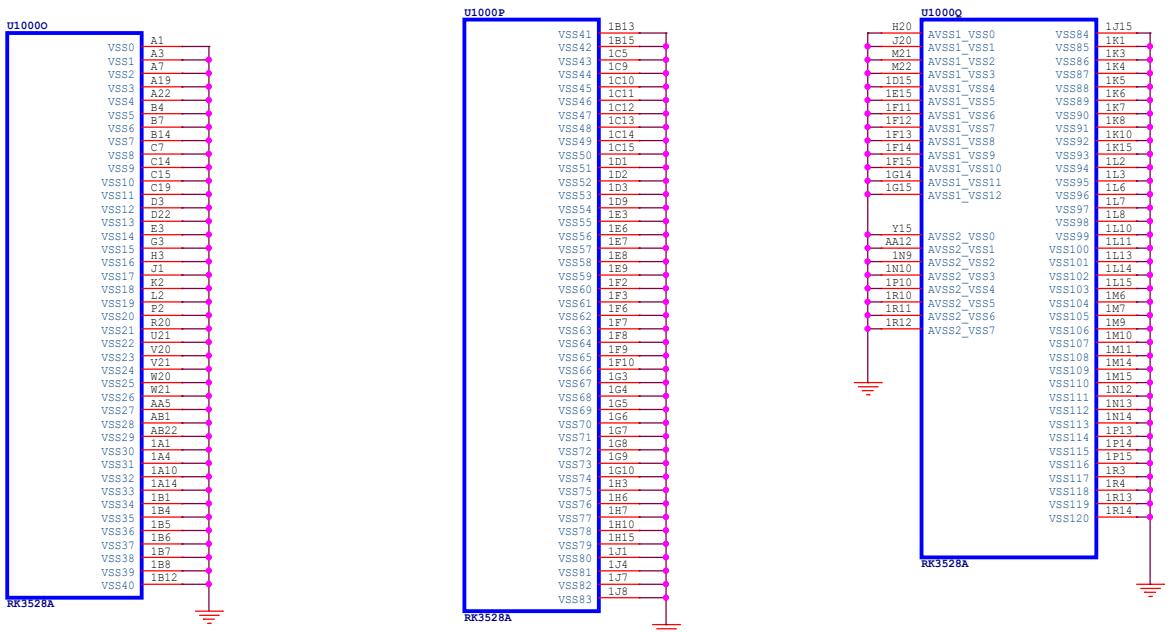
RK3528A Block Diagram



RK3528A_N (POWER)



RK3528A_O/P/Q (GND)

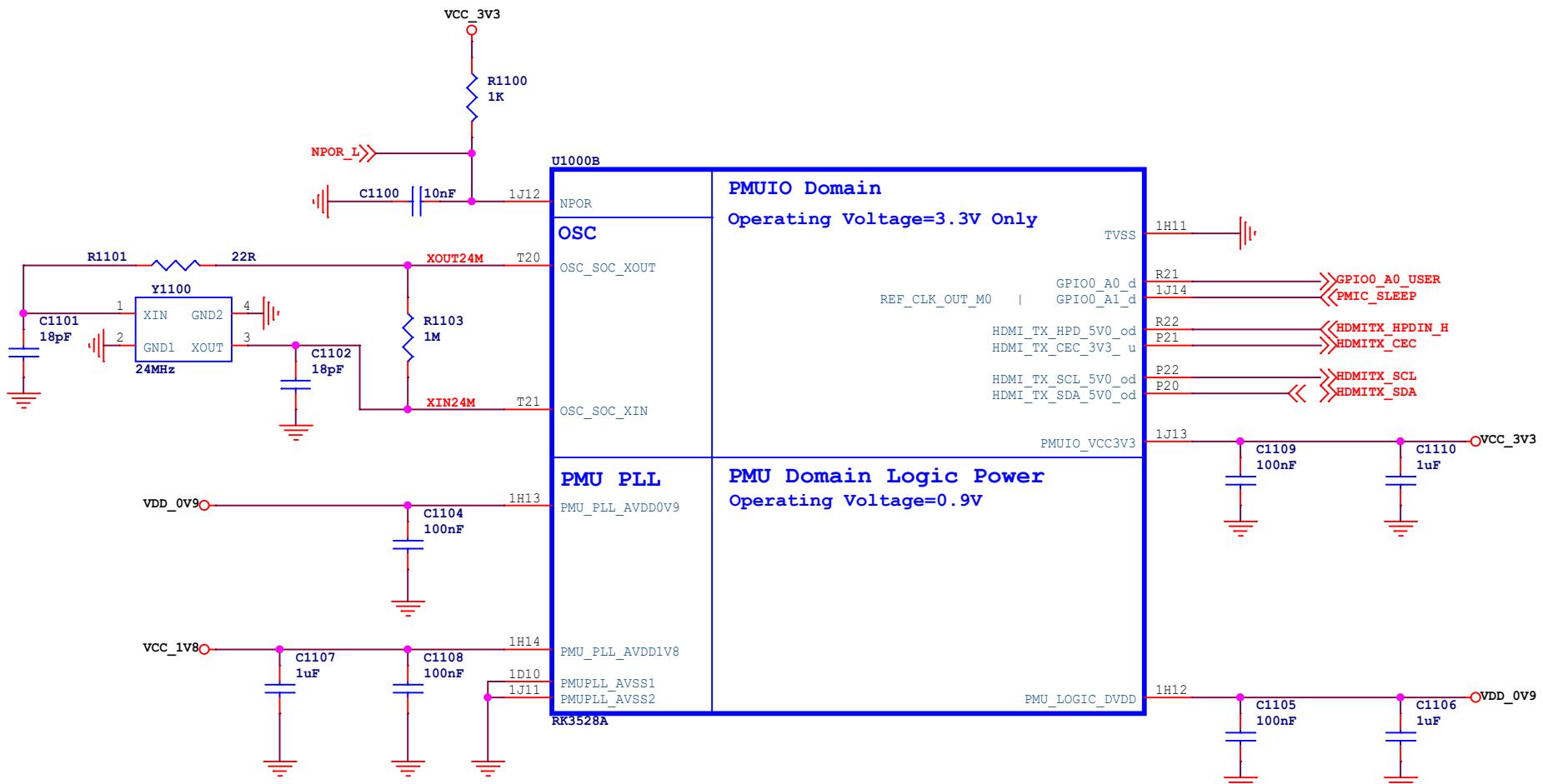


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A3	Page Name:	04.RK3528A Power/GND	V1.20

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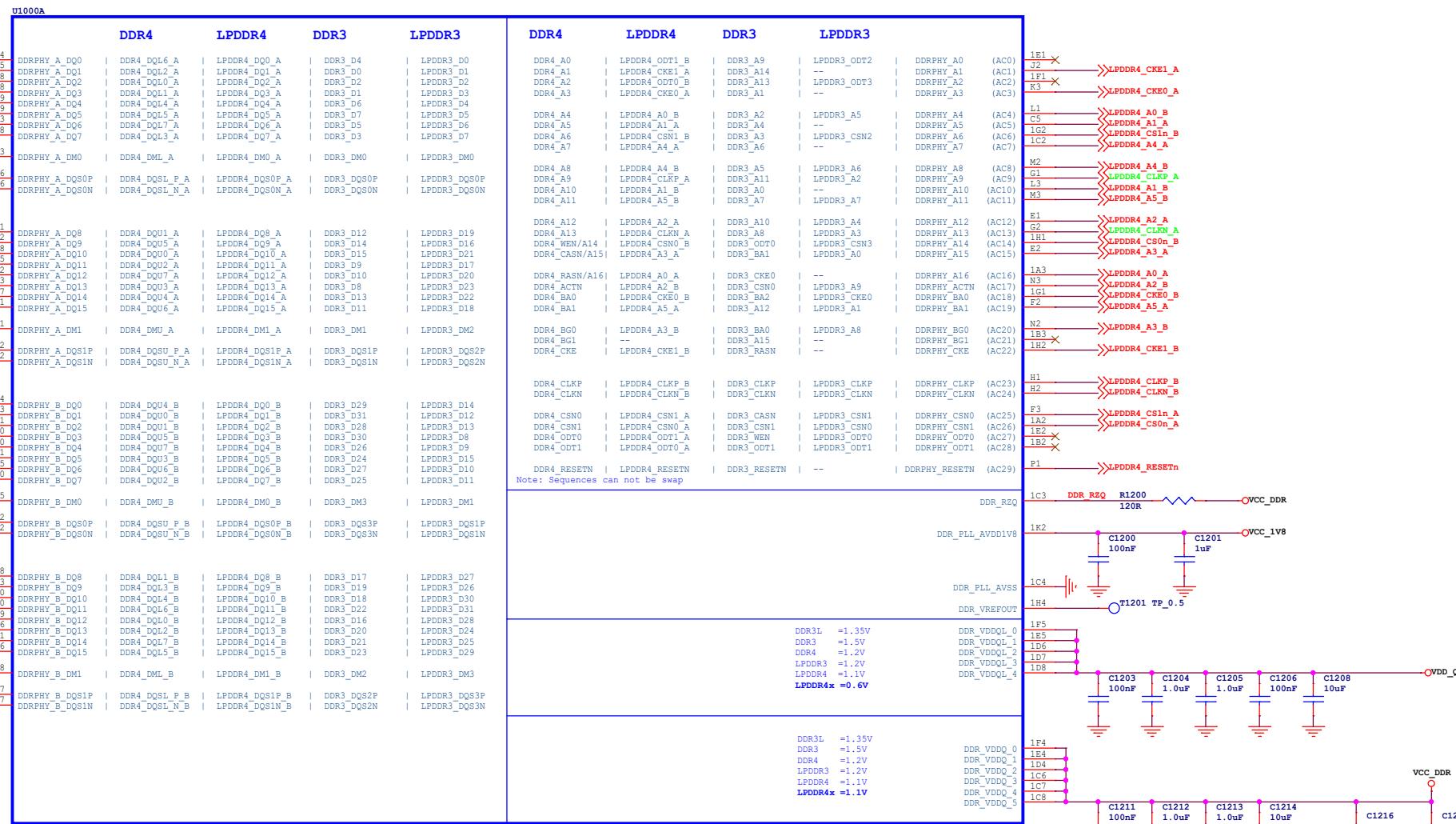
RK3528A_B (OSC/PLL/PMUIO Domain)



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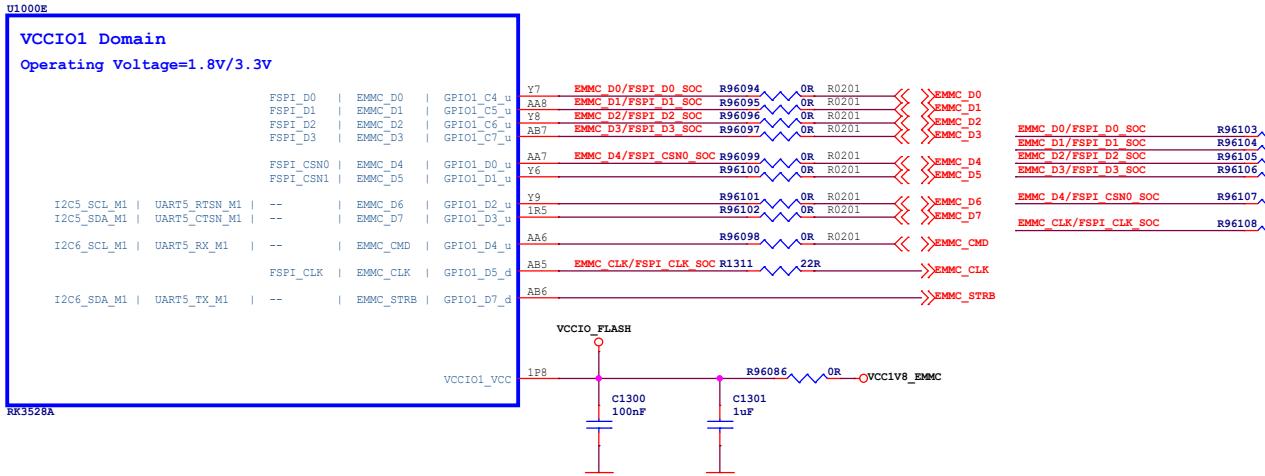
RK3528A A (DDR PHY)



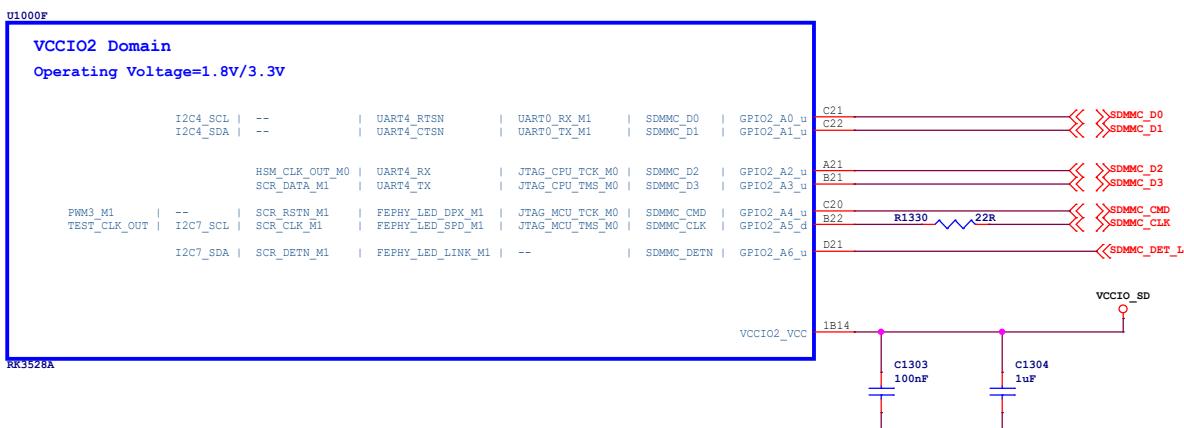
DDR4_RESETN | LPDDR4_RESETN

wap

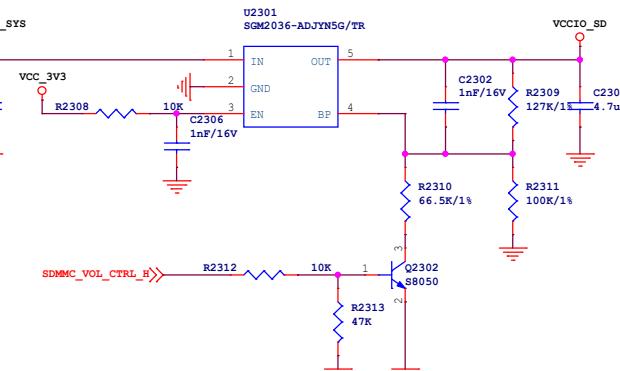
RK3528A_E (VCCIO1 Domain)



RK3528A_F (VCCIO2 Domain)



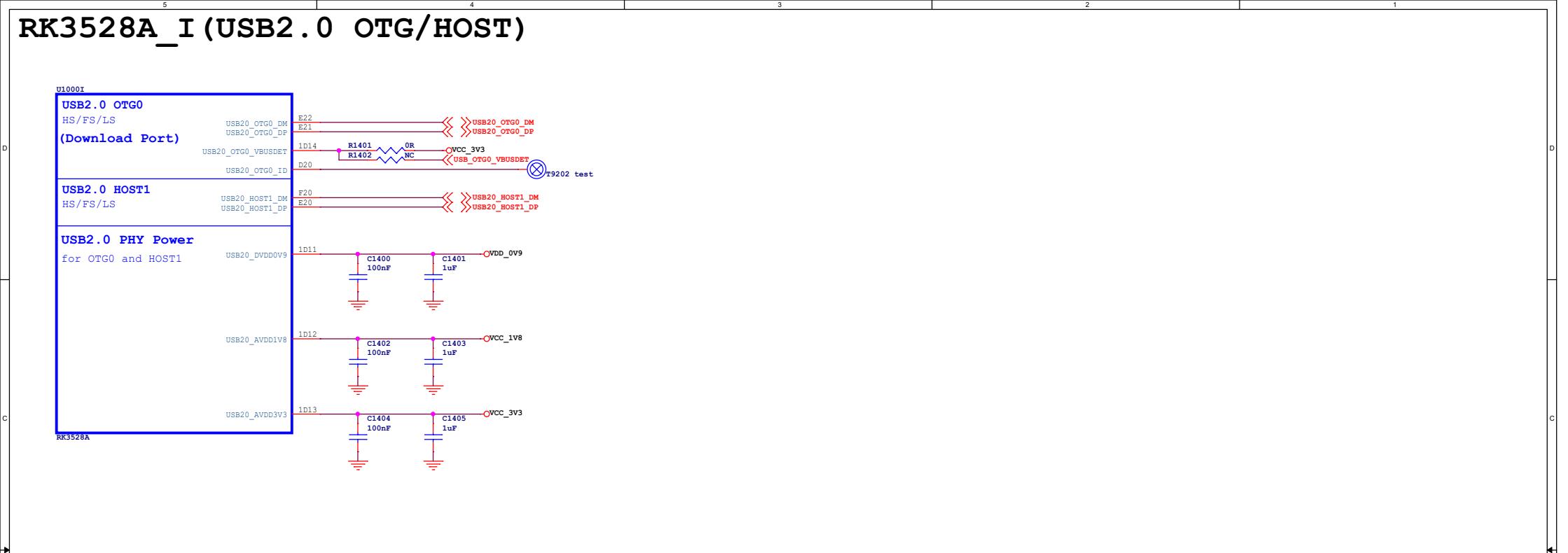
VCCIO_SD



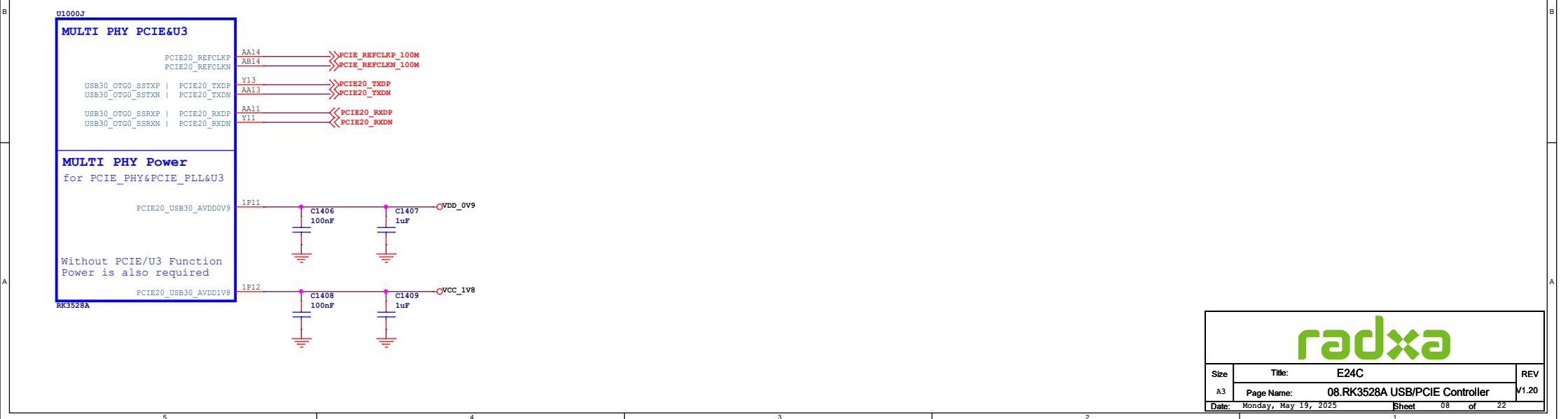
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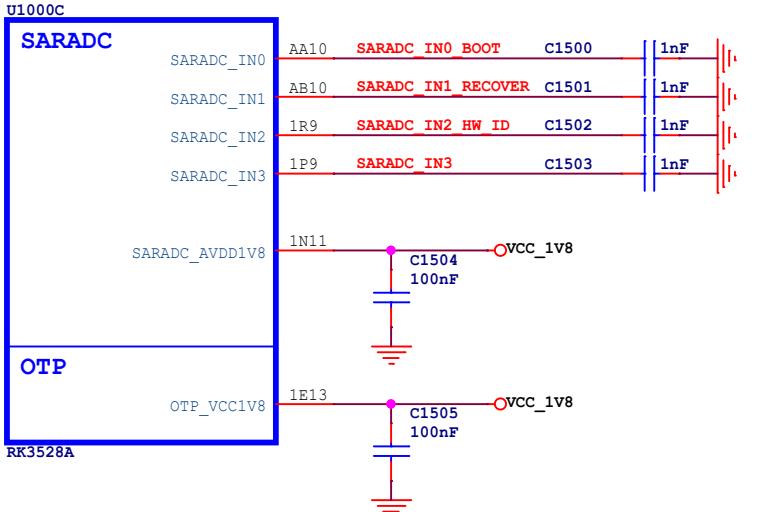
RK3528A_J (PCIE2.0/U3 PHY)



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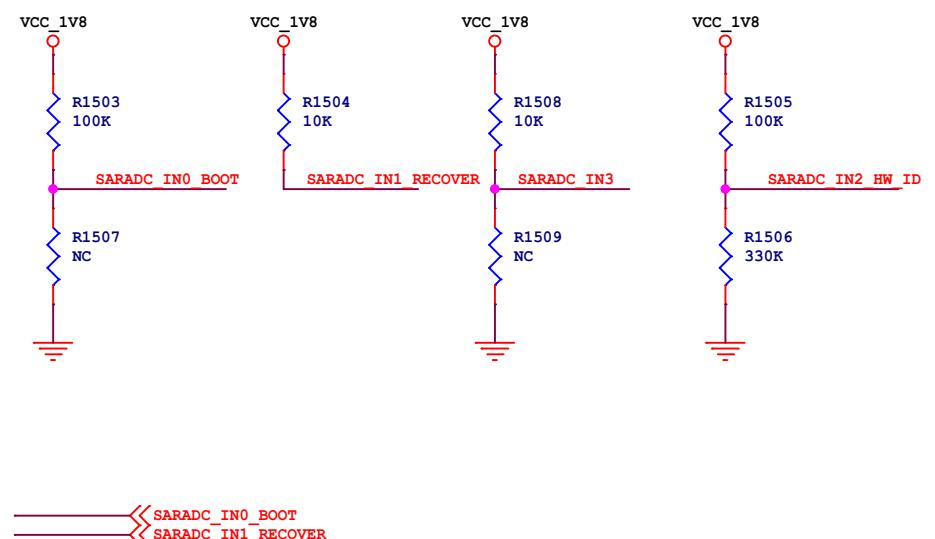
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RK3528A_C(Saradc/OTP)

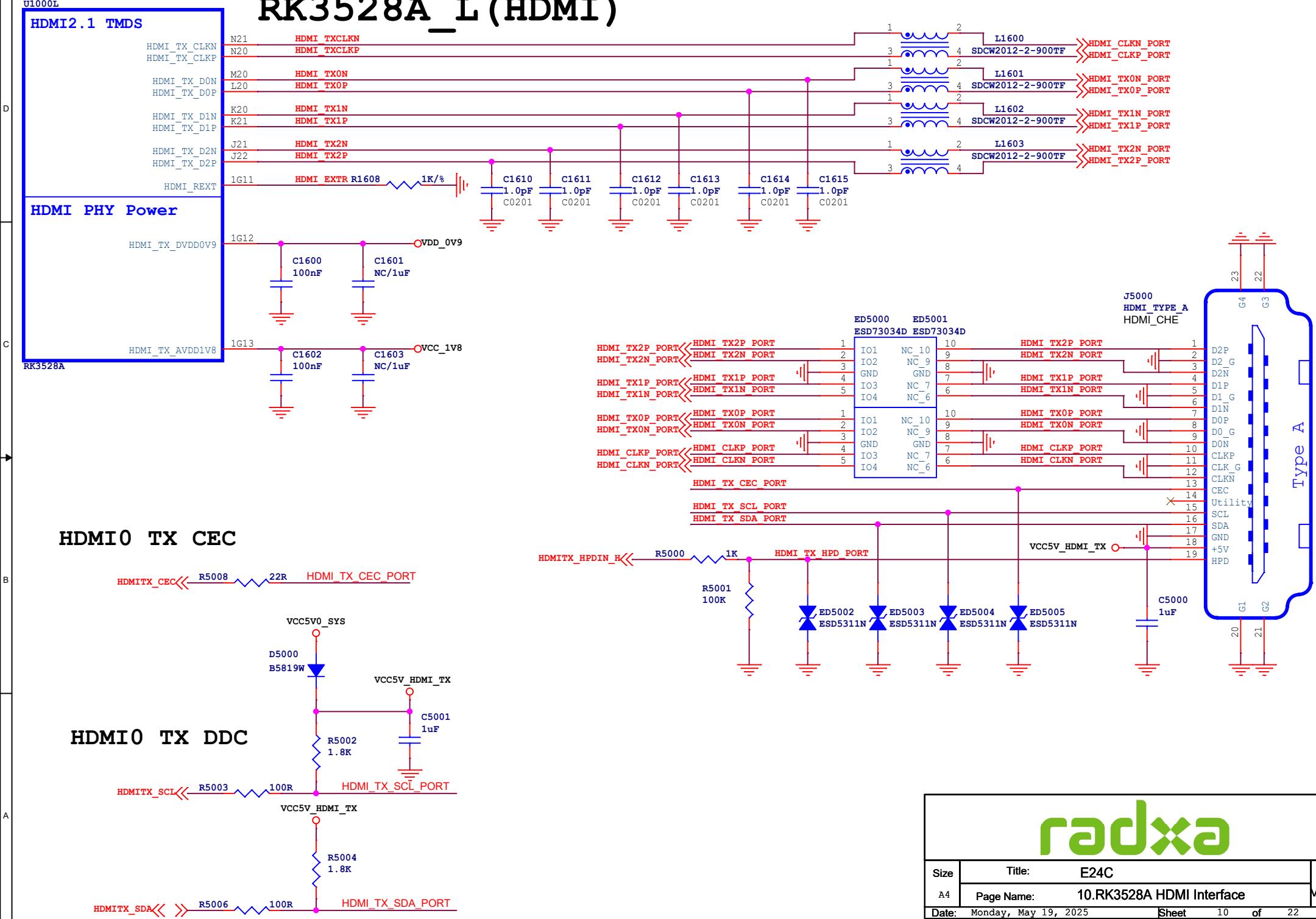


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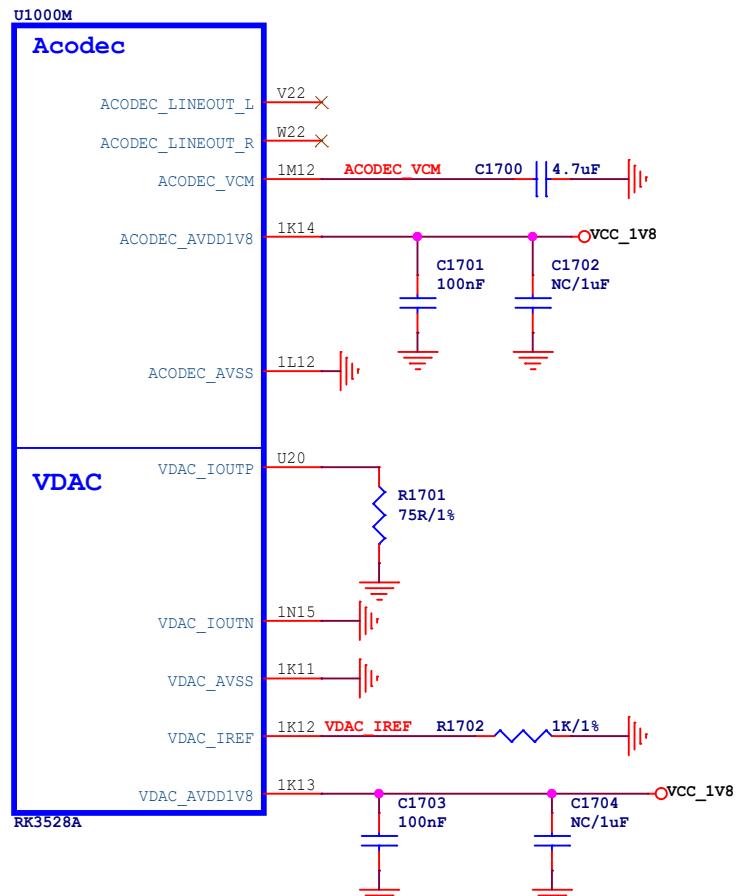
Item	Rup	Rdown	ADC	BOOT MODE
LEVEL1	DNP	100K	0	USB (Maskrom mode)
LEVEL2	100K	12K	114	
LEVEL3	100K	27K	228	FSPI--USB
LEVEL4	100K	51K	342	
LEVEL5	100K	82K	456	
LEVEL6	100K	120K	570	EMMC--USB
LEVEL7	100K	200K	683	EMMC--SD Card--USB
LEVEL8	100K	330K	796	SD Card--USB
LEVEL9	100K	820K	910	
LEVEL10	100K	DNP	1023	FSPI--EMMC--SD Card--USB



RK3528A_L (HDMI)



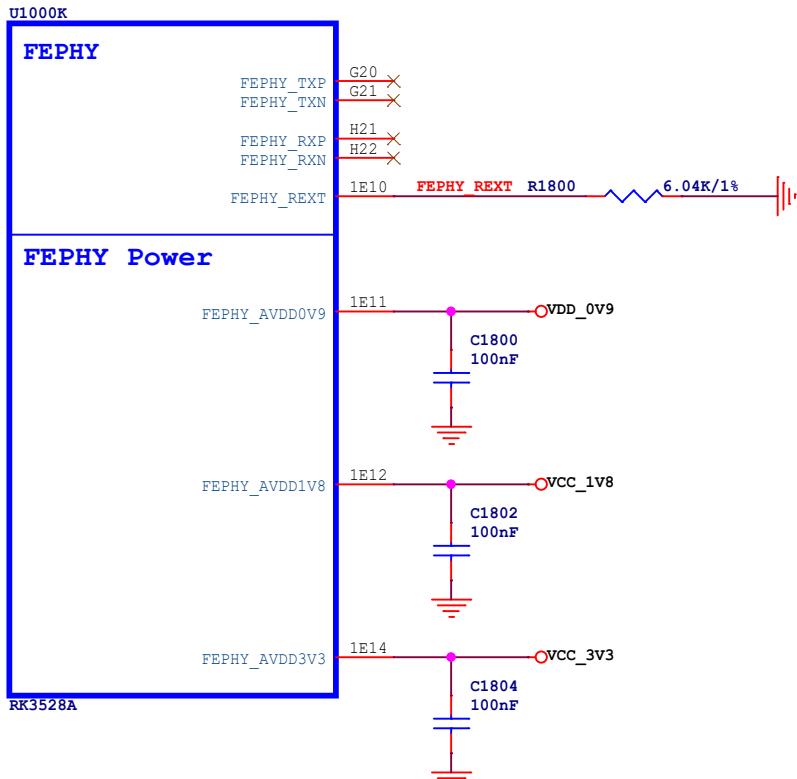
RK3528A_M(Acodec/VDAC)



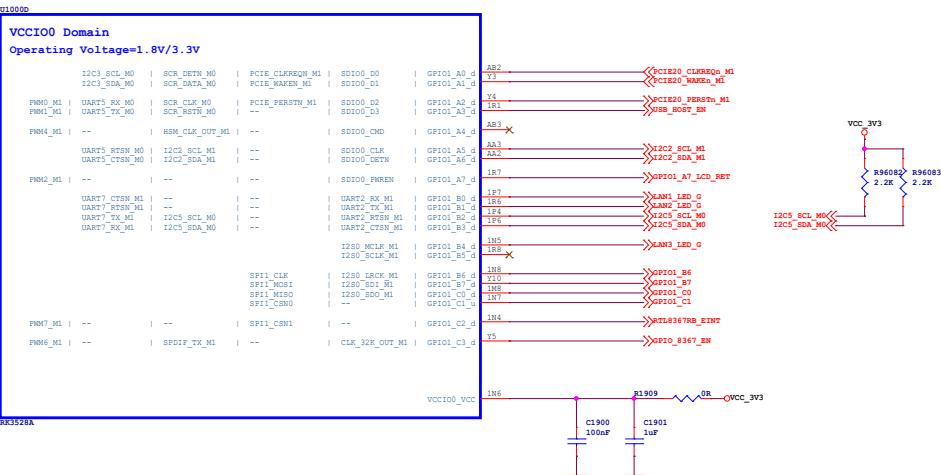
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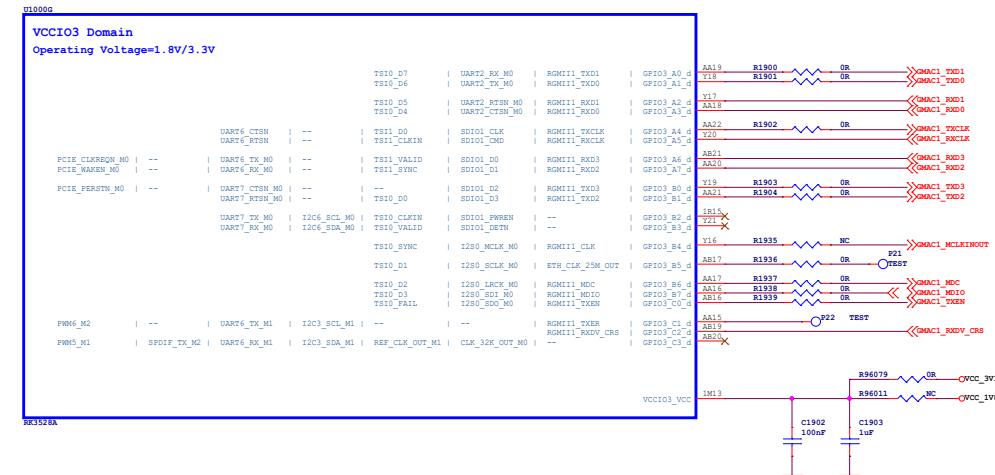
RK3528A_K(Embed FEPHY)



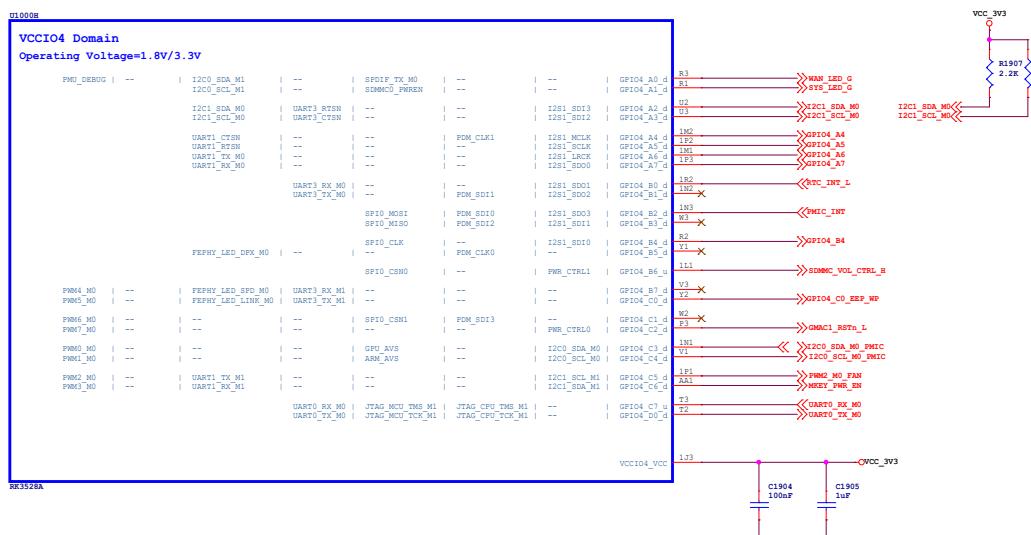
RK3528A_D (VCCIO0 Domain)



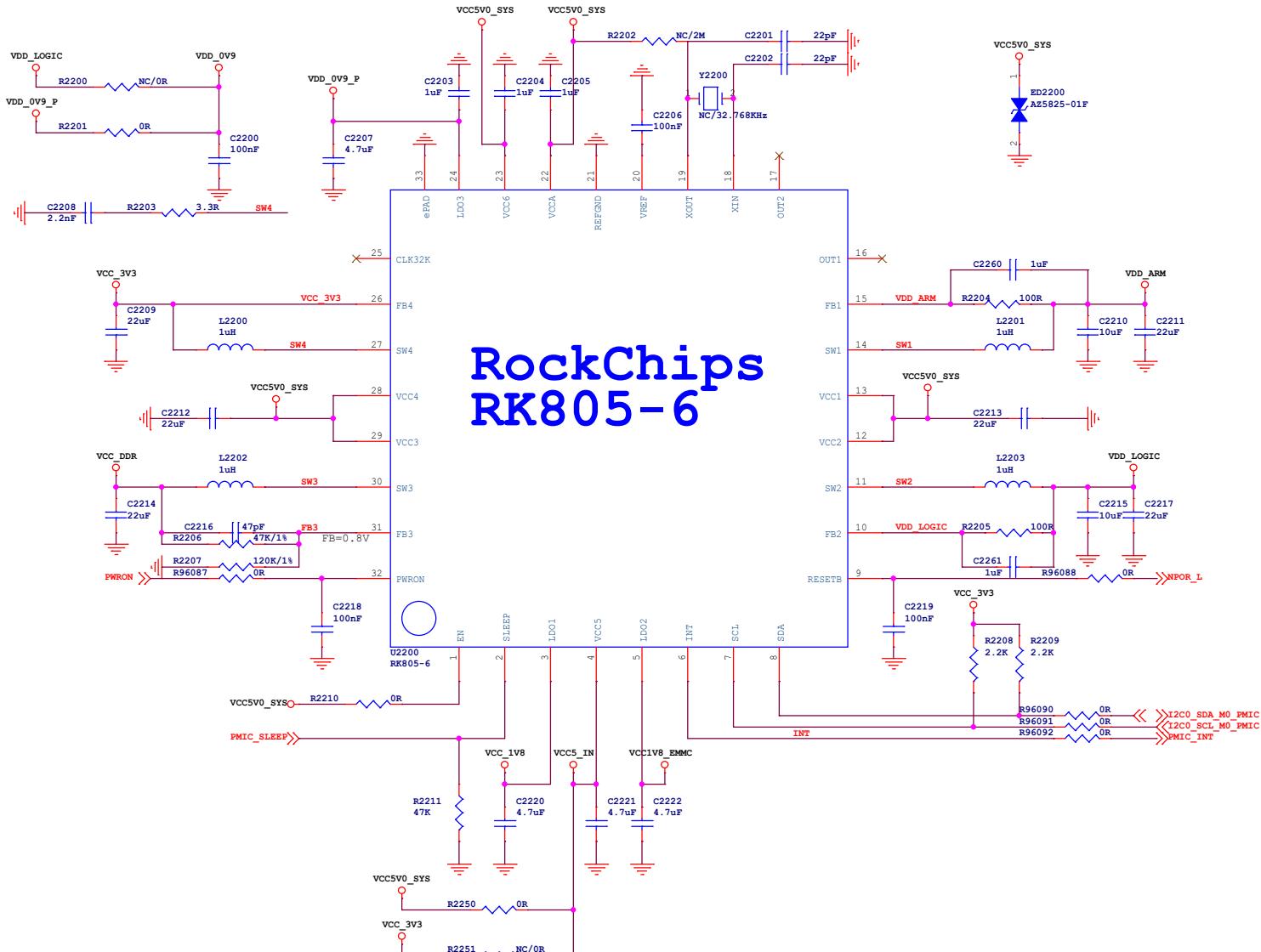
RK3528A_G (VCCIO3 Domain)



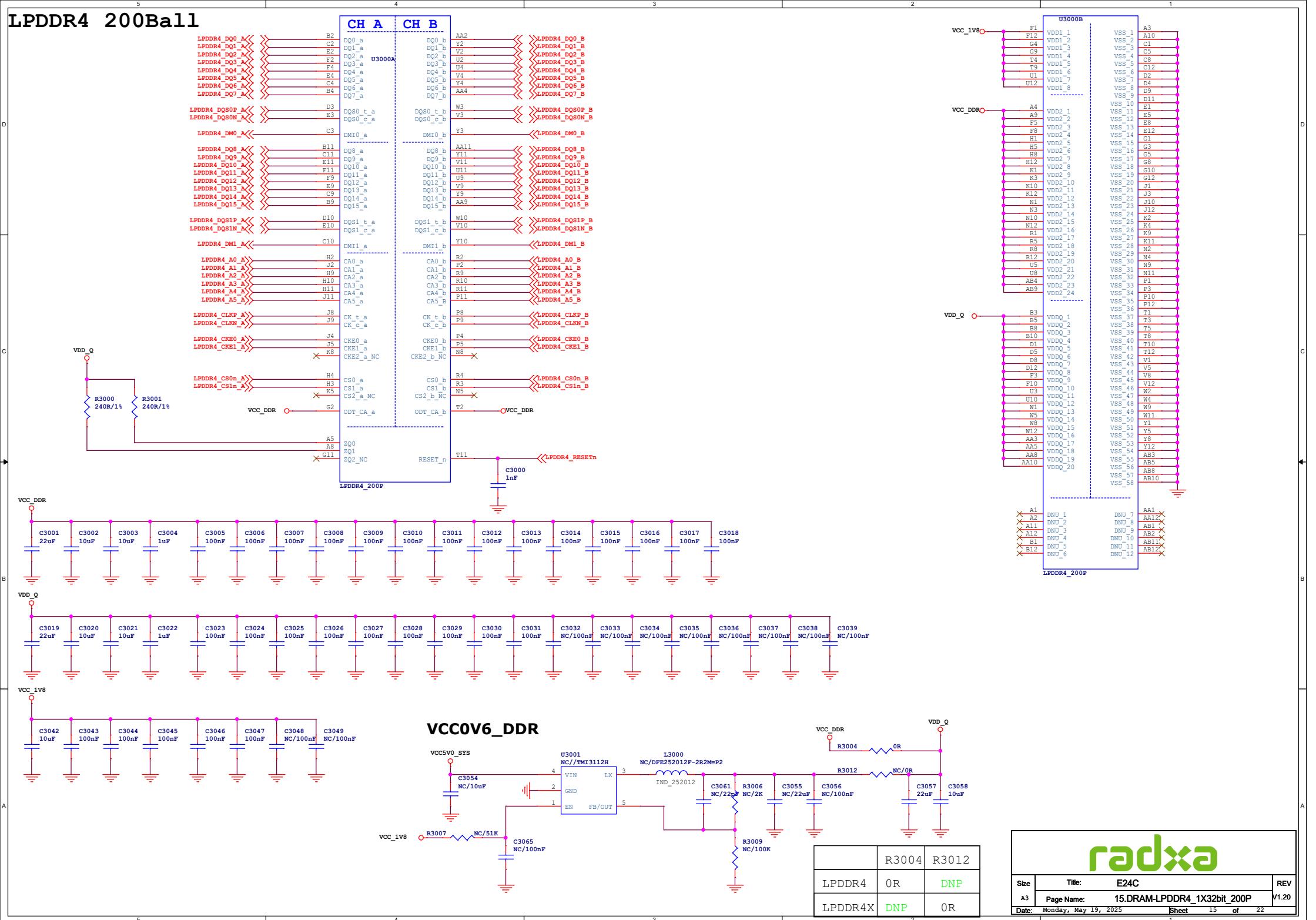
RK3528A_H (VCCIO4 Domain)

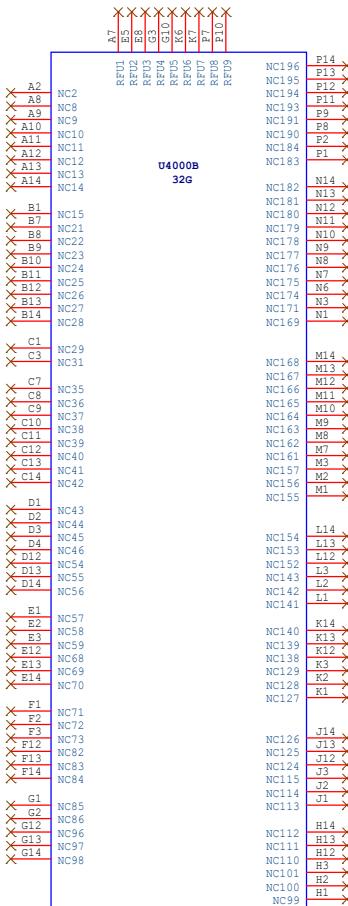
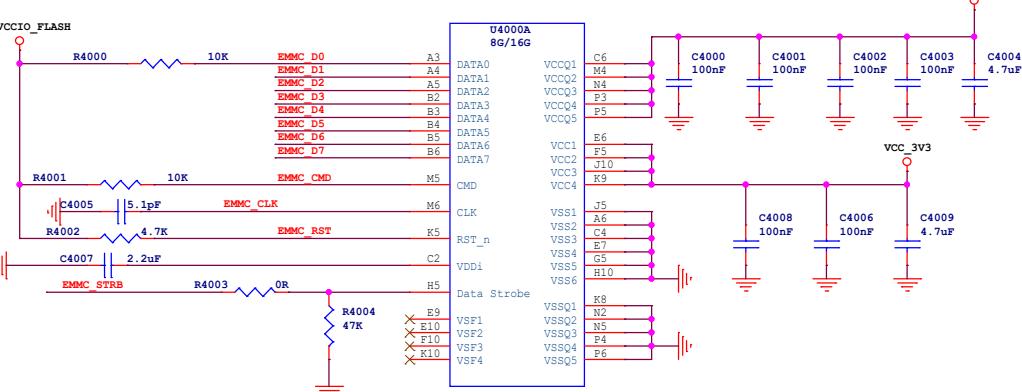
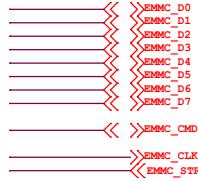


PMIC RK805-6

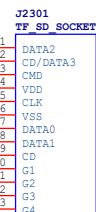
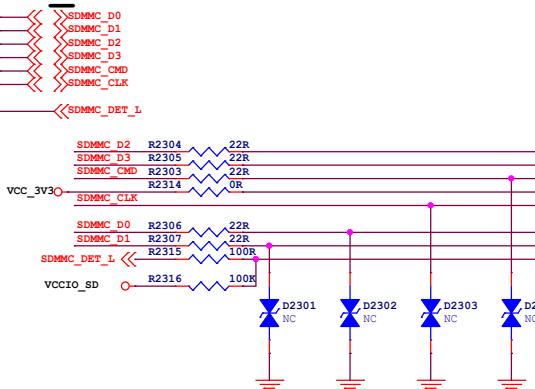


RockChips
RK805-6

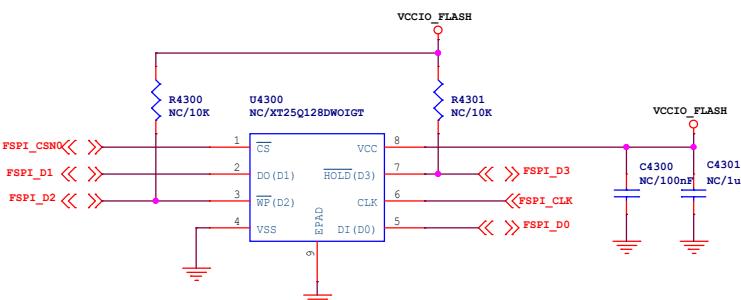




Micro SD Card

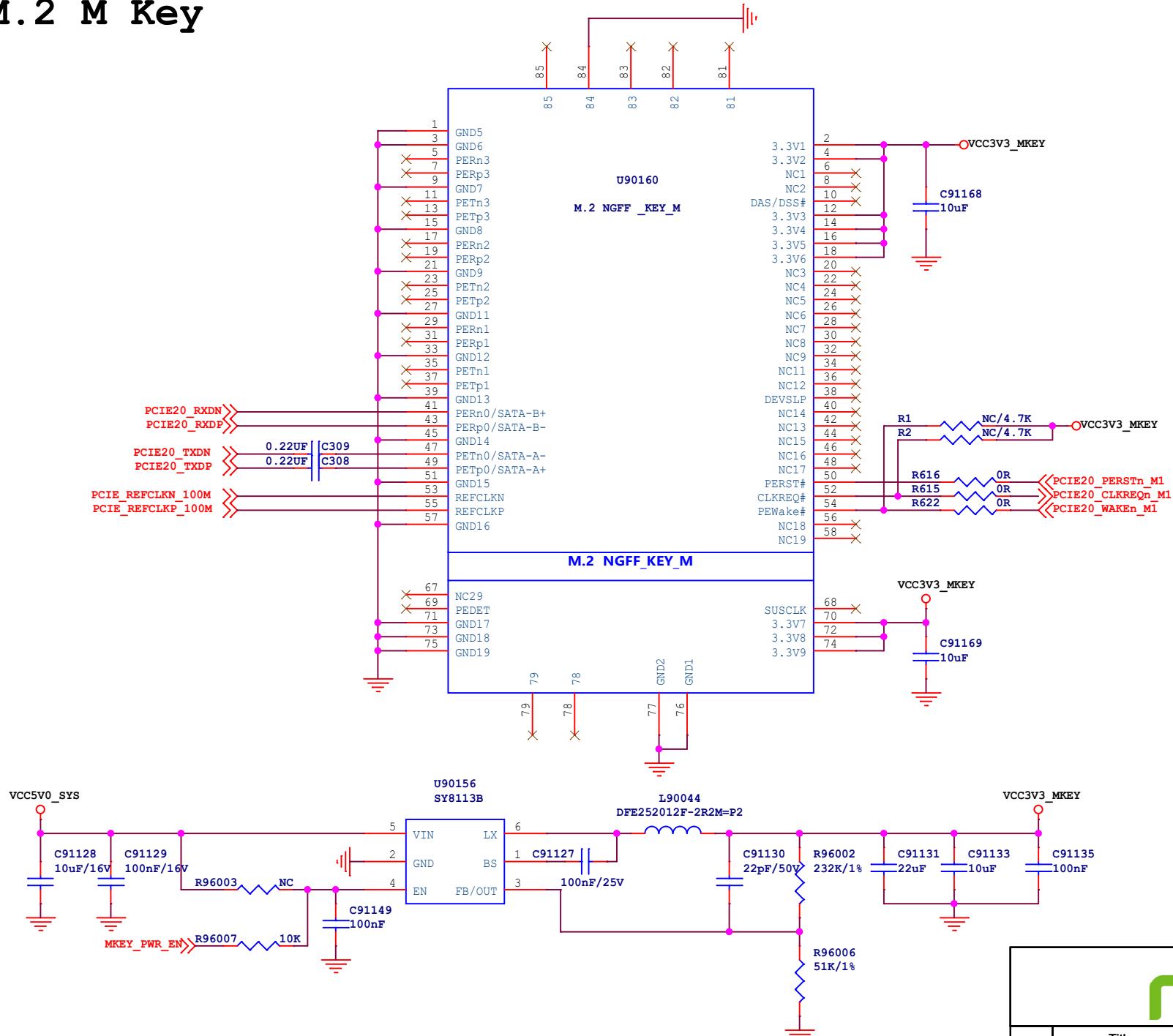


SPI FLASH (Option)

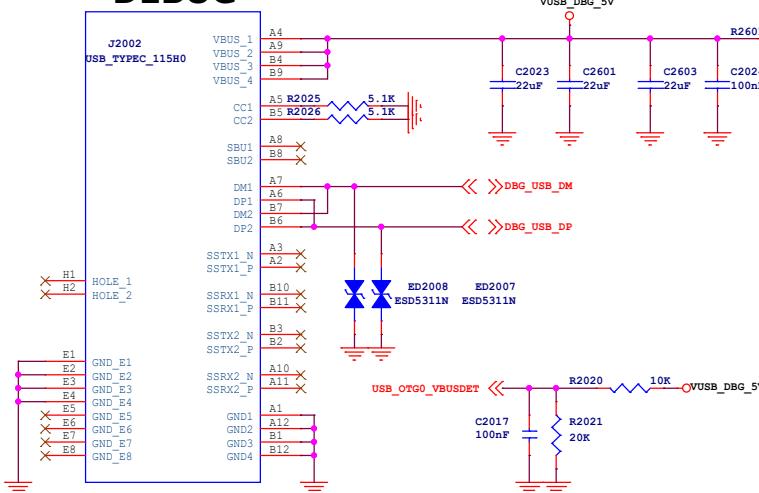


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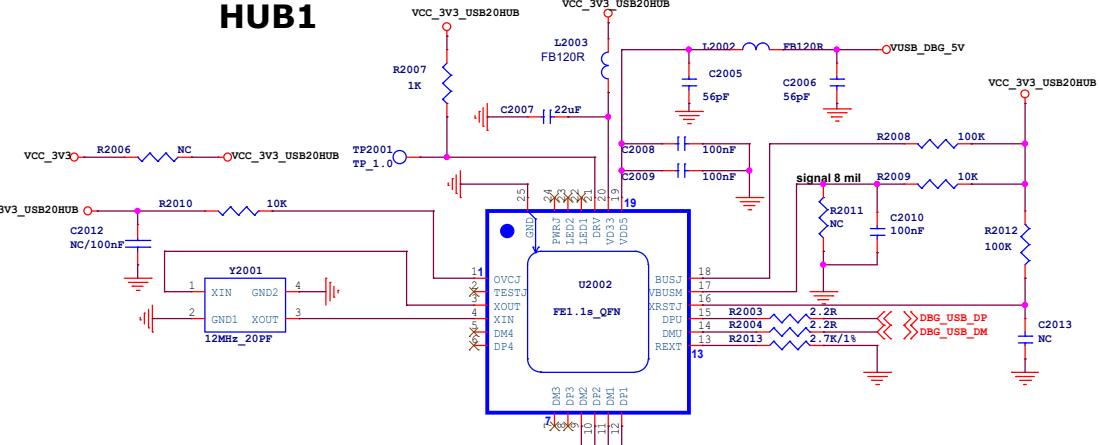
M.2 M Key



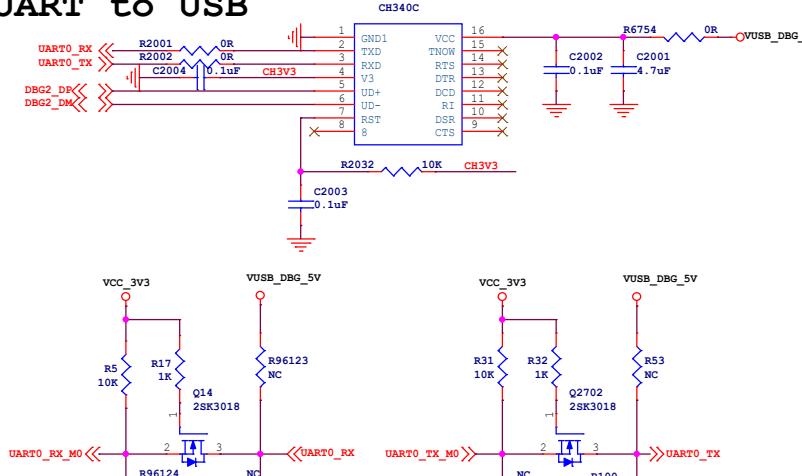
DEBUG



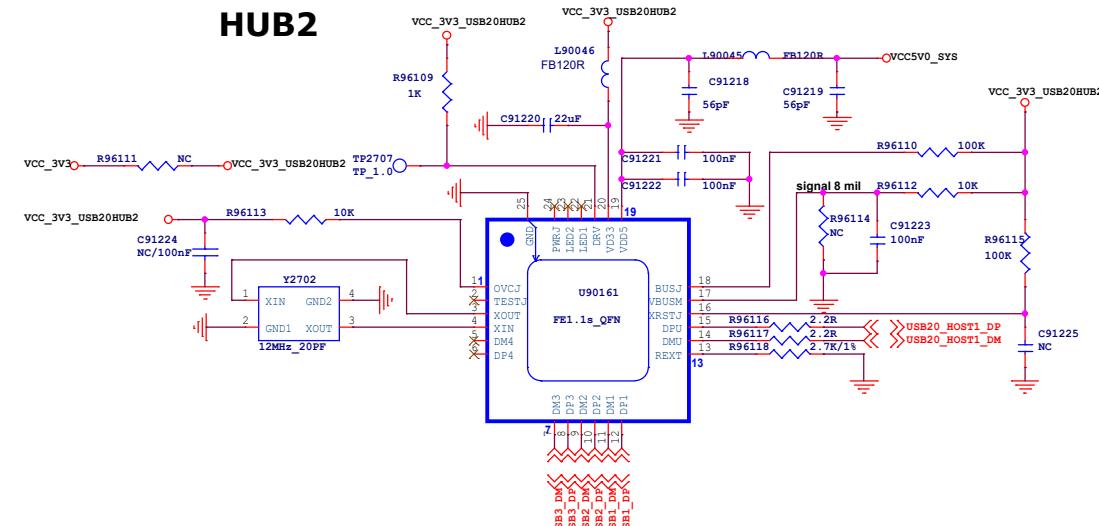
HUB1



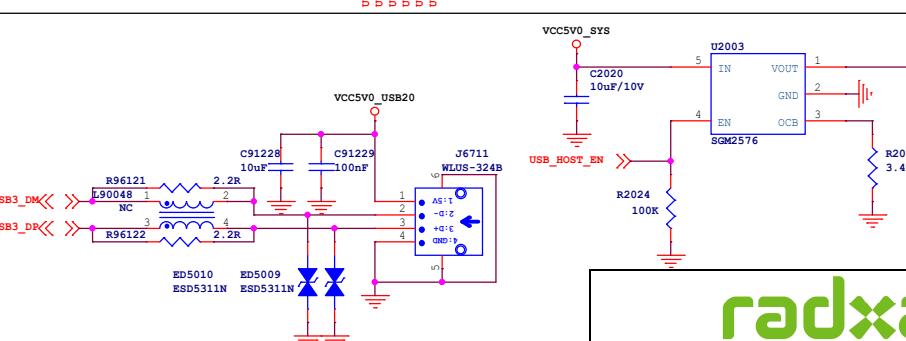
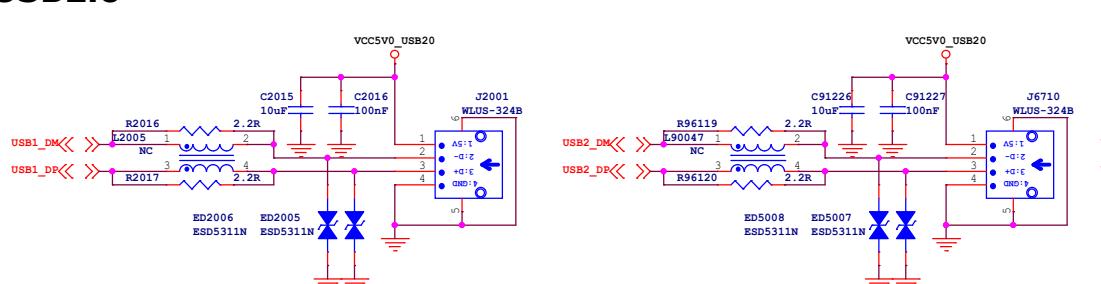
UART to USB



HUB2

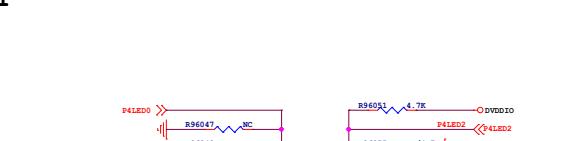
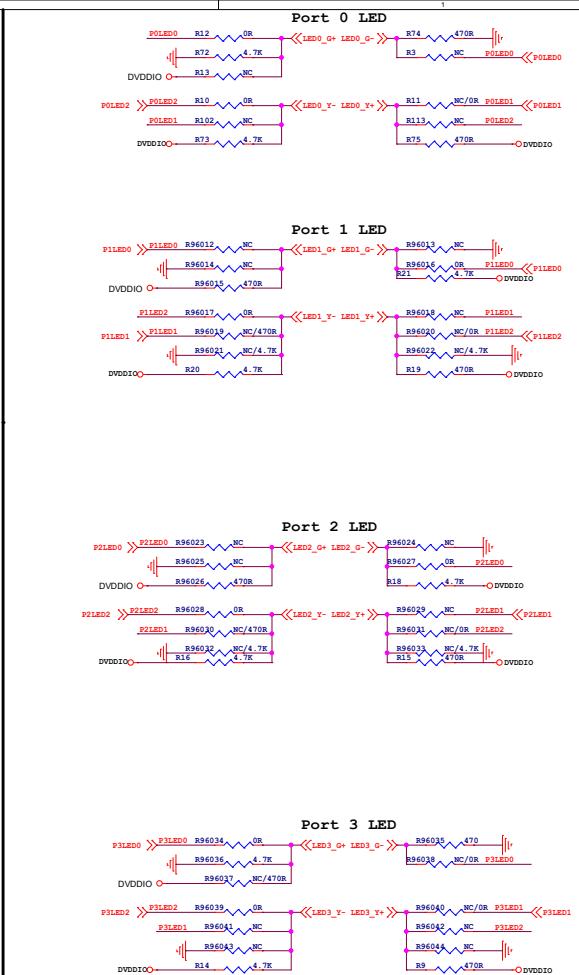
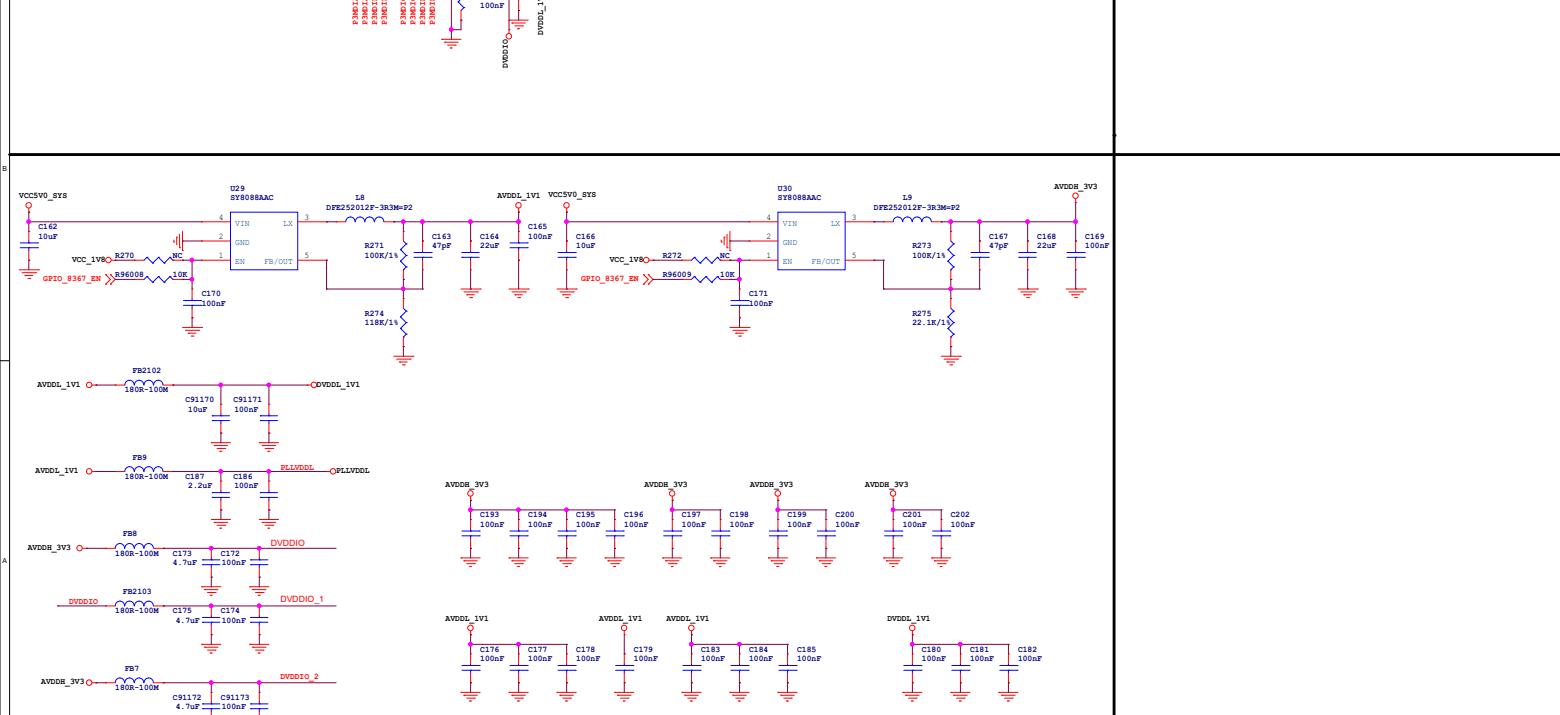
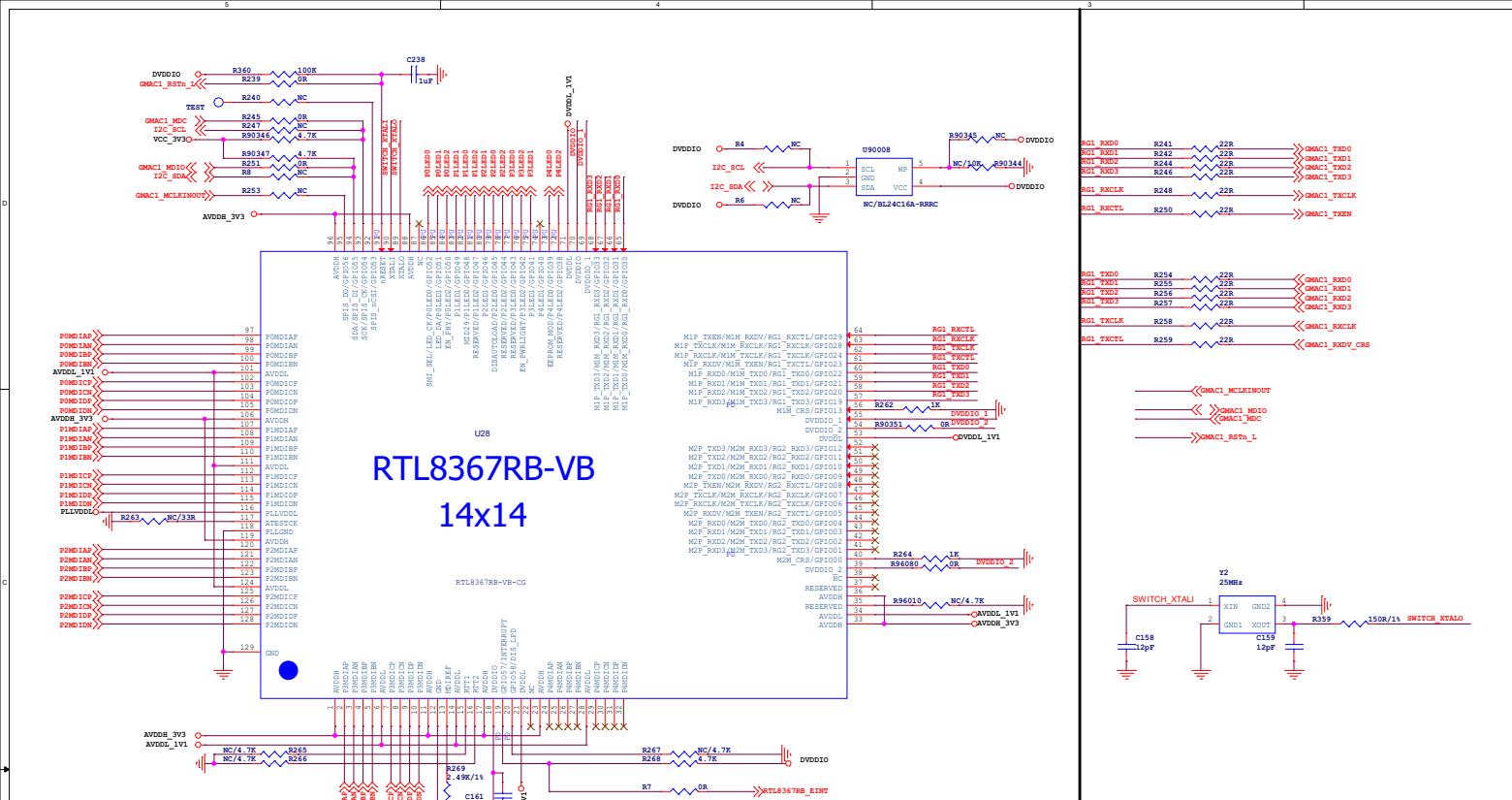


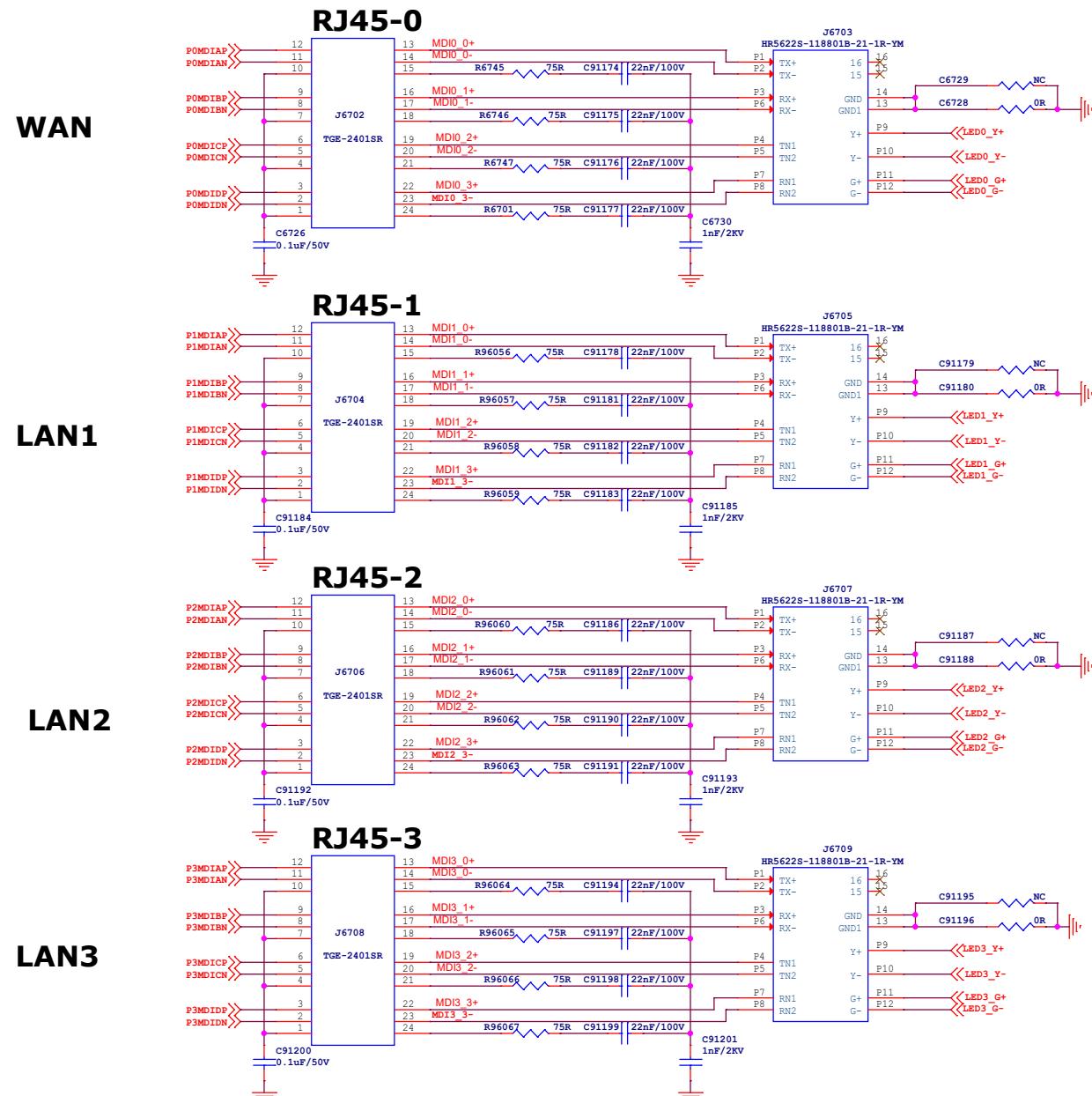
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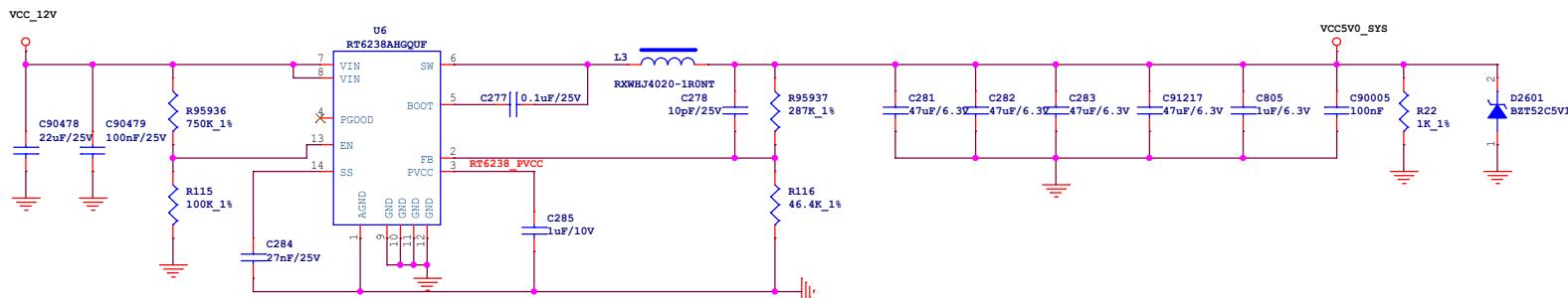
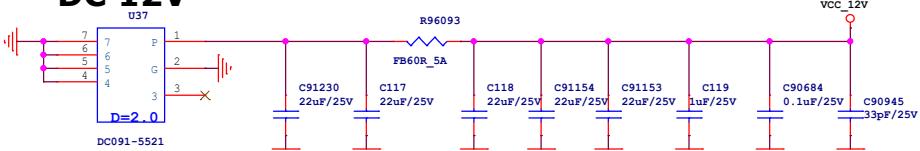


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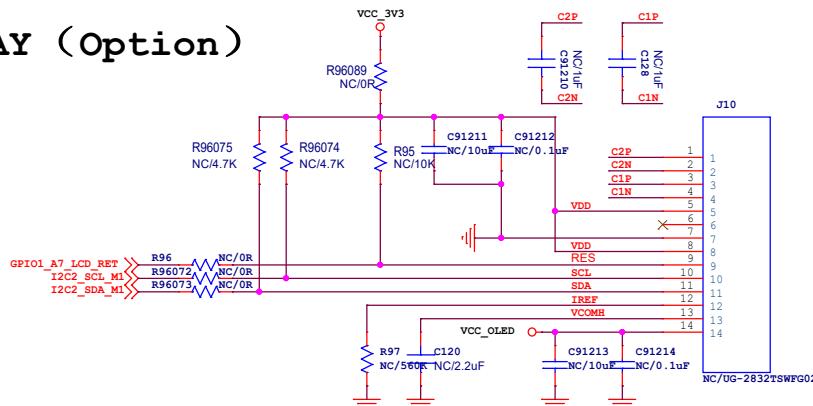
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DC 12V



TFT DISPLAY (Option)

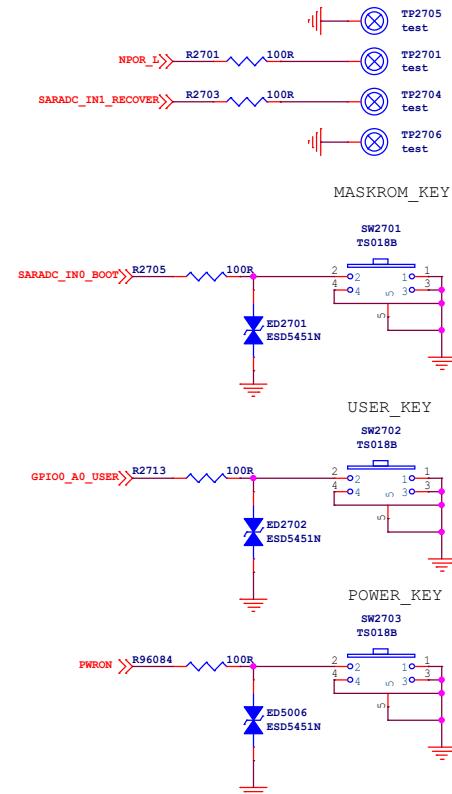


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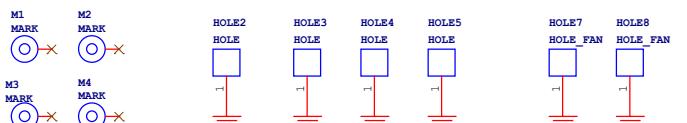
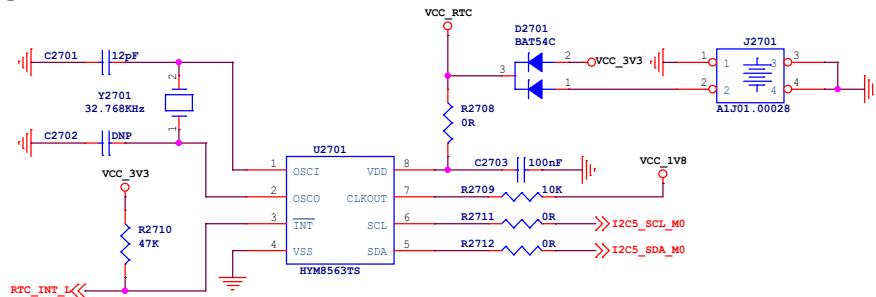
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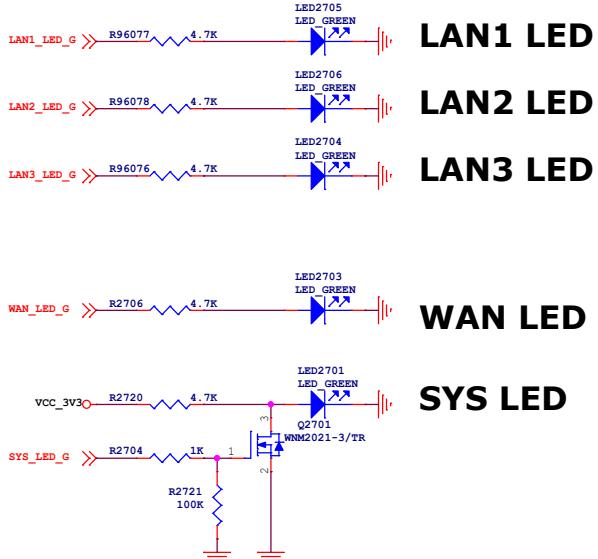
KEY



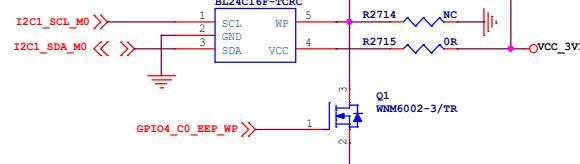
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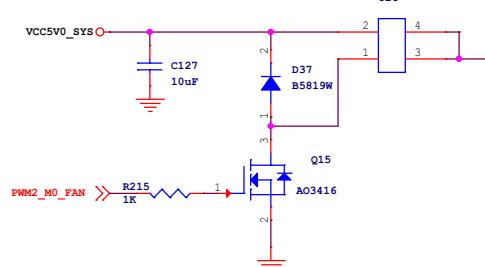
LED



EEPROM



FAN



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