

BOOT1	BOOT0	Boot Mode
X	0	Main Flash memory
0	1	System Memory
1	1	Embedded SRAM

Title		
Base Module Top level		
Size	Number	Revision
A		1.0
Date:	7/31/2015	Sheet 1 of 4
File:	C:\Users\...\BaseModuleTop.SchDoc	Drawn By: Aaron Smeenk

A

A

B

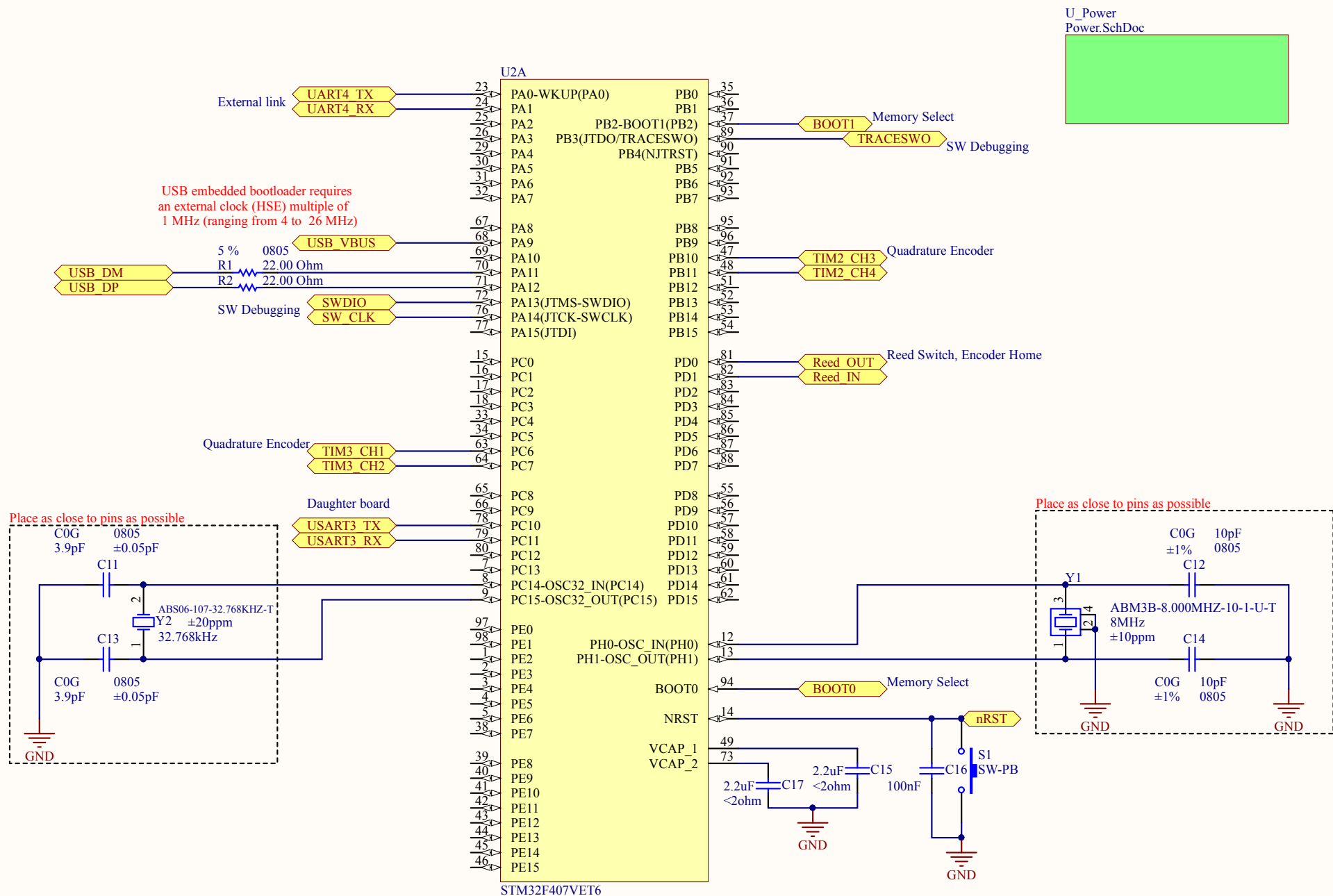
B

C

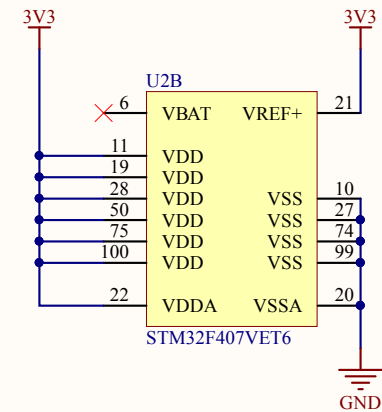
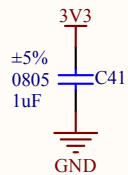
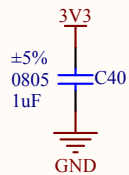
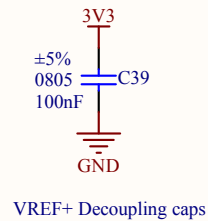
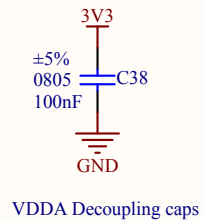
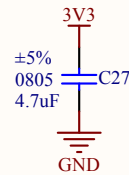
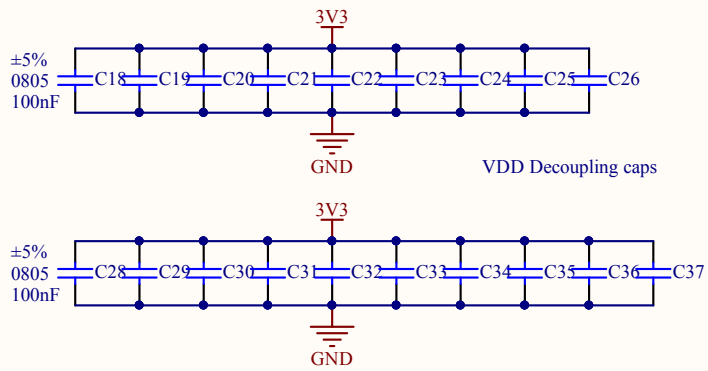
C

D

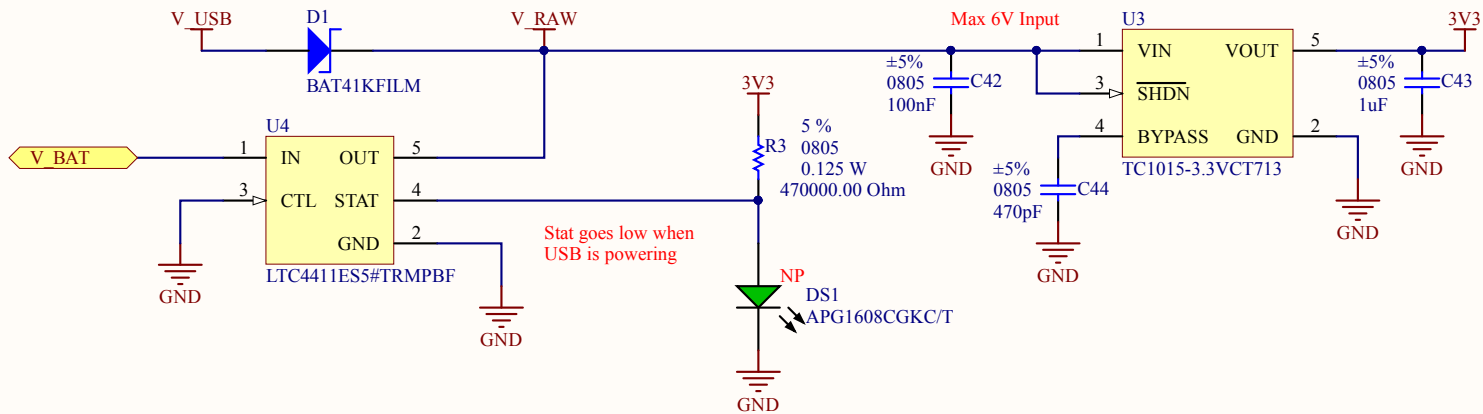
D



Title		
Microprocessor		
Size	Number	Revision
A		1.0
Date:	7/31/2015	Sheet 2 of 4
File:	C:\Users\...\MicroInterface.SchDoc	Drawn By: Aaron Smeenk



Title		
Microcontroller Power		
Size	Number	Revision
A		1.0
Date:	7/31/2015	Sheet 3 of 4
File:	C:\Users\...\Power.SchDoc	Drawn By: Aaron Smeenk



Title		
System Power		
Size	Number	Revision
A		1.0
Date:	7/31/2015	Sheet 4 of 4
File:	C:\Users\...\SystemPower.SchDoc	Drawn By: Aaron Smeenck