

Table Help

Current @ 3.3 V

Op Amp: $\leq 500 \mu A$

Transistor: $\sim 500 \mu A$ (max)

Rel Temp: $2.9 \mu A$ (max)

ASD: $300 \mu A$

Current @ 5V

Pinout - F411EE: $500 \mu A$ (max)

New Currents equal at $\sim 600 \mu A$ with a

comparable buffer.

Block Diagram Formulas:

$$R_{PST} = \frac{R_{PST}}{\left[\frac{V_{out}}{V_{ref}} - 1 \right]}$$

Assumed Value
 $R_{PST} = 100 \text{ k}\Omega$

$V_{out} = 5V$
 $V_{ref} = 1$

$$R_{PST} = \frac{100,000}{\left[\frac{5}{1} - 1 \right]}$$

$R_{PST} = 15,000 \Omega$

Inductor Selection

$$L = \frac{(V_{in} - V_{out})}{f_{sw} \times I_{out(max)}} \times \frac{V_{out}}{V_{in}}$$

20% of max
Current
margin

$V_{in} = 9V$
 $V_{out} = 5V$
 $I_{out(max)} = 0.6A$

$k = 0.5 I_{out(max)}$
 $k = 0.3 (0.6A)$
 $k = 0.18$

$$L = \frac{(9V - 5V)}{(400,000)(0.18)(0.6A)} \times \frac{5V}{9V}$$

$f_{sw} = 400 \text{ kHz}$

$L = 1.657 \times 10^{-4} \text{ H}$

$L = 165.7 \mu H$

we can pick

$L = 100 \mu H$ (Standard Capacitors)

Output Capacitor Selection

$$C_{out} \geq \frac{\Delta V_{out}}{f_{sw} \Delta V_{out} k} \left[(1-D) (1+k) \frac{h^2}{12} (2-D) \right] \text{ ?}$$

$\Delta V_{out} = 0.25V$
 $\Delta I_{out} = 100mA$

$k = 0.18$

$D = 0.55$