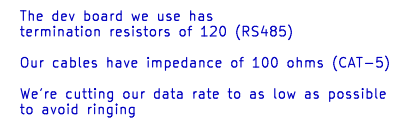


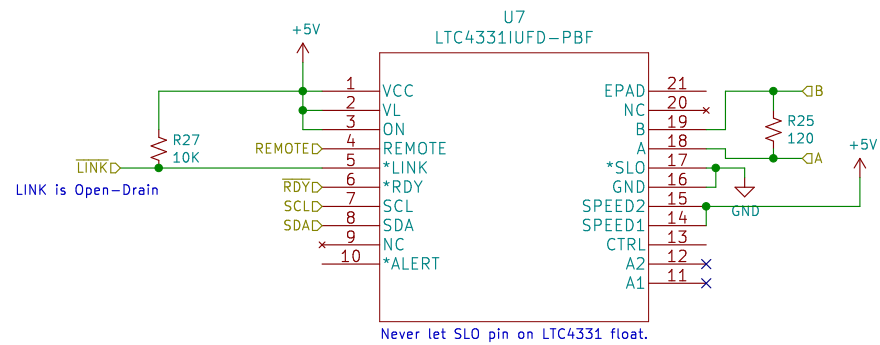
<https://www.analog.com/en/design-center/evaluation-hardware-and-software/evaluation-boards-kits/dc2754a.html>



Datasheet: <https://www.analog.com/media/en/technical-documentation/data-sheets/LTC4331.pdf>  
Page 18 for SPEED pin settings (currently 12.5khz)

We are using this chip through  
this dev board:

<https://www.analog.com/en/design-center/evaluation-hardware-and-software/evaluation-boards-kits/dc2754a.html>



The dev board we use has  
termination resistors of 120 (RS485)

Our cables have impedance of 100 ohms (CAT-5)

We're cutting our data rate to as low as possible  
to avoid ringing

Datasheet: <https://www.analog.com/media/en/technical-documentation/data-sheets/LTC4331.pdf>

Page 18 for SPEED pin settings (currently 12.5khz)

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