



We are using this chip through this dev board: https://www.analog.com/en/design-center/evaluation-hardware-and-software/evaluation-boards-kits/dc2754a.html +5V LTC4331IUFD-PBF The dev board we use has termination resistors of 120 (RS485) Our cables have impedance of 100 ohms (CAT-5) ₹R21 10K +5V REMOTED-REMOTE We're cutting our data rate to as low as possible to avoid ringing *LINK LINK is Open-Drain SCLD-8 SDA 10 *ALERT Never let SLO pin on LTC4331 float. Datasheet: https://www.analog.com/media/en/technical-documentation/data-sheets/LTC4331.pdf Page 18 for SPEED pin settings (currently 12.5khz) Sheet: /REMOTE_TRANSCEIVER/ File: TRANSCEIVER.sch Title: Size: A4 Date: Rev: KiCad E.D.A. kicad (5.1.6)-1 Id: 3/5

We are using this chip through this dev board: https://www.analog.com/en/design-center/evaluation-hardware-and-software/evaluation-boards-kits/dc2754a.html +5V LTC4331IUFD-PBF The dev board we use has termination resistors of 120 (RS485) R25 Our cables have impedance of 100 ohms (CAT-5) ₹R27 +5V REMOTED-REMOTE We're cutting our data rate to as low as possible to avoid ringing *LINK LINK is Open-Drain SCLD-8 SDA 10 *ALERT Never let SLO pin on LTC4331 float. Datasheet: https://www.analog.com/media/en/technical-documentation/data-sheets/LTC4331.pdf Page 18 for SPEED pin settings (currently 12.5khz) Sheet: /LOCAL_TRANSCEIVER/ File: TRANSCEIVER.sch Title: Size: A4 Date: Rev: KiCad E.D.A. kicad (5.1.6)-1 Id: 4/5

