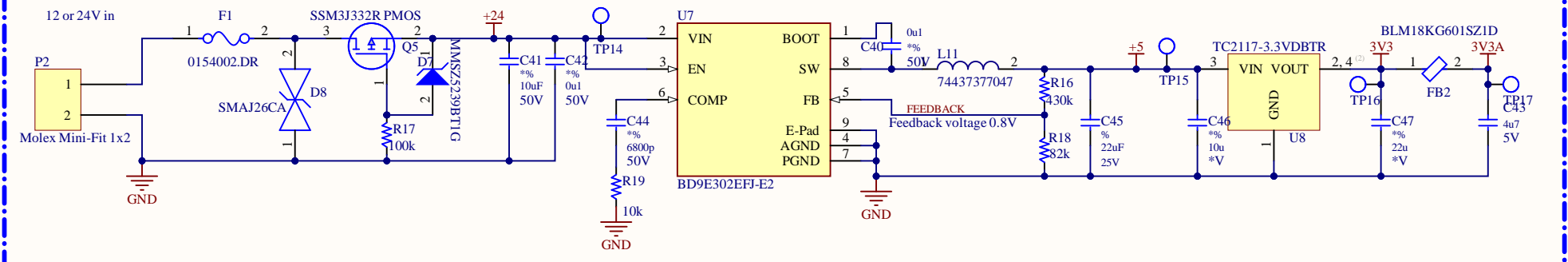
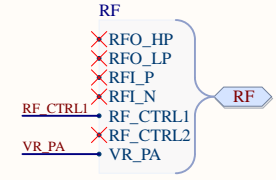
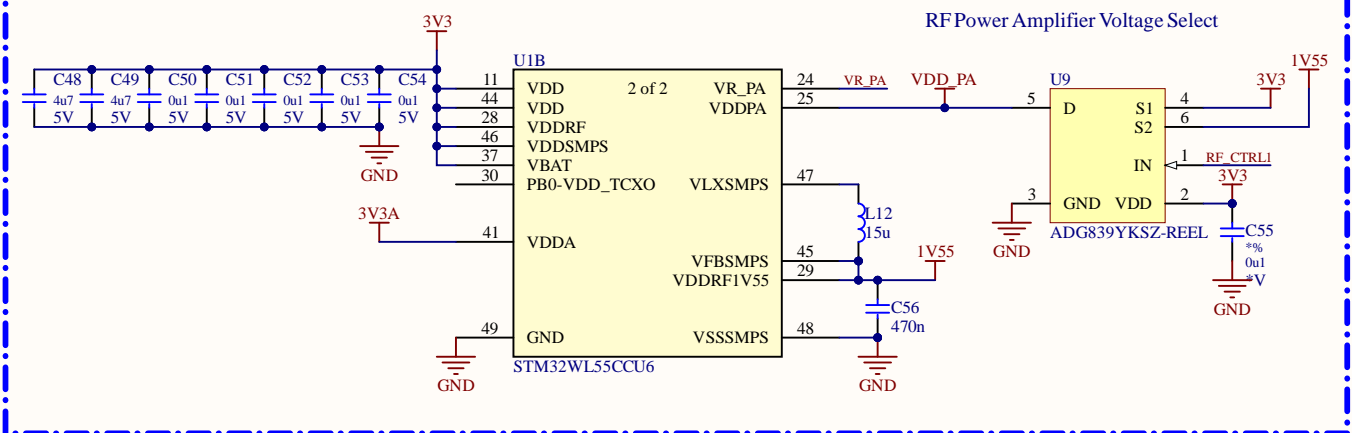
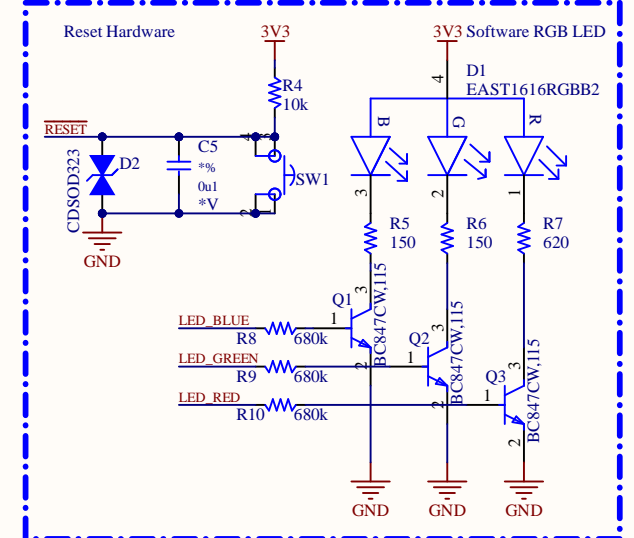
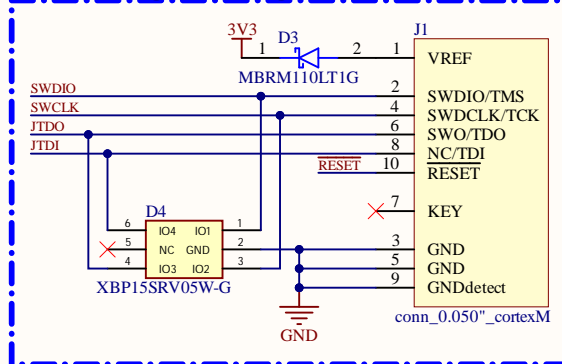
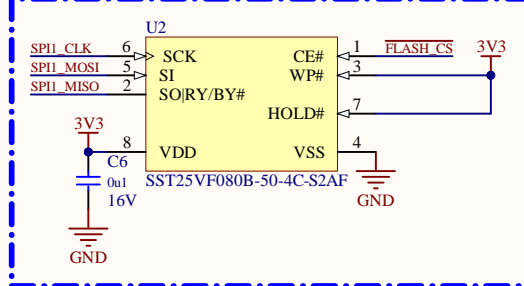
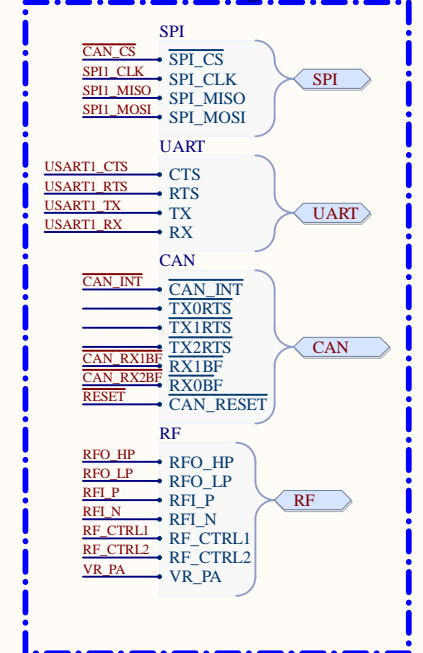
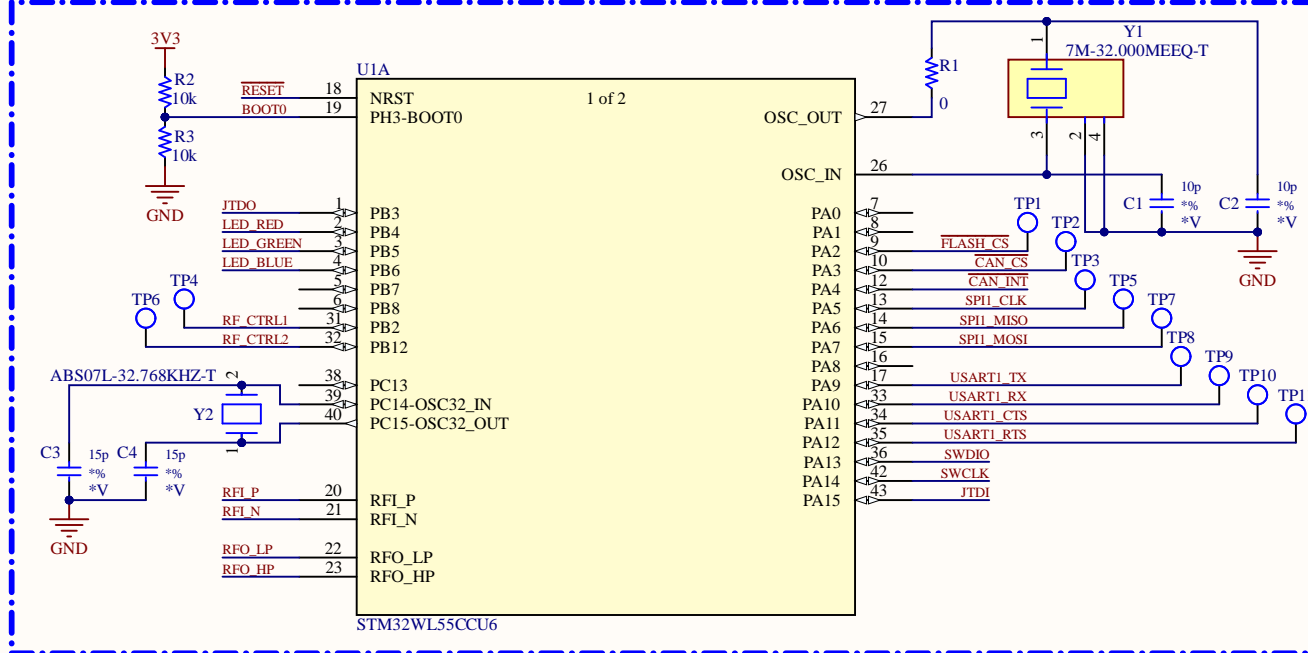


Power Converters



Microcontroller Power





Date	*
File	mcu.SchDoc

**CAN**

- CAN\_INT
- CAN\_TX0RTS
- CAN\_TX1RTS
- CAN\_TX2RTS
- CAN\_RX1BIF
- CAN\_RX0BIF
- CAN\_RESET

**SPI**

- SPI\_CS
- SPI\_CLK
- SPI\_MISO
- SPI\_MOSI

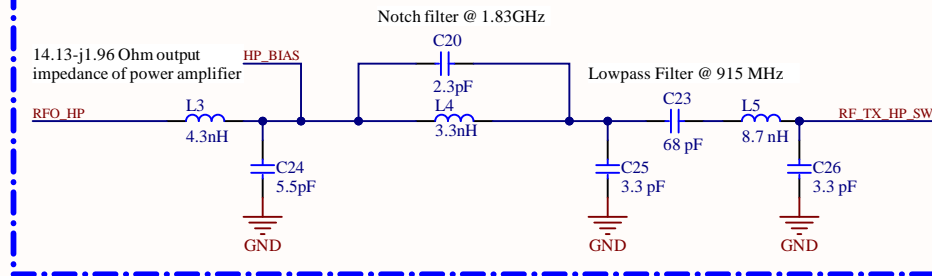
**UART**

- CTS
- RTS
- TX
- RX

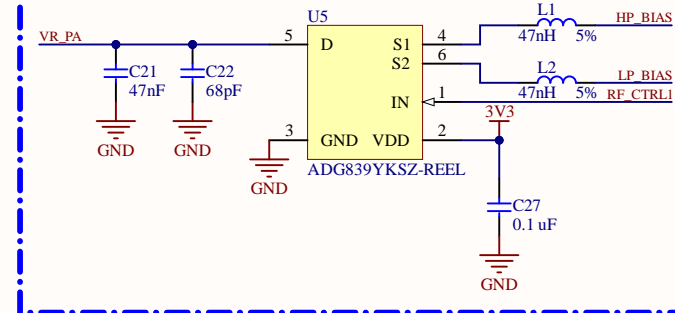
File	Communication.SchDoc
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Refer to ST AN5457 for design guides

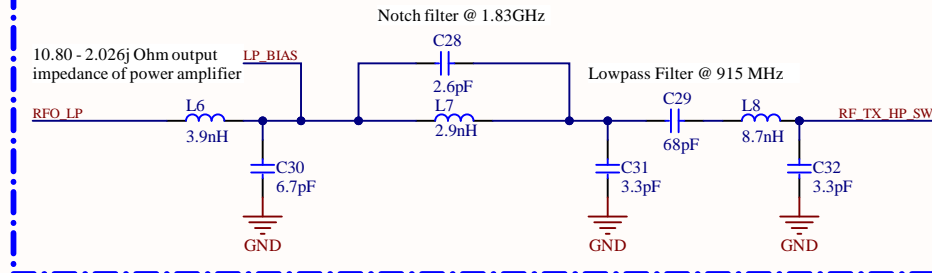
## RF High Power Output Match and Filter



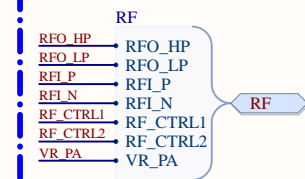
## RF Output Bias Network



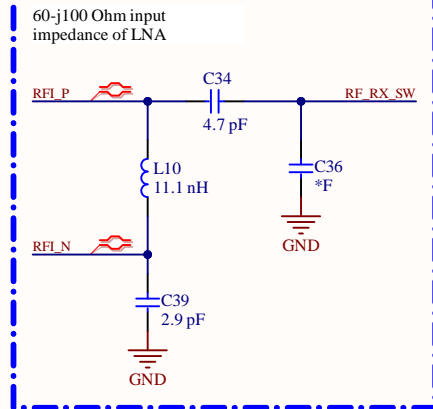
## RF Low Power Output Match and Filter



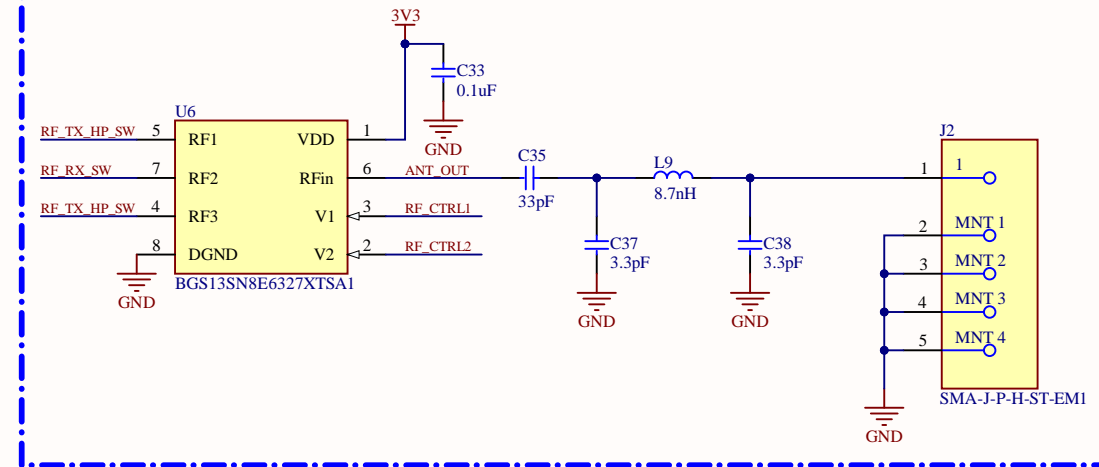
## Off Sheet Connection



## RF Input Matching



## RF Switch and Antenna



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s\Shield\_Black  
@20x.png. File  
does not exist.

Project: Helios\_Telemetry\_Radio.PrjPcb

University of Calgary Solar Car Team

Title: RF Schematic

Size: Sheet 5 of 5

Revision  
6.0

Drawn By: Andy Smit

Date \*

Approved By: \*

File RF.SchDoc