OPCODE	TIPO	MNEMONIC	NOME	OPERAÇÃO		
0000 / 0	R	add	Add	R[ra] = R[ra] + R[rb]		
0001 / 1	I	addi	Add Immediate	R[ra] = R[ra] + Imm		
0010 / 2	I	jal	Jump and Link	R[ra] = PC+1 PC = PC + Imm		
0011 / 3	I	jalr	Jump and Link Register	R[ra] = PC+1 PC = R[ra] + Imm		
0100 / 4	R	brzr	Branch on Zero Register	if (R[ra] == 0) PC = R[rb]		
0101 / 5	R	ge	Greater or Equal	if (R[ra] >= R[rb]) R[ra] = 0 else R[ra] = 1		
0110 / 6	R	sw	Store Word	M[R[rb]] = R[ra]		
0111 / 7	R	lw	Load Word	R[ra] = M[R[rb]]		
1000 / 8	R	sub	Sub	R[ra] = R[ra] - R[rb]		
1001 / 9	R	mul	Multiply	R[ra] = R[ra] * R[rb]		
1010 / 10	R	slr	Shift Left Register	R[ra] = R[ra] << R[rb]		
1011 / 11	R	srr	Shift Right Register	R[ra] = R[ra] >> R[rb]		
1100 / 12	R	not	Not	R[ra] = not R[rb]		
1101 / 13	R	or	Or	R[ra] = R[ra] R[rb]		
1110 / 14	R	and	And	R[ra] = R[ra] && R[rb]		
1111 / 15	R	xor	Xor	R[ra] = R[ra] xor R[rb]		

TIPO R												
Bits	11	10	9	8	7	6	5	4	3	2	1	0
	OPCODE				RS1				RS2			

TIPO I												
Bits	11	10	9	8	7	6	5	4	3	2	1	0
	OPCODE				RS1				IMM			