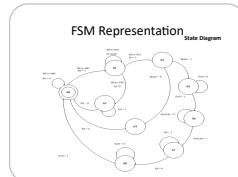


Q Menus



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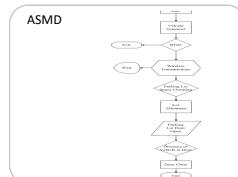


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Algorithm state machine diagram

- Provide flow chart to describe system;
 - Whenever vehicle enter authentication process starts.
 - If it is identified then wireless data transmit to FPGA through RfTx.
 - Then FPGA further process the system to perform lot allotment and gate control system

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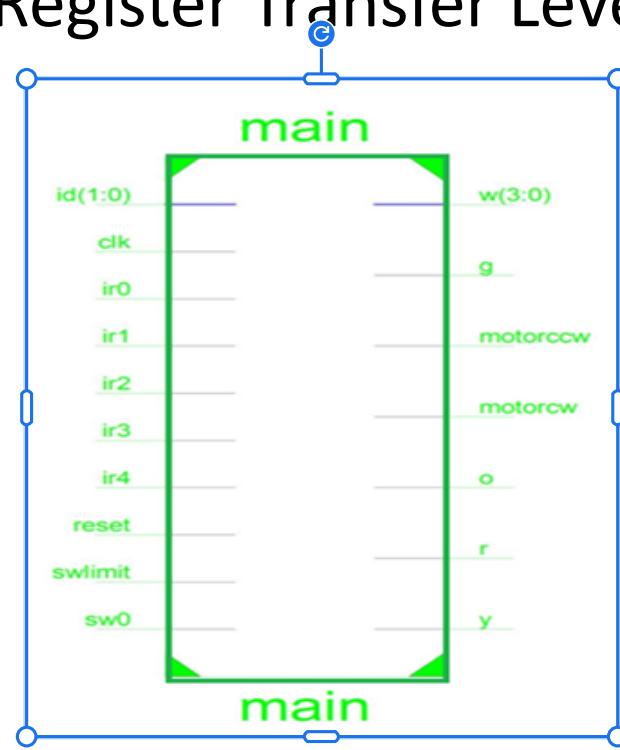
27



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Register Transfer Level (RTL)



Click to add speaker notes



Q