IDENTIFICATION

PRODUCT CODE:

MAINDEC-08-D1MA-D

PRODUCT NAME:

MEMORY ADDRESS TEST

ADDRSLO ADDRSHI

DATE CREATED:

FEBRUARY 11, 1971

MAINTAINER:

DIAGNOSTIC GROUP

AUTHOR:

R. GREEN/B. HANSEN

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- The Memory Address Test checks for proper memory address selection on the PDP-8.
- 2. REQUIREMENTS
- 2.1 <u>Equipment</u>

Standard PDP-8 Computer.

2.2 Storage

The low version occupies locations 0000-0222. The high version occupies locations 7400-7575, 0-3. The binary loader must be stored in the last memory page.

2.3 <u>Preliminary Programs</u>

It is assumed that the only malfunction is in the memory addressing circuits.

- 3. LOADING PROCEDURE
 - The program is supplied in RIM format.
- 4. STARTING PROCEDURE
- 4.1 <u>Control Switch Settings</u>

 SRO Halt after error printout.
- 4.2 Starting Addresses

0004 Low Storage Restart 0000 7400 High Storage

- 4.3 Operator Action
 - a. Load the starting address into the program counter.
 - b. Set the SWITCH REGISTER to 4000, if halt on error is desired.
 - c. Push START.
- 5. OPERATING PROCEDURE

Same as section 4.

6. ERRORS

6.1 Error Printouts

Axxxx Cyyyy (Error printout format)

Axxxx. (Address). xxxx = Address containing the wrong data

Cyyyy. (Contents). yyyy = Contents of location xxxx.

The address should always equal the contents.

6.2 <u>Error Recovery</u>

Analysis of several error printouts should establish a meaningful pattern that will single out a particular address selector card.

If it is necessary to scope the problem, the following two instruction loop may be entered into memory by the operator.

TAD [Bad Location]

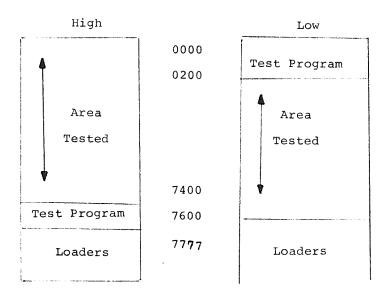
JMP .-1

7. MISCELLANEOUS

7.1 Execution Time

An 11 is printed after every 96 complete program loops (every 28 seconds).

7.2 Memory Maps



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