This Word file contains material for a PYNQ and Vivado xilinx laboratory.

GitHub:<https://github.com/UMONSMicroelectronics>

**Mail ID:** [patelmonalumons@gmail.com](mailto:patelmonalumons@gmail.com)

**Lab.1**

# **Latest version of PYNQ Manual:**

<https://buildmedia.readthedocs.org/media/pdf/pynq/latest/pynq.pdf>

# **PYNQ Board reference Manual:**

<https://reference.digilentinc.com/_media/reference/programmable-logic/pynq-z1/pynq-rm.pdf>

# **Downloadable PYNQ Image:**

<http://www.pynq.io/board.html>

# **PYNQ Tutorial :**

<https://github.com/Xilinx/PYNQ_Workshop>

**Lab.2**

# **Overlay Create Tutorial**

https://pynq.readthedocs.io/en/v2.5.1/overlay\_design\_methodology/overlay\_tutorial.html

https://discuss.pynq.io/t/tutorial-creating-a-hardware-design-for-pynq/145