

A

B

C

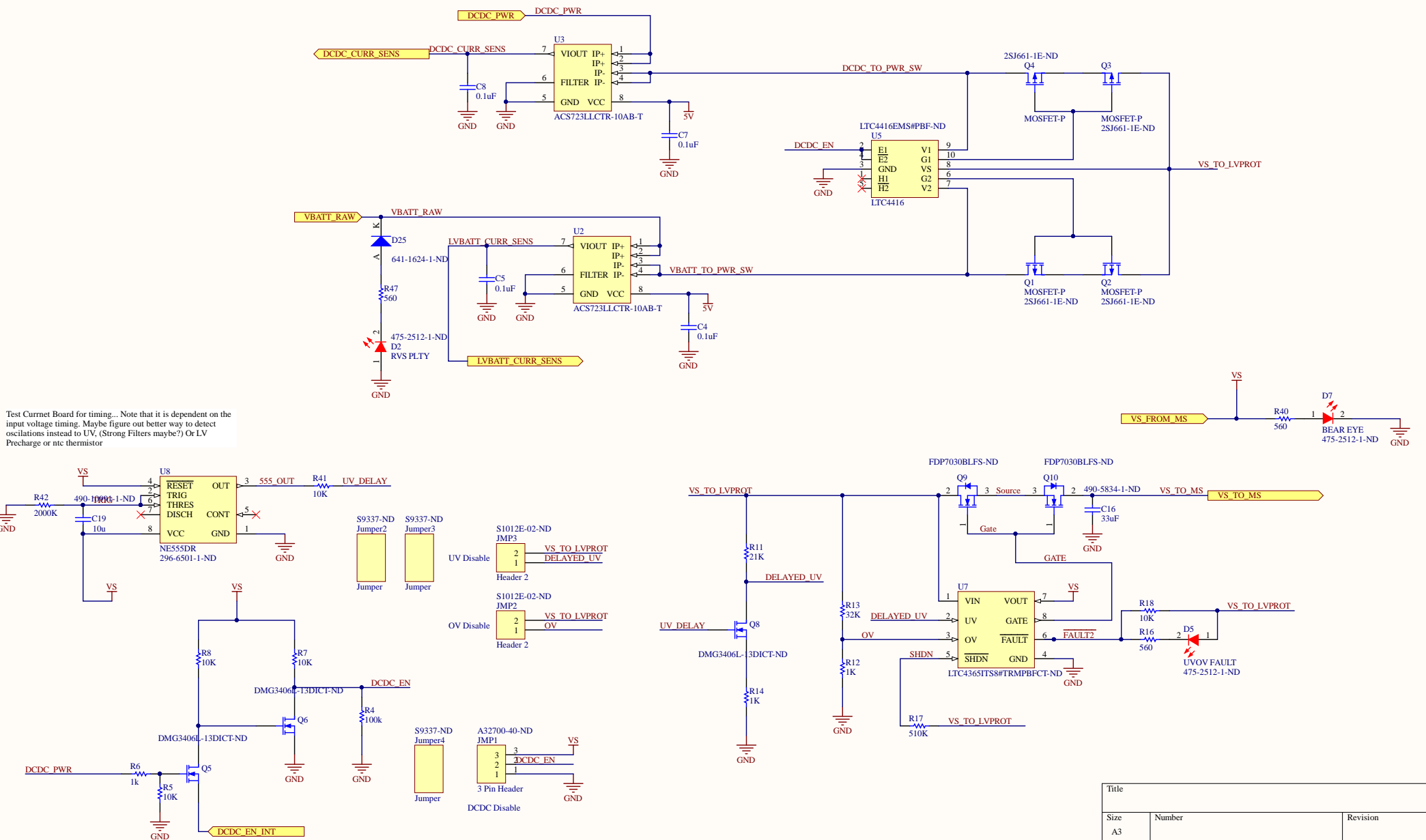
D

A

B

C

D



Test Current Board for timing... Note that it is dependent on the input voltage timing. Maybe figure out better way to detect oscillations instead to UV. (Strong Filters maybe?) Or LV Precharge or ntc thermistor

Title		
Size	Number	Revision
A3		
Date:	2019-10-04	Sheet of
File:	C:\Users\...12 - Power Protection.SchDoc Drawn By:	

1

2

3

4

A

A

B

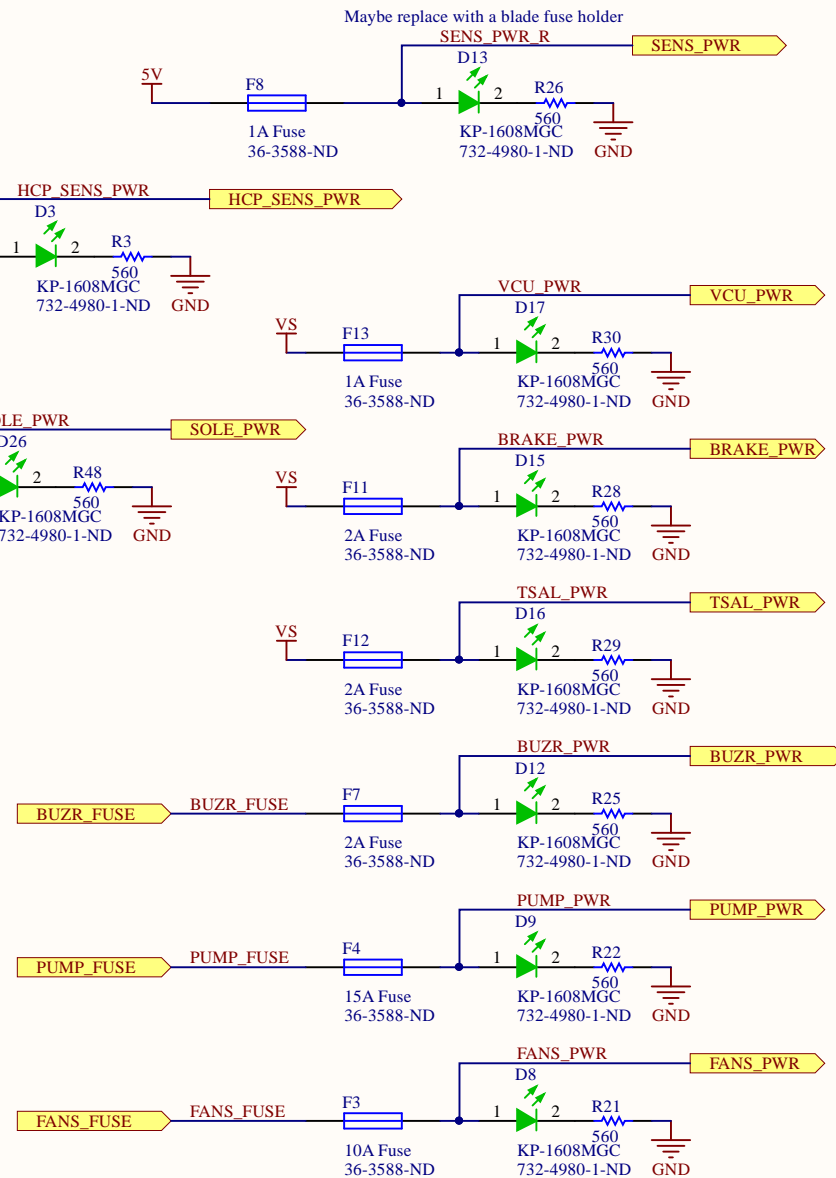
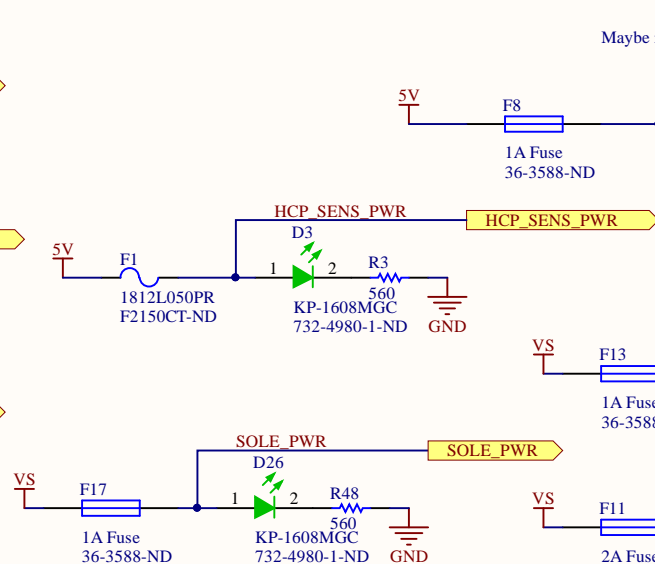
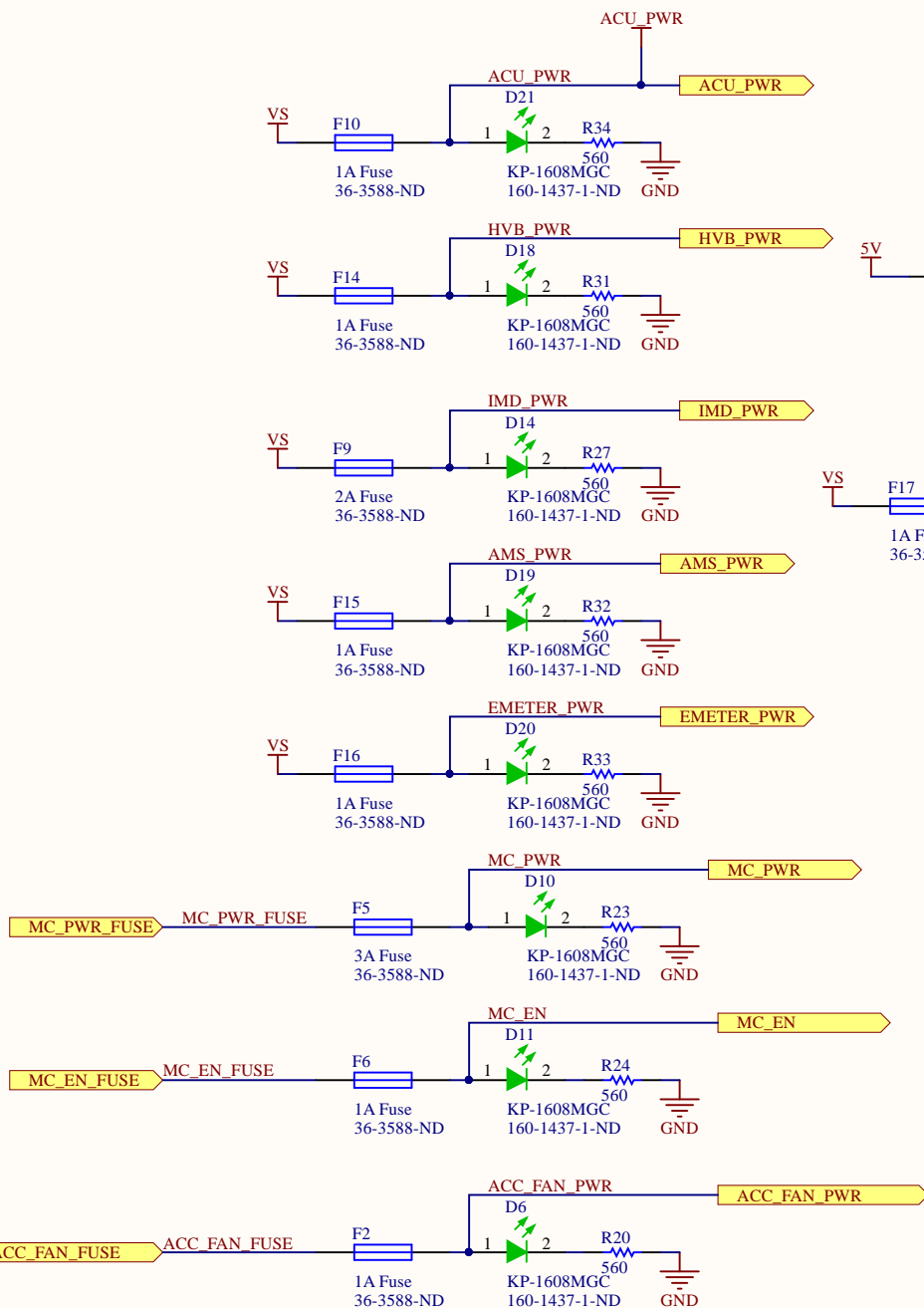
B

C

C

D

D



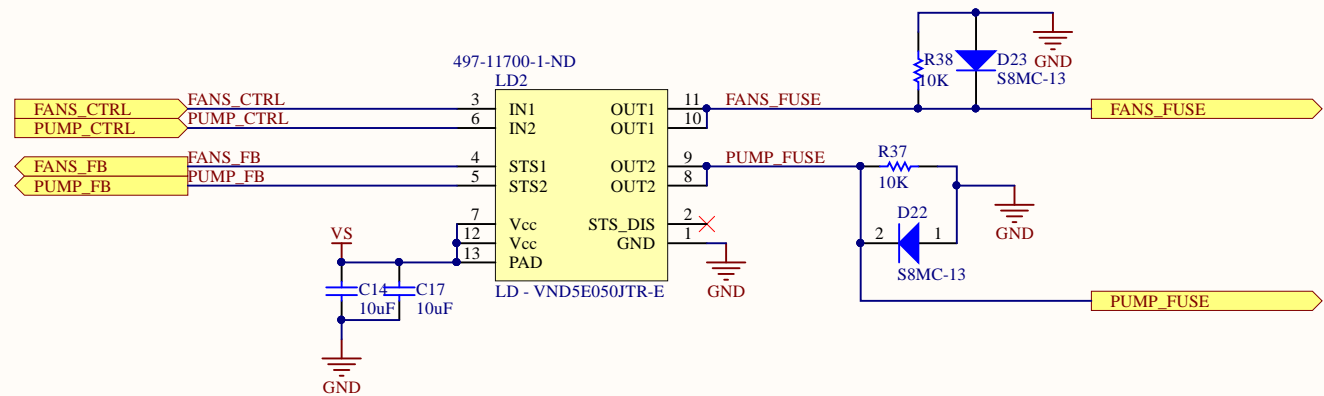
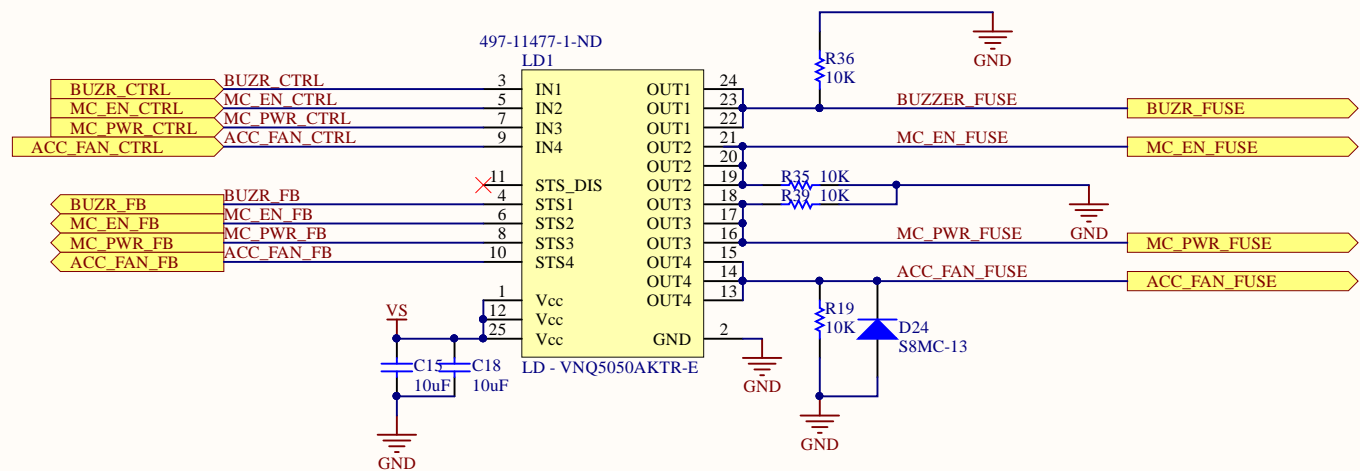
Title		
Size	Number	Revision
A4		
Date:	2019-10-04	Sheet of
File:	C:\Users\...\3 - Fuses.SchDoc	Drawn By:

1

2

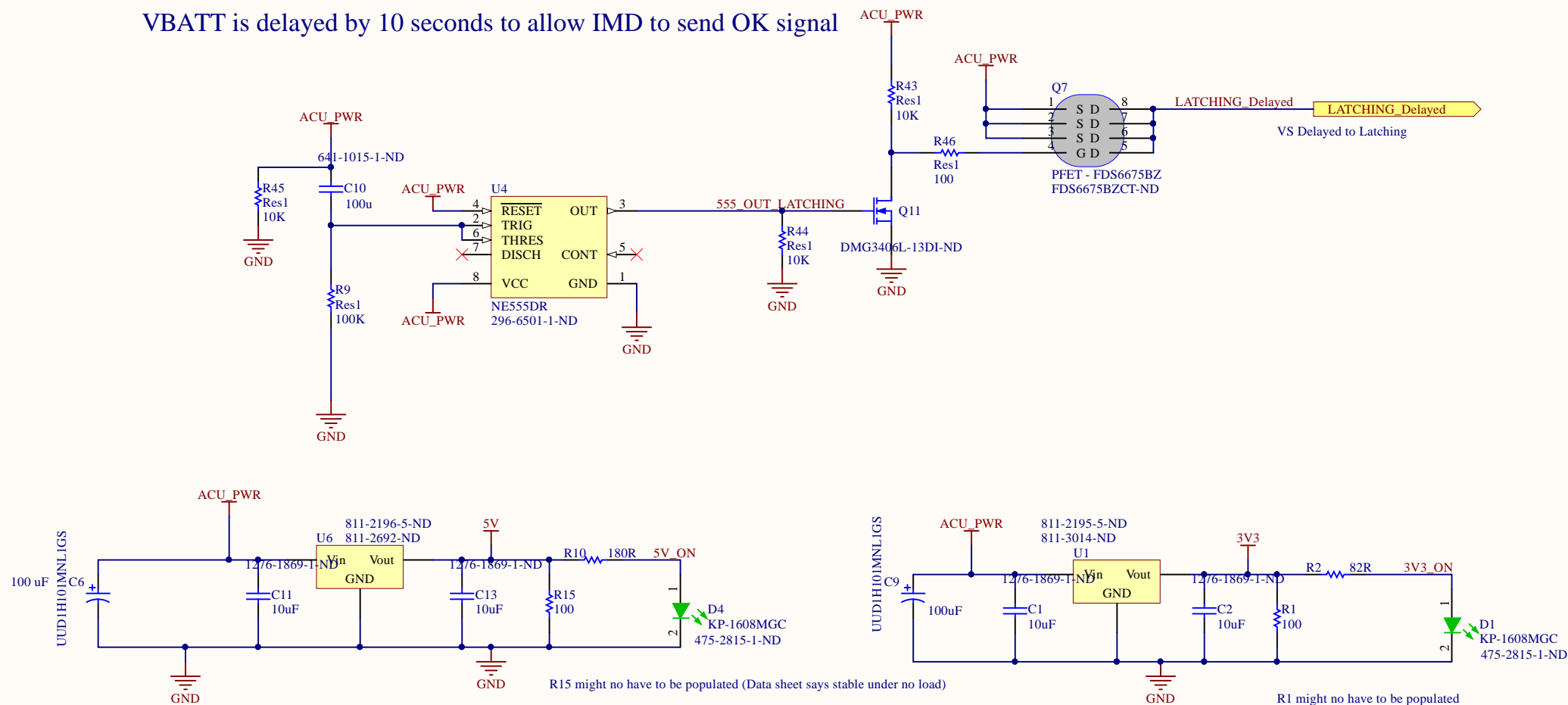
3

4

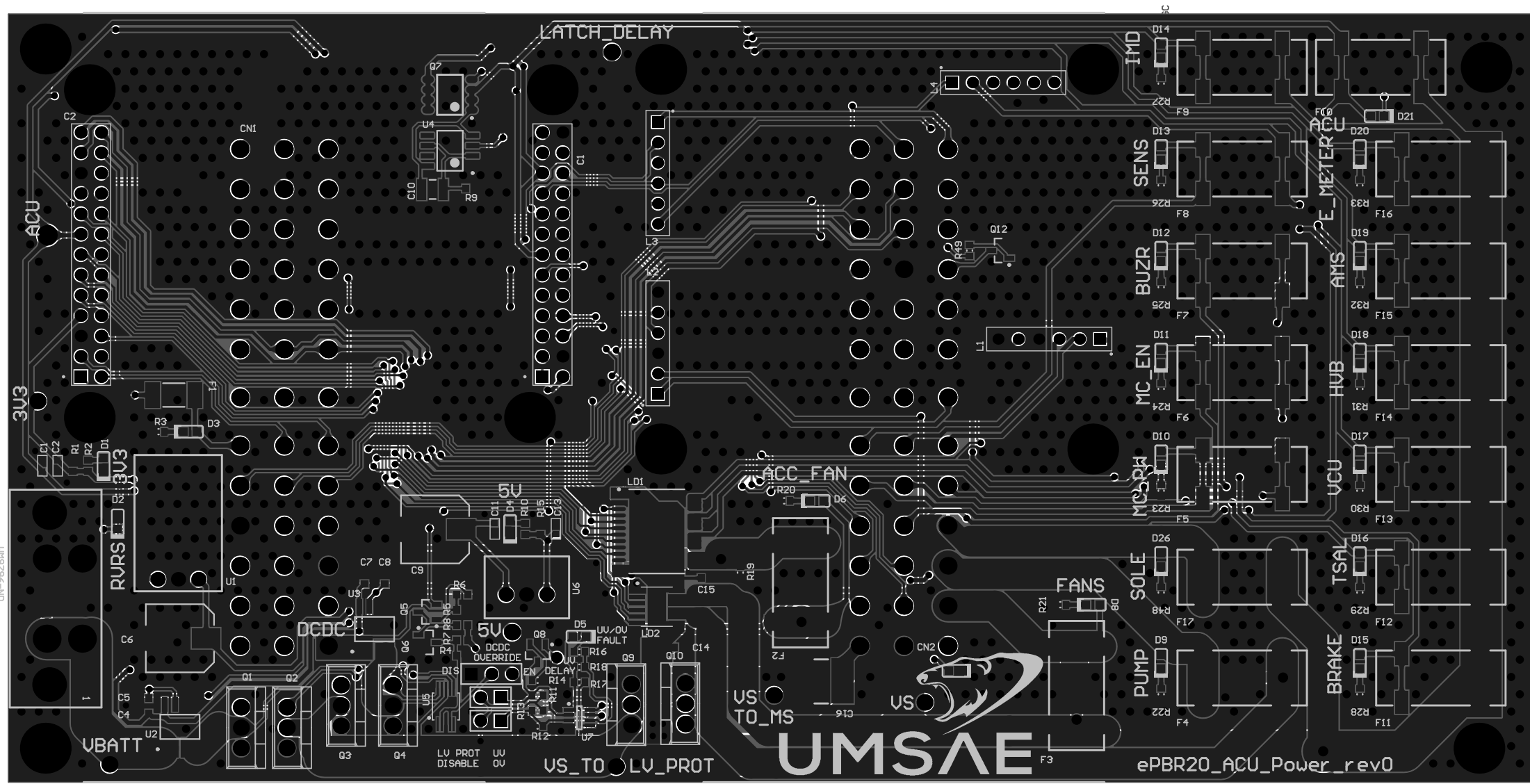


Title		
Size A4	Number	Revision
Date: 2019-10-04	Sheet of	
File: C:\Users\...\6 - Load Drivers.SchDoc	Drawn By:	

VBATT is delayed by 10 seconds to allow IMD to send OK signal



Title		
Size	Number	Revision
A4		
Date:	2019-10-04	Sheet of
File:	C:\Users\...8 - Voltage Conversions Delayed Send By:	



UMSAE

ePBR20_ACU_Power_rev0

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