

DevLab Development Board User Guide and Technical Reference

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Department of Research, Innovation, and Development

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The DevLab Development Board based on the PY32F003L24D6TR microcontroller is designed for rapid prototyping, embedded systems education, IoT experimentation, and wearable devices. This board combines flexible power options, modern connectivity, and accessible interfaces to accelerate your hardware development. The microcontroller features a 32-bit ARM Cortex-M0 core, up to 24 MHz clock speed, 16KB Flash memory, and 2KB SRAM, making it suitable for a wide range of applications. With built-in peripherals like SPI, I2C, UART, and a 12-bit ADC, the board supports diverse project requirements.

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**CHAPTER
TWO**

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INTRODUCTION

The DevLab Development Board, powered by the PY32F003L24D6TR microcontroller, provides an efficient and compact platform for rapid prototyping, embedded systems education, IoT experimentation, and wearable technology development. Its design emphasizes flexibility and simplicity, integrating essential features that allow developers to focus on innovation rather than complex setups. The board operates around a 32-bit ARM® Cortex®-M0 core running up to 24 MHz, with 16 KB of Flash and 2 KB of SRAM, offering an ideal balance between processing performance and memory efficiency. This configuration enables smooth execution of control algorithms, communication routines, and sensor data processing across a wide range of embedded applications.

To complement its processing capability, the DevLab board includes a rich set of onboard peripherals and hardware conveniences. Interfaces such as SPI, I²C, and UART simplify communication with external modules and sensors, while the 12-bit ADC and dual comparators support precise analog signal acquisition. The integrated 5 V → 3.3 V voltage regulator, 4-pin JST 1.0 mm connector for programming and I²C communication, reset button, and status LED on PB5 streamline both debugging and prototyping. Furthermore, the board maintains level-shifter compatibility between 3.3 V and 5 V logic, ensuring seamless operation with a broad ecosystem of peripherals. Together, these features make the DevLab PY32F003 board a practical and reliable tool for students, makers, and engineers aiming to explore low-power, high-efficiency embedded systems.

3.1 Key Features

- **ARM® Cortex®-M0 Core:** 32-bit RISC processor running up to 24 MHz, optimized for real-time control and low-power operation.
- **Compact and Efficient Design:** Designed for embedded and IoT applications requiring small form factor and low energy consumption.
- **Wide Operating Voltage:** Fully functional from 1.7 V to 5.5 V, suitable for battery or USB-powered sys-
- tems.
- **Onboard Voltage Regulator:** Integrated 5 V → 3.3 V LDO regulator, ensuring stable MCU operation from USB or external 5 V supply.
- **Communication Interface:** 4-pin JST 1.0 mm connector for programming and I²C communication, compatible with standard DevLab and Qwiic wiring schemes.
- **User Interface Components:**
 - Reset Button: Allows manual restart of the microcontroller.
 - Status LED (PB5): Blinks for power or firmware activity indication.
- **Analog and Timing Peripherals:**
 - 12-bit ADC (4 external + 2 internal channels)
 - 2 analog comparators
 - 1 advanced and 4 general-purpose 16-bit timers
 - Low-power timer, SysTick, and dual watchdogs
- **Communication Peripherals:** 1 × SPI, 1 × I²C, 1 × USART — supporting up to 1 MHz I²C, 18 MHz SPI, and LIN/IrDA UART modes.
- **Direct Memory Access (DMA):** 3 independent channels for high-speed peripheral data handling.
- **Real-Time Clock (RTC):** Built-in calendar and alarm support with low-power retention.
- **GPIO and Expansion:** Up to 7 multipurpose I/O pins with alternate function mapping.
- **Level Shifter Compatibility:** Fully supports logic level interfacing between 3.3 V and 5 V systems, ensuring cross-platform signal safety.
- **Operating Environment:** -40 °C to +85 °C industrial temperature range.
- **DevLab format compatibility.**

The DevLab format is a standardized hardware layout that exposes the majority of the microcontroller or integrated

circuit functional pins, including connections for external sensors, converters, and other peripheral devices. It provides direct access to I²C, SPI, UART, and additional communication interfaces for flexible hardware interaction.

3.2 Hardware Electrics characteristics

Table 3.1: Electrical characteristics

Pa- ram- eter	Description	Min	Typ	Max	Unit
Vin	Input voltage to power on the module	2.5	—	5.5	V
Ivcc	Current flowing into VCC pin of microcontroller	—	—	100	mA
I3v3*	Maximum current of 3V3 regulator	—	—	600	mA
Iio	Maximum Input/Output current of IOs	—	—	20	mA
f_ext	User external clock frequency	—	8	32	MHz
Vih	Input high level voltage	0.7V	—	—	V
Vil	Input low level voltage	—	—	0.3V	V
Vol	Output low level voltage	—	—	0.4	V
Voh	Output high level voltage	VCC	—	—	V
			0.4		

3.3 Software Support

- **Arduino IDE**
- **Py32f0xx-hal/ VS Code:** <https://github.com/UNIT-Electronics-MX/py32f0xx-hal> — Supports development with automated builds, serial monitoring, and advanced debugging projects.

3.3.1 Py32f0xx-hal

Hardware Abstraction Layer (HAL) for PY32F0xx microcontrollers in Rust

What is this?

This crate provides a Hardware Abstraction Layer (HAL) for the PY32F0xx family of microcontrollers from Puya Semiconductor. These are low-cost ARM Cortex-M0+ based MCUs that offer an excellent alternative to STM32F0xx series, perfect for:

3.4 Applications

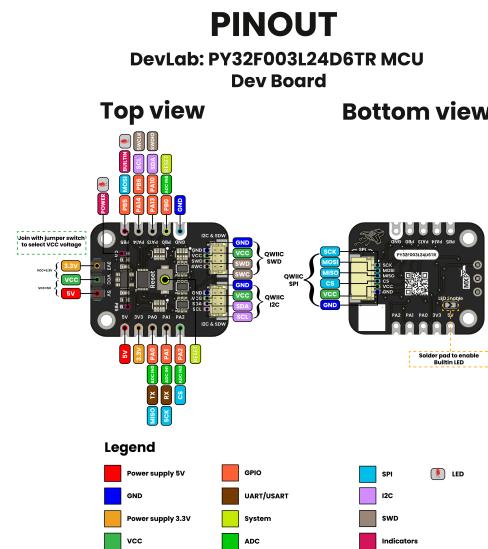
- **Consumer Electronics:** Smart home devices, small appliances, and battery-powered gadgets.
- **Industrial Control:** Sensor interfaces, automation modules, power monitoring, and fan controllers.
- **IoT Devices:** Environmental data loggers, wireless bridge modules, smart meters, and wearable submodules.
- **Lighting Systems:** RGB/NeoPixel LED controllers, PWM dimmers, and sound-reactive lighting.
- **Education & Prototyping:** Training kits, MCU programming practice, and portable debugging tools.
- **Interface Control:** I²C/SPI bridges, input devices, touch keys, and DAC-based tone generators.

PY32F0xx Hardware Abstraction Layer (HAL) documentation

HARDWARE PINOUT

This section describes the pin mapping of the DevLab Development Board based on the PY32F003L24T6 microcontroller.

4.1 Pinout Diagram



The image above shows the physical pinout of the DevLab board and its relationship with the PY32F003L24T6 MCU pins.

4.2 Board Pin Description

Table 4.1: DevLab Pinout Overview

Pin Label	Description
VCC	Power input
GND	Ground
PA0	USART2_TX / SPI1_MISO
PA1	USART2_RX / SPI1_SCK
PA2	ADC_IN2 / SPI_CS
PB0 / PF2	GPIO / NRST
PB5	Built-in LED / GPIO / SPI_MOSI
PA13	SWDIO / I2C_SDA
PA14	SWCLK / I2C_SCL
PB6	GPIO / I2C_SCL (alternate)
PA10	GPIO / I2C_SDA (alternate)

4.3 Connectivity Interfaces

Table 4.2: Available Interfaces

Interface	Description
I2C	JST 1.0 mm QWIIC connector (Power + I2C)
SPI	JST 1.0 mm connector (Power + SPI)
GPIO	Two 4-pin headers for general-purpose I/O
SWD	Dedicated programming and debugging interface

MULTIPLEXING FUNCTION MAPPING

5.1 GPIO Alternate Function Registers

Pin multiplexing is controlled by the GPIO Alternate Function Registers (AFR). Each pin uses 4 bits to select the alternate function.

5.1.1 AFR Bit Mapping

- Bits 0–3 : Pin 0
- Bits 4–7 : Pin 1
- Bits 8–11 : Pin 2
- Bits 12–15 : Pin 3
- Bits 16–19 : Pin 4
- Bits 20–23 : Pin 5
- Bits 24–27 : Pin 6
- Bits 28–31 : Pin 7

AF values:

- 0 → GPIO (default)
- 1 → AF1
- ...
- 15 → AF15

5.2 Configuration Example

Configure PA0 as USART2_TX (AF9):

```
GPIOA->AFR = (GPIOA->AFR & ~(0xF << 0)) |  
    (9 << 0);
```

5.3 Design Considerations

- PA13 / PA14 should remain reserved for SWD during development.
- I2C is recommended on PB6 / PA10 for applications.
- The onboard LED on PB5 shares SPI_MOSI.

5.4 Configuration Example

Configure PA0 as USART2_TX (AF9):

```
GPIOA->AFR = (GPIOA->AFR & ~(0xF << 0)) |  
    (9 << 0);
```

5.5 Design Considerations

- PA13 / PA14 should remain reserved for SWD during development.
- I2C is recommended on PB6 / PA10 for applications.
- The onboard LED on PB5 shares SPI_MOSI.

5.6 PA0 – Alternate Function Mapping

AF	Function
AF1	USART1_CTS
AF4	USART2_CTS
AF7	COMP1_OUT
AF9	USART2_TX
AF10	SPI1_MISO
AF13	TIM1_CH3
AF14	TIM1_CH1N
AF15	IR_OUT

5.7 PA1 – Alternate Function Mapping

AF	Function
AF0	SPI1_SCK
AF1	USART1_RTS
AF4	USART2_RTS
AF7	EVENTOUT
AF9	USART2_RX
AF10	SPI1_MOSI
AF13	TIM1_CH4
AF14	TIM1_CH2N
AF15	MCO

5.8 PA2 – Alternate Function Mapping

AF	Function
AF0	SPI1_MOSI
AF1	USART1_TX
AF4	USART2_TX
AF7	COMP2_OUT
AF10	SPI1_SCK
AF12	I2C_SDA
AF13	TIM3_CH1

5.9 PB0 – Alternate Function Mapping

AF	Function
AF0	SPI1_NSS
AF1	TIM3_CH3
AF2	TIM1_CH2N
AF5	EVENTOUT
AF7	COMP1_OUT

5.10 PB5 – Alternate Function Mapping

AF	Function
AF0	SPI1_MOSI
AF1	TIM3_CH2
AF2	TIM16_BKIN
AF3	USART1_CK
AF4	USART2_CK
AF5	LPTIM_IN1
AF7	COMP1_OUT

5.11 PA13 – Alternate Function Mapping

AF	Function
AF0	SWDIO
AF1	IR_OUT
AF7	EVENTOUT
AF9	USART1_RX
AF10	SPI1_MISO
AF13	TIM1_CH2
AF15	MCO

5.12 PA14 – Alternate Function Mapping

AF	Function
AF0	SWCLK
AF1	USART1_TX
AF4	USART2_TX
AF7	EVENTOUT
AF15	MCO

5.13 PB6 – Alternate Function Mapping

AF	Function
AF0	USART1_TX
AF1	TIM1_CH3
AF2	TIM16_CH1N
AF4	USART2_TX
AF5	LPTIM_ETR
AF6	I2C_SCL
AF7	EVENTOUT

5.14 PA10 – Alternate Function Mapping

AF	Function
AF1	USART1_RX
AF2	TIM1_CH3
AF4	USART2_RX
AF6	I2C_SDA
AF7	EVENTOUT
AF9	USART1_TX
AF10	SPI1_NSS
AF12	I2C_SCL

OPENOCD AND PYOCD OVERVIEW

6.1 Introduction

6.1.1 OpenOCD (Open On-Chip Debugger)

OpenOCD is an open-source software tool that facilitates debugging, in-system programming, and boundary-scan testing of embedded devices. It supports a diverse range of microcontrollers including STM32, RP2040, and PY32. Through interfaces such as JTAG or SWD, OpenOCD provides a command-line interface for effective interaction with the target devices.

6.1.2 PyOCD: A Python Interface to OpenOCD

PyOCD simplifies the use of OpenOCD by providing a Python-based high-level interface. It offers a Python API that abstracts the complexities of the OpenOCD command-line interface, making it easier to program and debug microcontrollers. PyOCD is user-friendly, flexible, and designed to be extensible for various microcontroller interactions.

Tool	Functionality
OpenOCD	Provides a command-line interface for debugging and programming microcontrollers.
PyOCD	Offers a Python API for seamless interaction with microcontrollers via OpenOCD.

6.2 ARM Cortex-M Debug Capabilities

6.2.1 Debugging Interface of ARM Cortex-M

The ARM Cortex-M architecture includes a comprehensive debug interface that allows external tools to access the microcontroller core for debugging and programming tasks. This interface includes a suite of registers and commands that empower debuggers to halt the core, access memory, and manage the execution of programs.

6.2.2 Connection Through Debug Pins

Access to the debug interface is facilitated through specific pins on the microcontroller, such as SWDIO, SWCLK, and nRESET. These are linked to a debug adapter like the ST-Link or CMSIS-DAP, which forms the physical bridge between the host computer and the target device.

6.3 CMSIS-DAP: A Standardized Debug Adapter

6.3.1 Overview of CMSIS-DAP

CMSIS-DAP (Cortex Microcontroller Software Interface Standard - Debug Access Port) serves as a standardized debug adapter for ARM Cortex-M devices. It is endorsed by OpenOCD and provides a cost-effective solution compared to proprietary debug adapters.

6.4 SWD Communication Protocols

6.4.1 Signals and Operations

The SWDIO (Serial Wire Debug Input/Output) and SWCLK (Serial Wire Clock) signals are integral for communication over the SWD interface. SWDIO is a bidirectional line used for transmitting debug commands and data, while SWCLK is a clock signal that ensures synchronized data transfer between the host and the target.

6.4.2 Advantages of SWD Over JTAG

Utilizing a two-wire connection, the SWD interface minimizes the pin count required compared to the traditional JTAG interface. This reduction is particularly beneficial for microcontrollers where pin availability is limited.

SUPPORTED BASIC ARDUINO FUNCTIONS

Packages the basic functions of the Arduino IDE, allowing you to program the DevLab Development Board using the familiar Arduino environment. This includes digital and analog I/O operations, PWM control, serial communication, and more.

```
https://raw.githubusercontent.com/UNIT-  
-Electronics-MX/unit_electronics_py32_  
-arduino_package/main/package_unit_  
-electronics_py32_index.json
```

Learn how to program the **DevLab: PY32F003L24D6TR** development board using **Arduino IDE** and the **CH552 Multi-Protocol Programmer** in **CMSIS-DAP mode**.

- Rapid prototyping
- Embedded systems education
- Wearable device development

These resources are sufficient for a wide range of control, signal acquisition, and communication applications.

7.1 Materials Required

To program the **DevLab: PY32F003L24D6TR** using Arduino IDE, you will need:

- USB Type-C cable
- JST 1.0 mm cable (4-pin)
- Arduino IDE installed on your computer
- DevLab: PY32F003L24D6TR board
- DevLab: CH552 Multi-Protocol Programmer

7.2 Required Installations

This tutorial uses the **pyOCD Debugger**, therefore the following must be installed:

- Python (installed on your system)
- pyOCD (Python package)

7.3 CMSIS-DAP Mode

The programmer must operate in **CMSIS-DAP mode**.

The following repository contains all available firmware images for the Multi-Protocol Programmer. For this tutorial, you only need:

```
cmsis_dap.bin
```

7.3.1 Firmware – CMSIS-DAP

Warning: Before connecting the programmer, make sure the device is powered with **+5 V**. Use the onboard voltage selector switch to choose the correct supply level.

7.4 Flashing the Programmer

Use **WCHISPStudio** to flash the file `cmsis_dap.bin` to the Multi-Protocol Programmer.

Wait until the update process is complete before using the programmer in **CMSIS-DAP mode**.

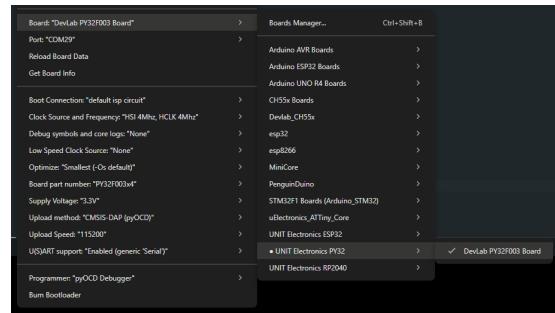
Refer to the official tutorial for more details:

```
WCHISPStudio Tutorial
```

7.5 Additional Boards Manager (Arduino IDE)

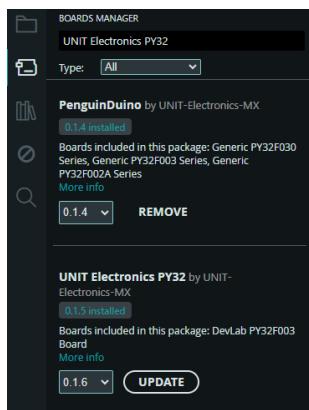
1. Open Arduino IDE
2. Go to File > Preferences
3. In Additional Boards Manager URLs, paste the following URL:

```
https://raw.githubusercontent.com/UNIT-Electronics-MX/unit_electronics_py32_arduino_package/main/package_unit_electronics_py32_index.json
```



7.6 Board Manager

1. Open Boards Manager
2. Search for “UNIT Electronics PY32”
3. Install the package



7.7 Selecting the Board

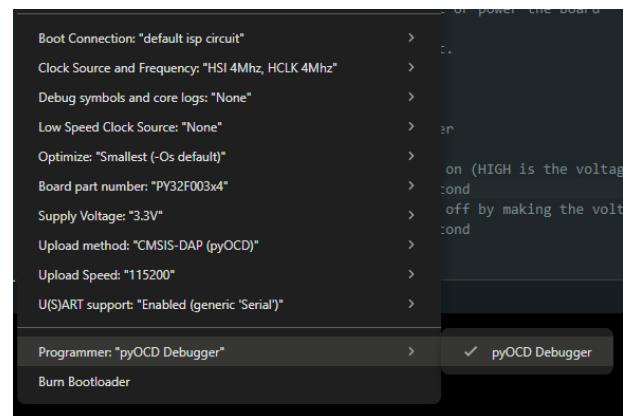
In Arduino IDE, select:

```
Tools > Board > UNIT DevLab: PY32F003
```

7.8 Programmer Selection

Select the programmer:

```
Tools > Programmer > pyOCD Debugger
```



7.9 Connections

With the Multi-Protocol Programmer operating in **CMSIS-DAP mode** and the DevLab board already selected in **Arduino IDE**, follow the steps below.

7.9.1 Step-by-step connection

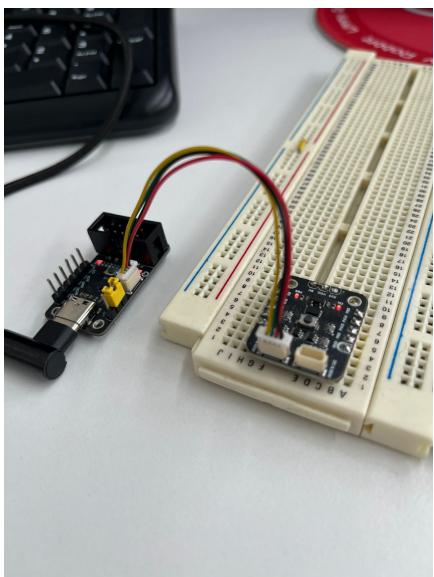
1. Connect the **CH552 Multi-Protocol Programmer** to your computer using a USB cable.
2. Using a **JST 1.0 mm (4-pin) cable**, connect the programmer to the **DevLab: PY32F003L24D6TR** board.
3. Verify that the SWD signals are correctly aligned between both devices.

7.9.2 Required pin alignment

The following signals **must match exactly** between the programmer and the board:

- **VCC** – Target power
- **GND** – Ground reference
- **SWDIO** – SWD data line
- **SWCLK** – SWD clock line

7.9.3 Connection reference



```
void setup() {
    // Initialize digital pin LED_BUILTIN as
    // an output
    pinMode(LED_BUILTIN, OUTPUT);
}

void loop() {
    digitalWrite(LED_BUILTIN, HIGH);   // LED
    // ON
    delay(500);
    digitalWrite(LED_BUILTIN, LOW);    // LED
    // OFF
    delay(500);
}
```

7.9.4 Result

Once the connections are completed and verified, the **De-vLab: PY32F003L24D6TR** is correctly connected to the **CH552 Multi-Protocol Programmer** and ready to be programmed using **Arduino IDE** via **CMSIS-DAP**.

7.10 Example Code (Blink)

Compile and upload the following example to blink the onboard LED.

Note: It is not necessary to select a COM port, since programming is performed via **CMSIS-DAP**.

RUST HAL FOR PY32F0XX MICROCONTROLLERS

Rust HAL is developed to provide a safe and efficient interface for programming PY32F0xx microcontrollers using the Rust programming language. It abstracts the low-level hardware details, allowing developers to write high-level code while still having access to the underlying hardware features.

- [Rust HAL for PY32F0xx microcontrollers](#)
- [Documentation](#)

3. **Build and Flash:** Use Cargo to build your project and flash it to the PY32F0xx microcontroller using a compatible programmer.
4. **Explore Examples:** Check out the example projects provided in the repository to understand how to use various peripherals and features of the microcontroller.

8.1 Why Rust HAL?

- **Safety:** Rust's ownership model and type system help prevent common programming errors, such as null pointer dereferencing and data races, making embedded development more reliable.
- **Performance:** Rust is designed for performance, allowing developers to write low-level code that runs efficiently on resource-constrained microcontrollers.
- **Abstraction:** The HAL provides a high-level interface to interact with the microcontroller's peripherals, making it easier to develop applications without needing to manage hardware registers directly.
- **Community Support:** The Rust embedded ecosystem is growing, with active community contributing to libraries, tools, and documentation.

8.2 Getting Started

To get started with Rust HAL for PY32F0xx microcontrollers, follow these steps:

1. **Set Up Rust Toolchain:** Install Rust and the necessary toolchains for embedded development.
2. **Clone the Repository:** Clone the Rust HAL repository from GitHub.

```
git clone
```


HOW TO GENERATE AN ERROR REPORT

This guide explains how to generate an error report using GitHub repositories.

9.1 Steps to Create an Error Report

1. Access the GitHub Repository

Navigate to the [GitHub](#) repository where the project is hosted.

2. Open the Issues Tab

Click on the “Issues” tab located in the repository menu.

3. Create a New Issue

- Click the “New Issue” button.
- Provide a clear and concise title for the issue.
- Add a detailed description, including relevant information such as:
 - Steps to reproduce the error.
 - Expected and actual results.
 - Any related logs, screenshots, or files.

4. Submit the Issue

Once the form is complete, click the “Submit” button.

9.2 Review and Follow-Up

The development team or maintainers will review the issue and take appropriate action to address it.