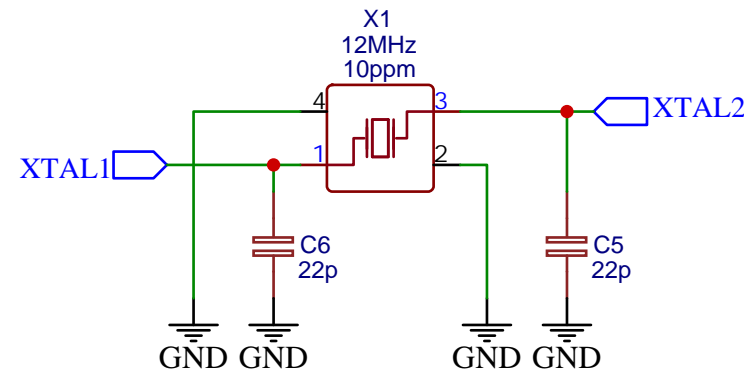
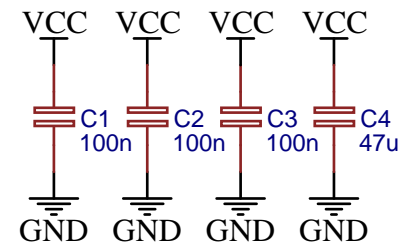
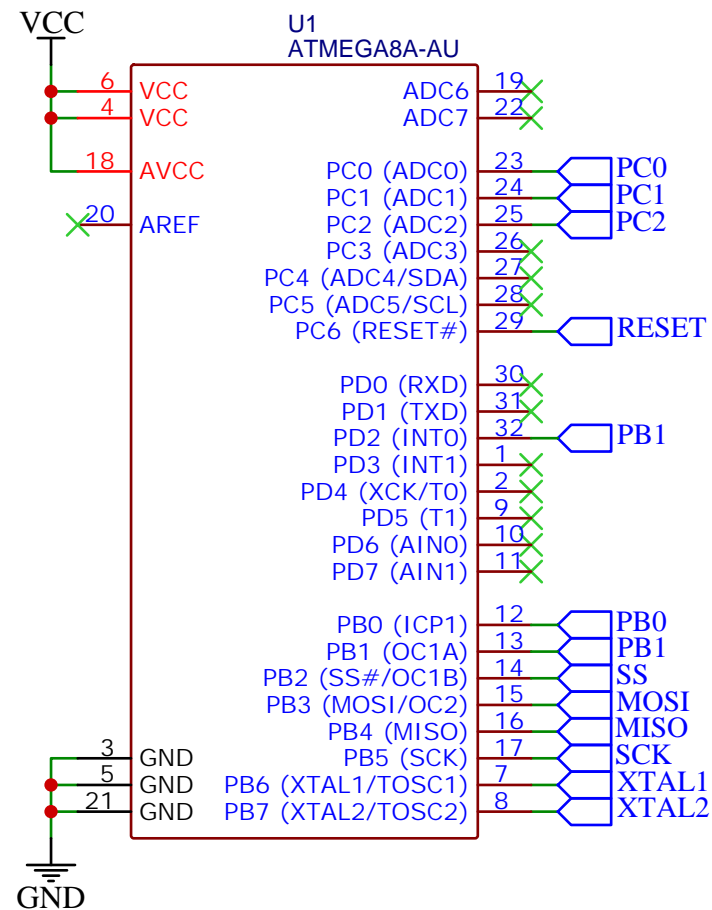
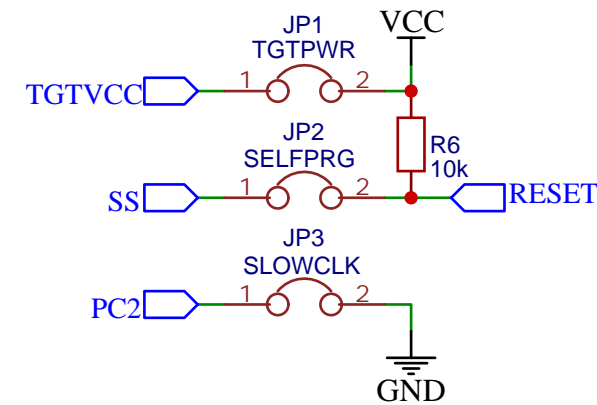


## Microcontroller

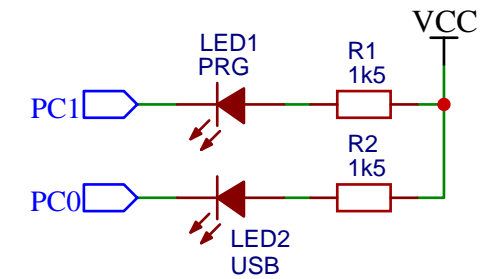


**Important: Use a precise crystal !!!**

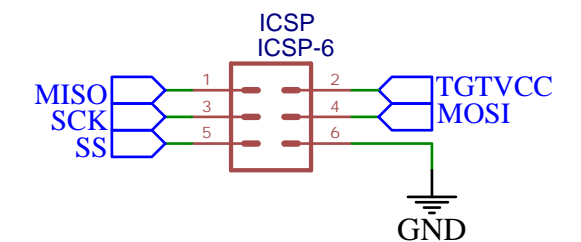
## Jumper



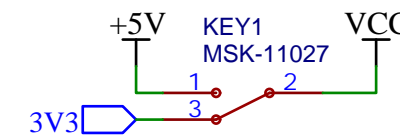
## Indicator LEDs



## Programming Header

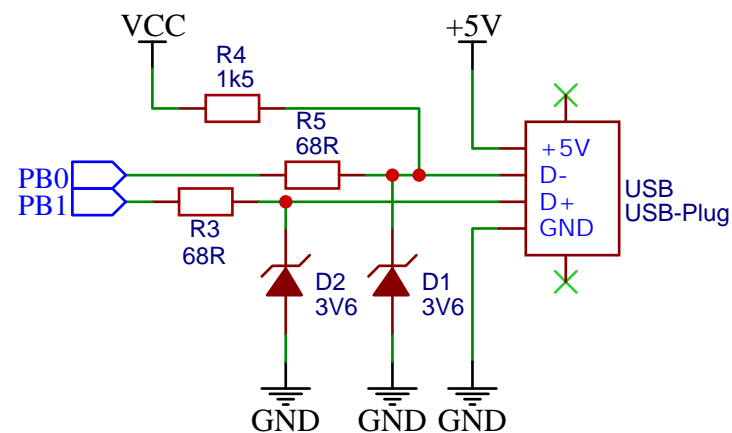


## Power Selection Switch

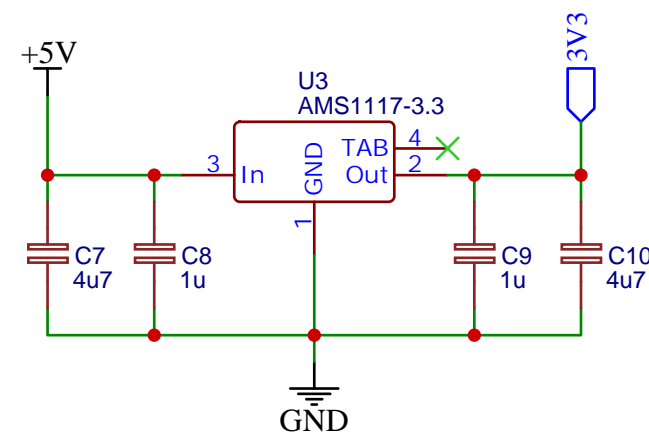


- TGT VCC - Close to power target device via ISP header.
- SELF PRG - Close to upload firmware to USBasp itself via ISP header.
- SLOW CLK - Close to upload firmware to target device with slow speed.  
(necessary for factory-new microcontrollers)

## V-USB Interface



## Voltage Regulator



Based on the design by Thomas Fischl.

TITLE: USBASP - AVR ISP Programmer		REV: 1.0
EasyEDA	Company: waginator	Sheet: 1/1
	Date: 2020-07-24	Drawn By: Stefan Wagner