CSC 411

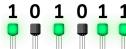
Computer Organization (Spring 2024) Lecture 3: Bitwise Operations

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Moore's Law – The number of transistors on integrated circuit chips (1971-2018) OurWorld in Data Moore's law describes the empirical regularity that the number of transistors on integrated circuits doubles approx This advancement is important as other aspects of technological progress – such as processing speed or the price of electronic products – are inked to Moore's law 50.000.000.000 10.000.000.000 5,000,000,000 1,000,000,000 500,000,000 100,000,000 50.000.000 10,000,000 5,000,000 100 000 he data visualization is available at OurWorldinData.org. There you find more visualizations and research on this topic Licensed under CC-BY-SA by the author Max Ros

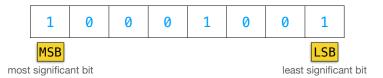
Bits, bytes, and computers

- A bit is the smallest unit of data in computing, representing a binary digit
 - can have a value of 0 or 1, easy to embed in digital devices
- Binary System
 - · computers use the binary system to represent data
 - · bits are the building blocks, forming the foundation for all digital information
- Bit Representation
 - bits are often represented by electrical voltages, where high voltage corresponds to 1 and low voltage to 0
 - this binary representation is the basis for encoding information in digital devices



Bits, bytes, and computers

- A group of 8 bits is called a byte
 - bytes are commonly used to represent characters, numbers, and other data in computer systems



- How many different values can be stored in 1 byte?
- · How many different values can be stored in n bits?

Typical data types in C

The C language does not mandate this, it depends on the compiler used

More in this later ...

| C declaration | Bytes | | |
|---------------|----------------|--------|--------|
| Signed | Unsigned | 32-bit | 64-bit |
| [signed] char | unsigned char | 1 | 1 |
| short | unsigned short | 2 | 2 |
| int | unsigned | 4 | 4 |
| long | unsigned long | 4 | 8 |
| $int32_t$ | $uint 32_t$ | 4 | 4 |
| $int64_t$ | $uint 64_t$ | 8 | 8 |
| char * | | 4 | 8 |
| float | | 4 | 4 |
| double | | 8 | 8 |

| Logical operation | Operator | Notation | Alternative notations | Definition |
|-------------------|----------|--------------|---|--|
| Conjunction | AND | $x \wedge y$ | x AND y, Kxy | $x \land y = 1 \text{ if } x = y = 1, x \land y = 0 \text{ otherwise}$ |
| Disjunction | OR | $x \vee y$ | x OR y, Axy | $x \lor y = 0$ if $x = y = 0$, $x \lor y = 1$ otherwise |
| Negation | NOT | ¬ <i>x</i> | NOT x , N x , \overline{x} , x' , ! x | $\neg x = 0 \text{ if } x = 1, \ \neg x = 1 \text{ if } x = 0$ |

| x | y | $x \wedge y$ | xee y |
|---|---|--------------|-------|
| 0 | 0 | 0 | 0 |
| 1 | 0 | 0 | 1 |
| 0 | 1 | 0 | 1 |
| 1 | 1 | 1 | 1 |

| x | $\neg x$ |
|---|----------|
| 0 | 1 |
| 1 | 0 |

Boolean algebra

- Developed by George Boole in the 19th century
 - branch of mathematics that deals with binary variables and logic operations
 - binary values represent logical states true or false
- Logic operations
 - defines three fundamental logic operations:
 - AND: output is 1 only if both inputs are 1 conjunction
 - OR: output is 1 if at least one input is 1 disjunction
 - NOT output is the opposite of the input negation
- Boolean expressions
 - formed by combining variables and logic operations

Bit vectors

- Bit vectors are just sequences of bits
 - · boolean algebra can be extended to operate on bit vectors
- Applications in Computer Science
 - · representing sets of elements efficiently
 - implementing data structures
 - performing bitwise manipulation in low-level programming
- Understanding boolean algebra with bit vectors is essential for working with binary data in computer science and digital design

Bitwise operations in C

- Used with "integer" data types
 - · long, int, short, chart, unsigned
 - · arguments are treated as bit vectors
 - · corresponding binary logic operators are applied bitwise to operands
- Bitwise operators are commonly used to manipulate sets and masks

| ~ | bitwise NOT | ~a | the bitwise NOT of a |
|----|---------------------|--------|--|
| & | bitwise AND | a & b | the bitwise AND of a and b |
| | bitwise OR | a b | the bitwise OR of a and b |
| ^ | bitwise XOR | a ^ b | the bitwise XOR of a and b |
| << | bitwise left shift | a << b | a left shifted by b |
| >> | bitwise right shift | a >> b | a right shifted by b |

Bitwise operations in C

| bit a | bit b | a & b (a AND b) |
|-------|-------|-----------------|
| 0 | 0 | 0 |
| 0 | 1 | 0 |
| 1 | 0 | 0 |
| 1 | 1 | 1 |

| bit a | bit b | alb (a OR b) |
|-------|-------|--------------|
| 0 | 0 | 0 |
| 0 | 1 | 1 |
| 1 | 0 | 1 |
| 1 | 1 | 1 |

| bit a | bit b | a ^ b (a XOR b) |
|-------|-------|-----------------|
| 0 | 0 | 0 |
| 0 | 1 | 1 |
| 1 | 0 | 1 |
| 1 | 1 | 0 |

~a (NOT a) is trivial

Examples

Examples

~0x102

0xABC & 0x411

Examples

0xABC | 0x411

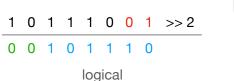
0x102030 & 0x00FF00

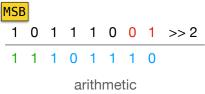
Shift operations

- Left shift (x << y)</p>
 - shifts each bit in x to the left by the number of positions indicated by y
 - throw away y bits on left
 - · blank spaces on right are filled up by zeroes

Shift operations

- Right shift (x >> y)
 - shifts each bit in x to the right by the number of positions indicated by y
 - throw away y bits on right
 - logical shift fill blank spaces on left with zeroes
 - arithmetic shift fill blank spaces by replicating original MSB on left (most compilers implement this)





Examples

0xF3 << 2

0x9A >> 3 (logical)

0x9A >> 3 (arithmetic)

Example: bit masking

- Assume an integer j that stores the value 0x1A35B127
 - define a mask to extract the most significant byte
 - write C code to store the extracted value in another variable (unsigned int)

Example: bit masking

- Assume an integer j that stores the value 0x1A35B127
 - write C code to set the least significant byte of j to all ones leaving all other bytes unchanged