

Homework 8

Name:

Section:

Homework Assignment: submit via gradescope

Design the control unit for the keyboard scancode converter started in lecture 11, based on the given mini-C code using the methods taught in lesson 10 and the state machine code style taught in lesson 9. Integrate it into the code and run a testbench showing two keys pressed - not the same ones that the existing testbench presses. Note: Line 10 of the mini-C "scan = shift[9-2]" implies the variable "scan" should be a register. Make the 8-bit output scan code be the output of an 8-bit register in your datapath, such that on system reset its value is 0x00 and it does not change until the final 8-bit scan code is known (and the following clock cycle, busy goes low [Line 2], indicating to the user that scan code can be used).

On the class website for lecture 11, you are given the VHDL code for: lec11_tb.vhdl, keyboard.vhdl, lec11.vhdl, and lec11_dp.vhdl. You need to write the VHDL code for lec11_cu.vhdl.

Turn in:

1. Hand-drawn datapath using BBBs
2. Hand-drawn state diagram for the control unit FSM.
3. Control Word (CW) output table for your control unit.
4. Your Control Unit VHDL code uploaded to your GitHub repo.
5. Image of your testbench simulation showing the following signals - remove all other signals.
 - clk
 - reset
 - keyboard clock
 - keyboard data
 - control unit state
 - Status word
 - Control word
 - busy signal
 - shift register
 - scan code
 - keyCntr

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