

Questions

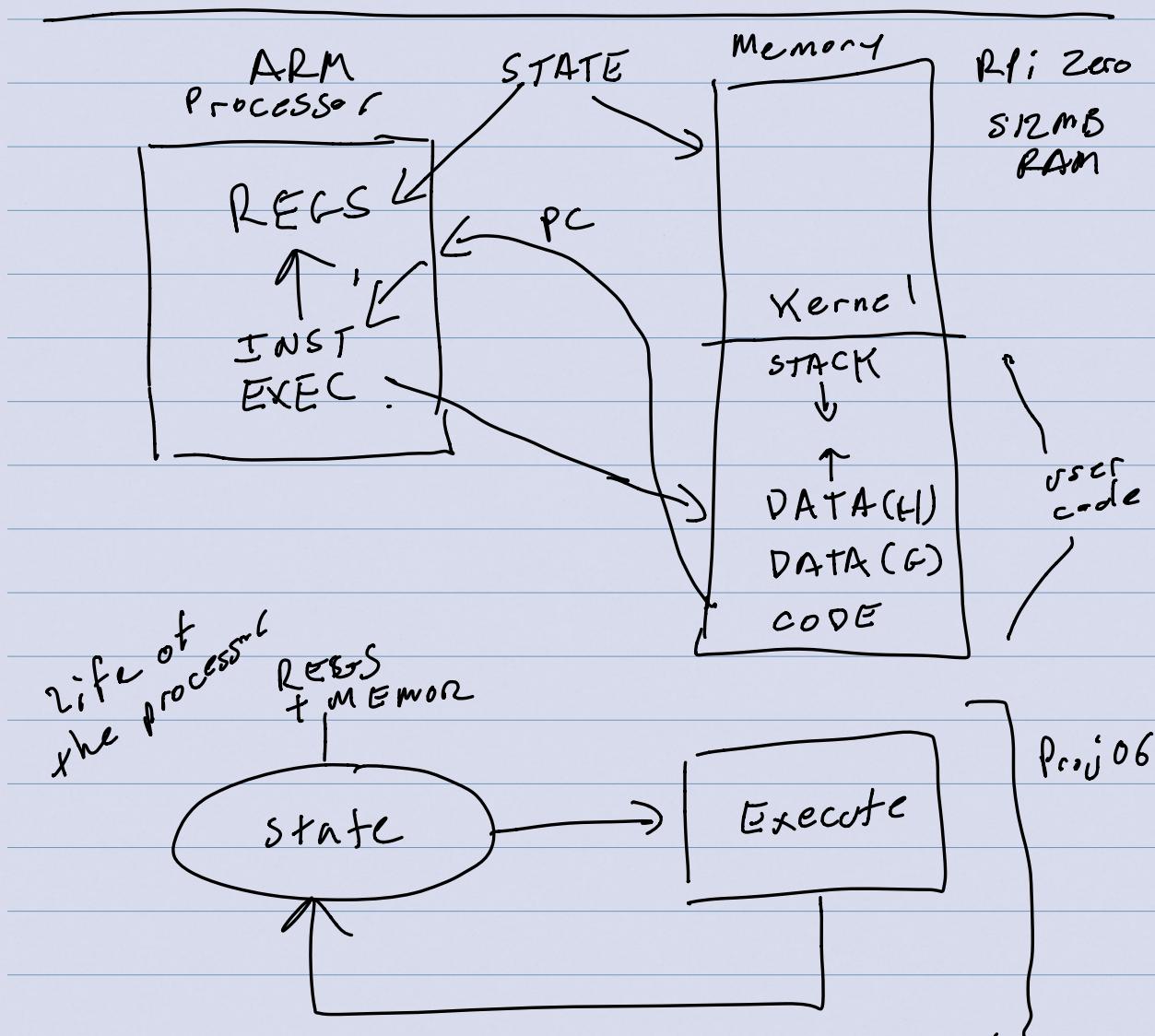
find_zeros_index

ARM Procedure Calling Conventions

strings and strlen

hello.s.s hello world

Recursion - factorial



ARM Instruction Types

Data Processing : add, mul, sub, mod, cmp

Memory : ldr, str

Control : b, b+, beq, bl, bx

ldr $r_0, [r_1]$ $r_0 = *r_1$

str $r_0, [r_1]$ $*r_1 = r_0$

ldr $r_0, [r_1]$

ldr $r_0, [r_1, \#4]$ $addr = r_1 + 4$

ldr $r_0, [r_1, r_2]$ $addr = r_1 + r_2$

ldr $r_0, [r_1, r_2, LSL\#2]$

↑

↑
logical shift left
 $r_2 \ll 2 = r_2 * 4$

→ $addr = r_1 + (r_2 * 4)$

int array[5]
 $x = array[i]$

↑

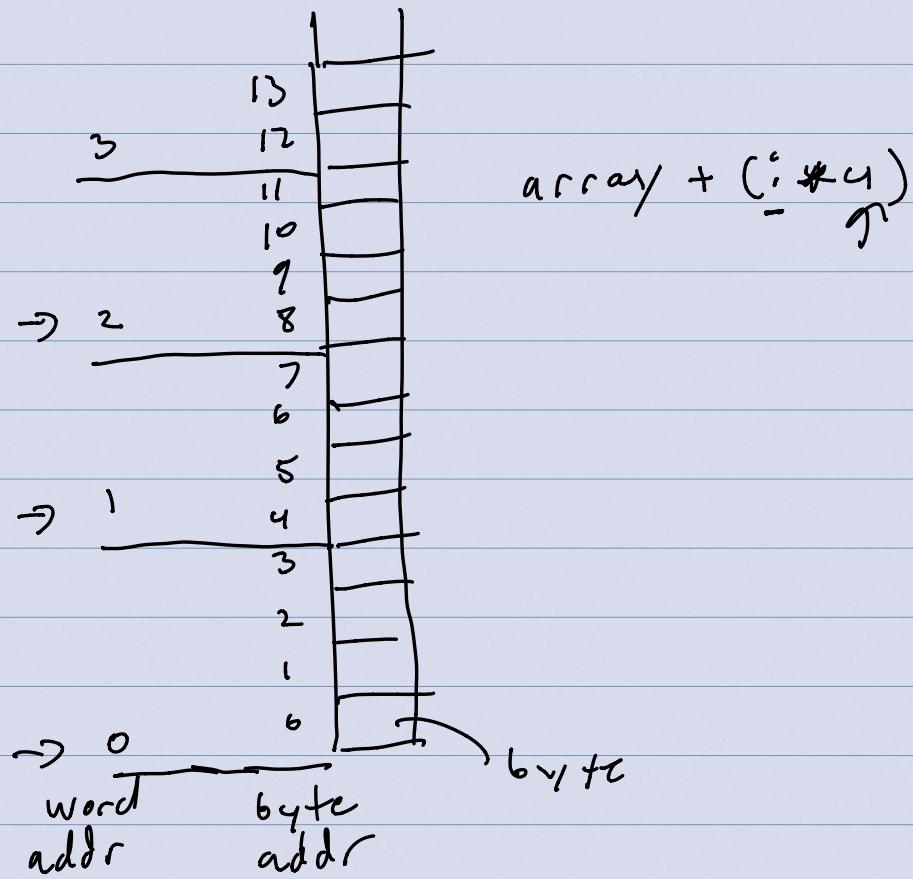
array is base pointer

array + ($i * 4$)

char ca[5] $ca[i] = ca + (i * 4)$

size of
the data type
of the
array

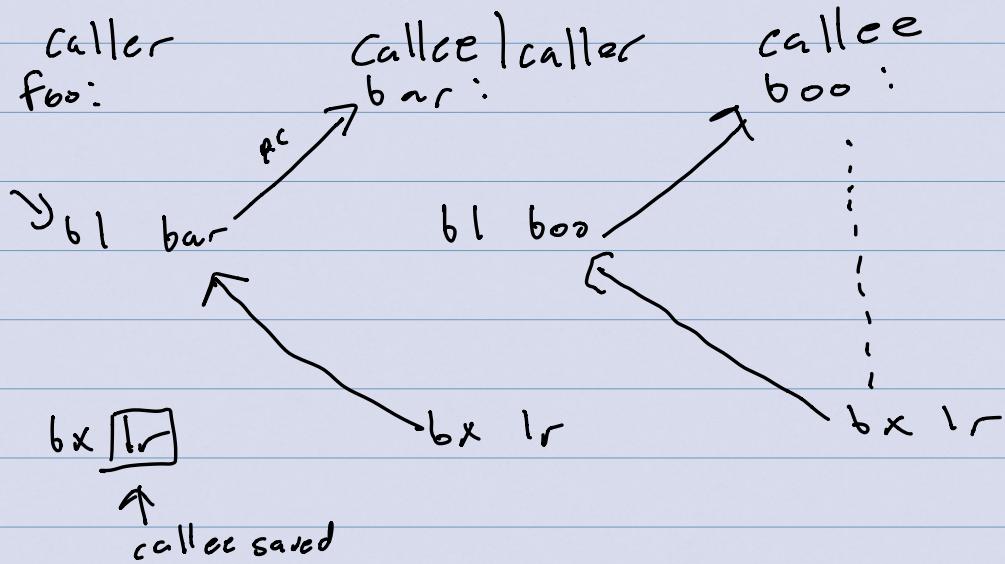
Memory is byte addressable



v = 6000 0110 ← 6 v << 2 × 4
// // /
0001,000 ← 24
16 8

r3, LSL #2

ARM Procedure Calling Conventions



`b1` branch and link

`b1 foo` $PC = \text{foo}$
 $\rightarrow \text{add}$ $LR = \text{addr of the inst after } b1 \text{ foo}$

\uparrow link register
return address

`bx lr` branch and exchange

caller-saved (non preserved)

callee-saved (preserved)

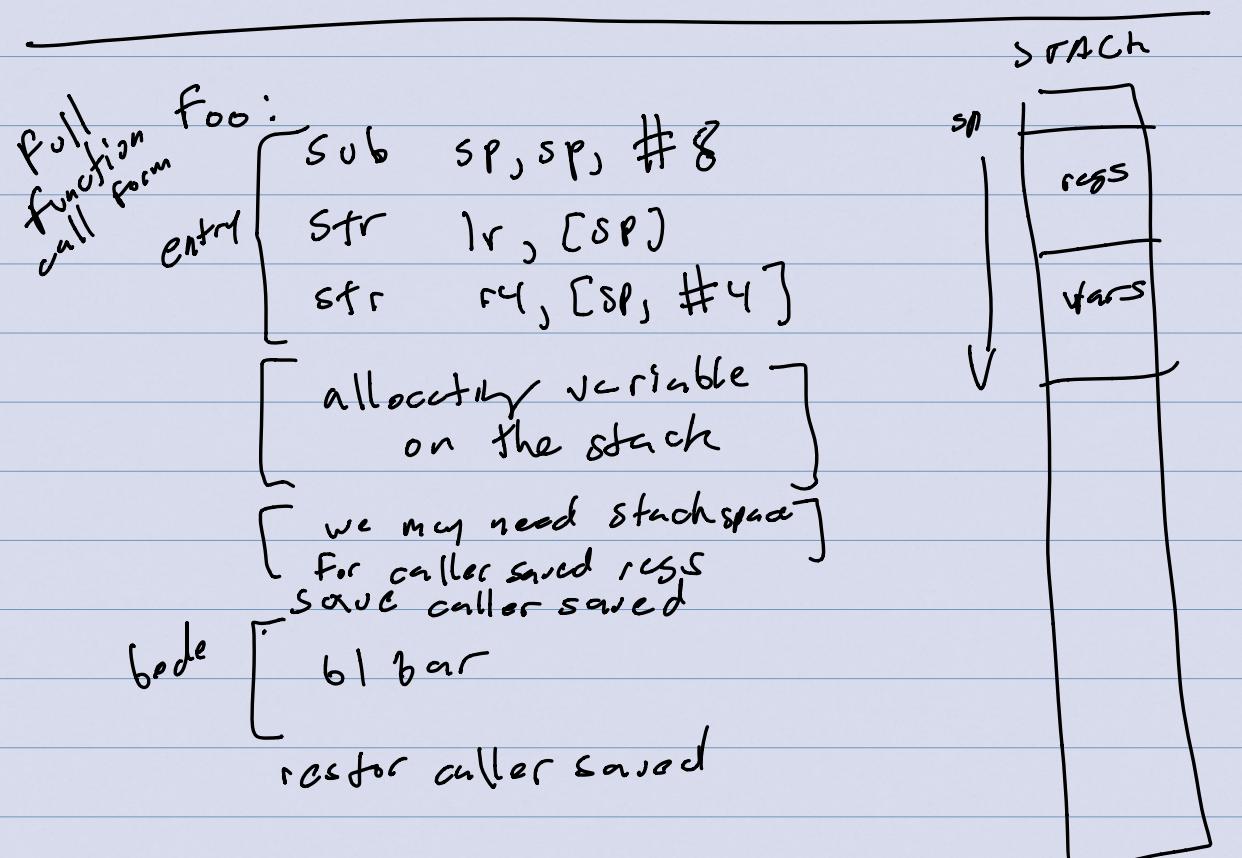
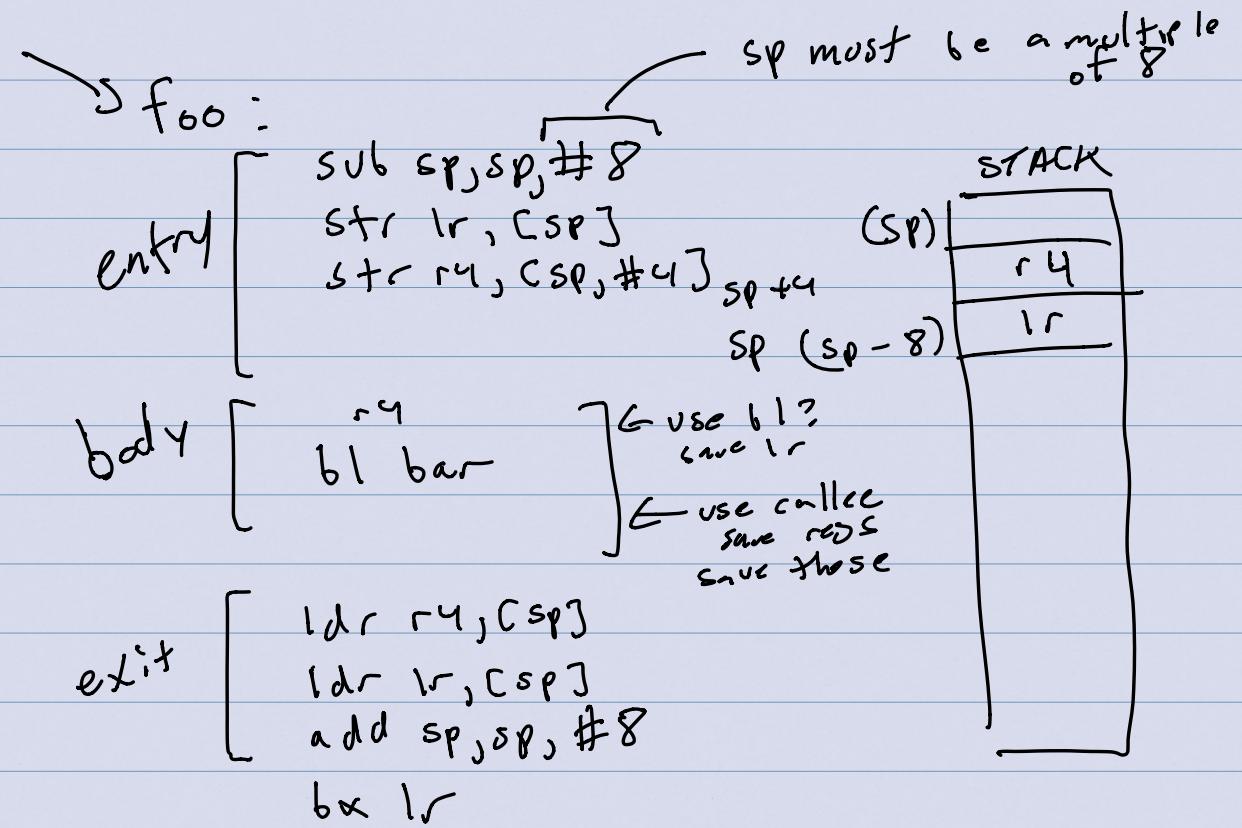
caller-saved

$r0, r1, r2, r3$
 $r12, CPSR$

callee-saved

$r4-r11, r13, r14$
SP LR

PC special



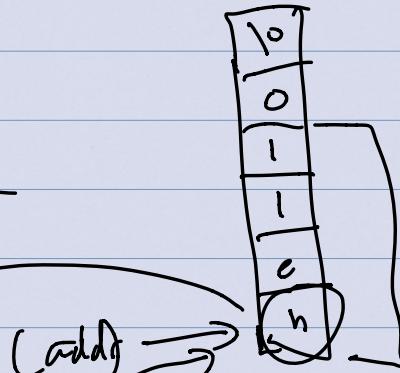
exit
 [ldr r4, [sp], #4]
 [ldr lr, [sp]]
 [add sp, sp, #8]
 bx lr

strings

r0 } 32 bits
 r1 } 4 bytes
 : }

ldr ← word

ldrb r0, [r1]
 strb r1, [r1]



(add)