

# Project03 updates

Lab06

sum\_array\_rec

strcat } Lab section ldrb, strb  
substr }

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push/pop and other instructions

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labels

.global foo

foo: ← entry point into a function

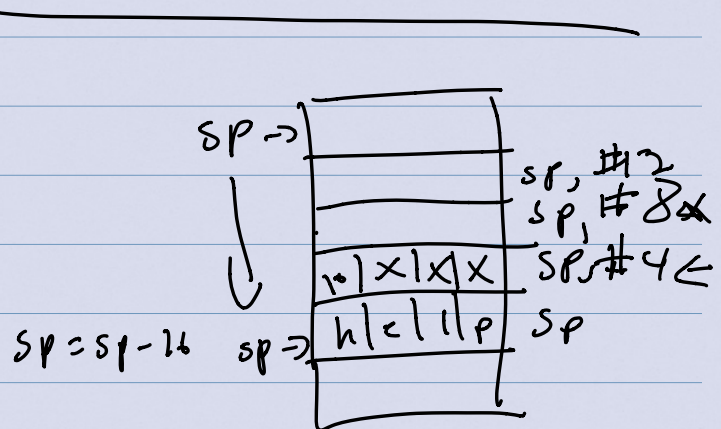
add  
↓  
loop: mov  
bx lr  
.global bar  
bar:

← if/then/else for/while

~~loop:~~

foo\_loop:

bar\_loop:



ldr, str

ldrb, stlb

base register  
base address

base address

$\text{ldr } r_0, [r_1]$   $\text{addr} = r_1$   $r_0 = *r_1$

$\text{ldr } r_0, [r_1, \#4]$   $\text{addr} = r_1 + 4$

$\text{ldr } r_0, [r_1, r_2]$   $\text{addr} = r_1 + r_2$

$\text{ldr } r_0, [r_1, r_2, \text{LSL} \#2]$   $\leftarrow$

$\nearrow \text{addr} = r1 + (r2 \ll 2)$   
 $r1 + (r2 \ll 4)$   
 $\text{array} + (i \ll 4)$

