

Lab 05 solutions

gdb

ARM registers

ARM execution

Control

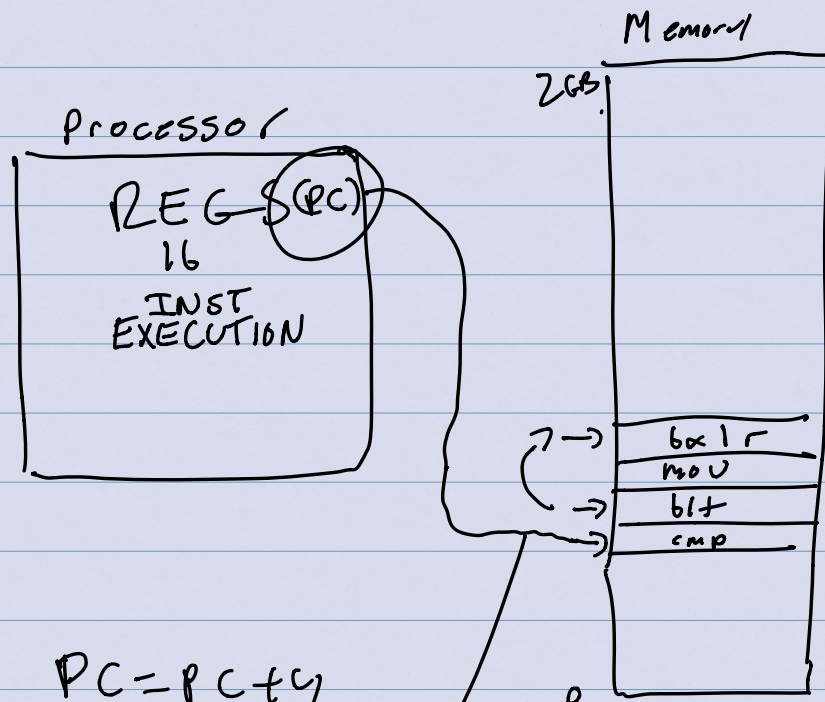
conditional (if/then/else)

loops (for/while)

Memory - memory access

Arrays

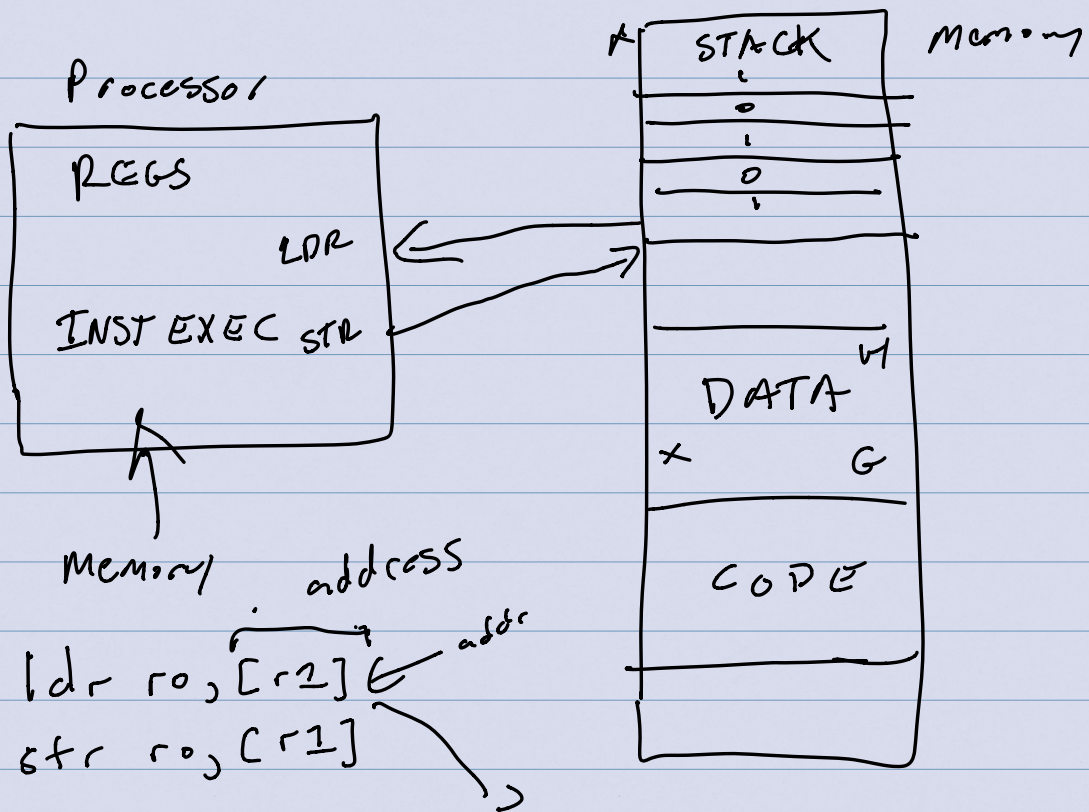
Function calls



$$PC = PC + 4$$

32 bit address

Each instruction
is 32 bits
4 bytes



$r0 = *r1$
 $*r1 = r0$

C equivalent

count_zeros_c(int array, int len)

↑

r0