

cgdb

Lab 03

eval

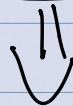
Lab 04

Bases and bit manipulation

Lab 04

TK_BINLIT

value "1010"



int => [uint32_t]

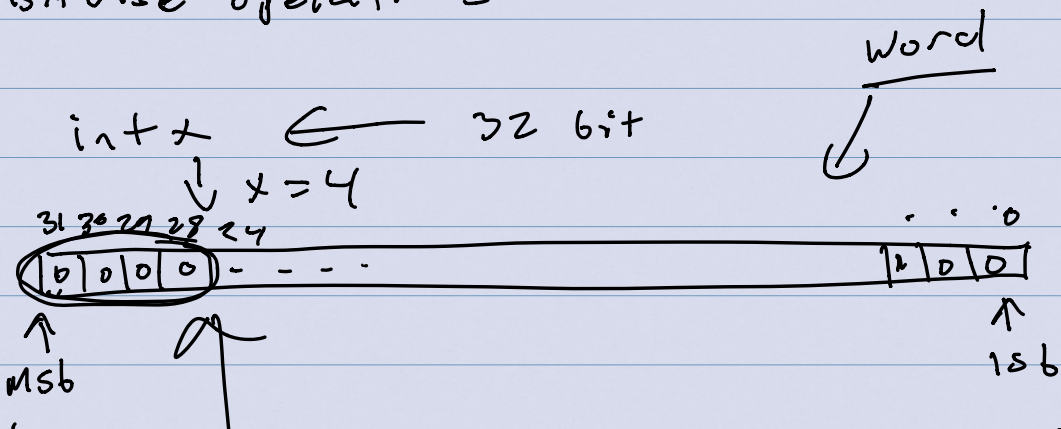


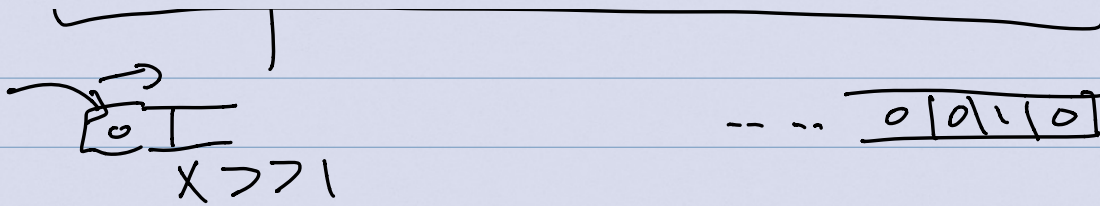
unsigned int

32 bits

4 bytes

Bitwise operations





int top4 = x >> 28

\sim NOT	$<<$ LSL logical shift left
$\&$ AND	\gg LSR logical shift right
$ $ OR	$\gg-$ ASR arithmetic shift right
\wedge XOR	

a	$\sim a$
0	1
1	0

$a \ b$	$a \& b$
0 0	0
1 0	0
0 1	0
1 1	1

$a \ b$	$a b$
0 0	0
1 0	1
0 1	1
1 1	1

$a \ b$	$a \sim b$
0 0	0
1 0	1
0 1	1
1 1	0

$$a = 061101$$

$$b = 061100$$

$$\underline{\hspace{1.5cm}} \\ 1100$$

$$a \& b = 061100$$

$$\begin{array}{rcl}
 200 / 16 & = & 12 \quad \text{REM} \quad \text{base 16} \\
 12 / 16 & = & 0
 \end{array}
 \quad
 \begin{array}{|c|} \hline 8 \\ \hline 12 \\ \hline \end{array}
 \begin{array}{l} \rightarrow 8 \\ \rightarrow C \end{array}$$

0xC8

0xABCD

13

$$\begin{array}{cccc}
 1016 & 1011 & 1100 & \overset{84}{1101} \\
 & & & \hline
 \end{array}$$

0b 1010 1011 1100 1101

0b 1101

0xF7D

$$(15 \times 16^2) + (7 \times 16^1) + (13 \times 16^0)$$