

Labs - collaborative

Bitwise operators Projects - individual

masking

get-bit(), set-bit()

conv-uint32_to_binstr()

change parse-node-st and eval to use uint82t
command line args

2's complement] Tue
ASR

\sim $\&$ $|$ \wedge $<<$ $>>$ $>-$
NOT AND OR XOR LSL LSR ASR

a	$\sim b$	a b	$a \& b$	a b	$a \oplus b$	a b	$a \neg b$
0	1	0 0	0	0 0	0	0 0	0
1	0	0 1	0	0 1	1	0 1	1

↓

↓

$<<$
LSL

0

0 1 1 0
0 1 1 0

$<< 1$
number of bits
to shift

uint32_t *;
31 30 29
| | | |

↑
msb

$(2^{32} - 1)$

0 + $(2^{32} - 1)$

... 2 1 0
| | | |

↑

lsb

$$0b0011 \rightarrow 3$$

$\times 2$

$$\begin{array}{rcl} 0b0011 & \ll & 1 \\ = 0b0110 & \rightarrow & 6 \\ & & \times 2 \end{array}$$

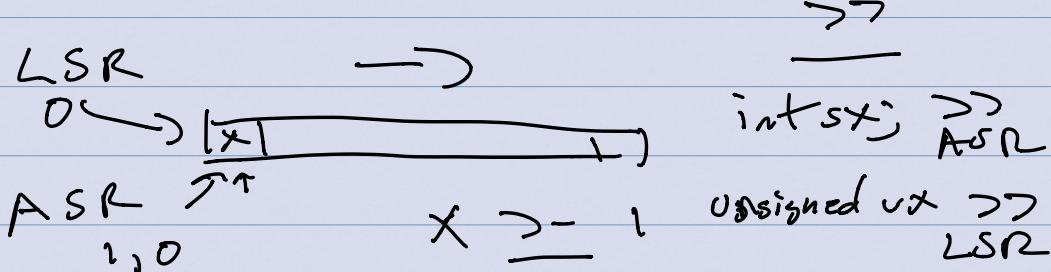
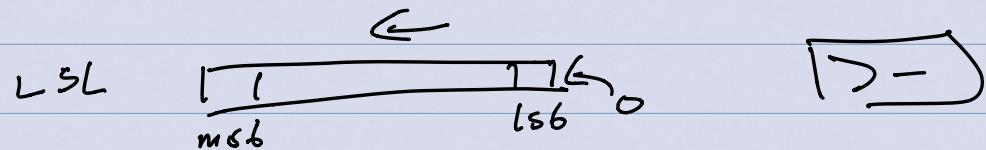
$$\begin{array}{rcl} 0b0110 & \ll & 1 \\ = 0b1100 & \rightarrow & 12 \end{array}$$

$$\begin{array}{rcl} 0b0011 & \ll & 2 \\ 0b1100 & \rightarrow & 12 \end{array}$$

bitwise ops

$$0b1100 \quad 0b1001$$

$$\begin{array}{rcl} 0b1100 & \text{or} & 0b1100 \\ \cancel{0} \cancel{b} \cancel{1} \cancel{0} \cancel{0} & \cancel{1} \cancel{0} \cancel{b} \cancel{1} \cancel{0} \cancel{0} & \cancel{x} \cancel{o} \cancel{r} \cancel{0} \cancel{b} \cancel{0} \cancel{1} \\ \hline 1000 & 1101 & 0101 \end{array}$$



Masking

06 1010 1110
 \underbrace{ }_{ } \underbrace{\overset{\times}{\text{X}}}_{\text{ }}

06 1010 1110
δ 06 1111 0000 mask
 $\overbrace{1010}^{\text{1010}} 0000 \gg 4$
 $= \underbrace{0000}_{\text{ }} \underbrace{1010}_{\text{ }} \Rightarrow 10$

06 1100 $\overbrace{1010}^{\text{1010}}$ 1110

06 0000 $\underbrace{1111}_{\text{ }} 0000$

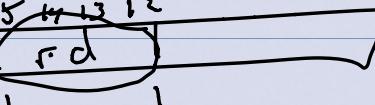
06 1100 $\underbrace{1010}_{\text{ }} \overbrace{1110}^{\text{1110}} \gg 4$

06 0000 1100 1010
δ 06 0000 0000 $\circlearrowleft 1111$

06 0000 0000 1010

instruction word 32 bits

15 14 13 12



add (r0), r1, r2

~

$\sim(610101100)$

$= 60101\ 0011$

\uparrow Unsigned int

$\lceil \text{uint32_t} \rceil \quad x = 1;$

$x = x + (^)$

$x += ()$

$x = x \cancel{\delta} \cancel{z}$

$x \cancel{\delta} = z;$

$x \cancel{\delta} = 0b10$

Scanner

"0b1101"
 ↓

TK. value = "1101"

parse-operand()

TK_BINLIT

convert binstr \rightarrow uint32_t
of
intval

~~int get_bit(int i, uint32_t v) {~~

~~i = get_bit(2, 0b1001)~~

~~0 > get_bit(1, 0b1101)~~

~~* int get_bit(int i, uint32_t v) {~~

~~int r;~~

~~r = (v >> i) & 0b1~~

~~return r;~~

~~3~~

~~uint32_t set_bit_on(int i, uint32_t v) {~~

~~uint32_t r;~~

~~r = (0b1<<i)| v;~~

~~return r;~~

~~5~~

~~= set_bit_on(3, 0b11000001)~~

~~= 0b11001001~~

~~0b 0000 0000 1
0b 0000 0001~~

~~1 0b1100 0001
1 0b0000 0001~~

`vint32_t set_bit_off (int i, vint32_t v) {`

`vint32_t r;`

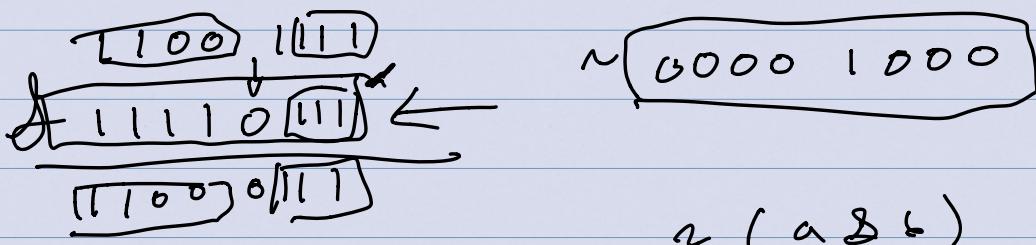
`r = ~(0b1<<i) & v;`

`return r;`

`3`

`set_bit_off (3, 0b11001111)`

`= 0b11000111`



`~ (a & b)`

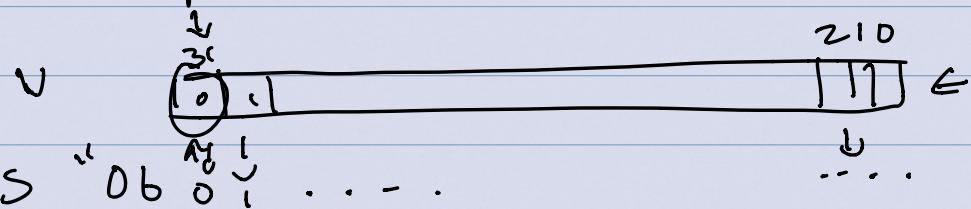
`~a | ~b`

`vint32_t set_bit (int i, vint32_t v, int b);`

`i`

lab4 input "0b1010" \Rightarrow vint32_t

output vint32_t \Rightarrow "0b1010"



void conv_int32_to_binstr(uint32_t v,
char *s)

int i, j, b; t

s[0] = '0';

s[1] = 'b';

j = 2;

for (i = 31; i >= 0; j--) {

b = get_bit(i);

b = (v >> i) & 0b1

{ if (b == 0) {
 s[j] = '0';
} else {
 s[j] = '1';
}
j += 3;
s[j] = '\0';

}

uint32_t conv_binstr_to_int32(char *s)

uint32_t r = 0;

"0b" "0101100"

↓

setbit()