

Week09 encoding \rightarrow generating dynamic code

armasm_start1 code

add r1, r2, r3

bx lr

sub r0, r1, r2

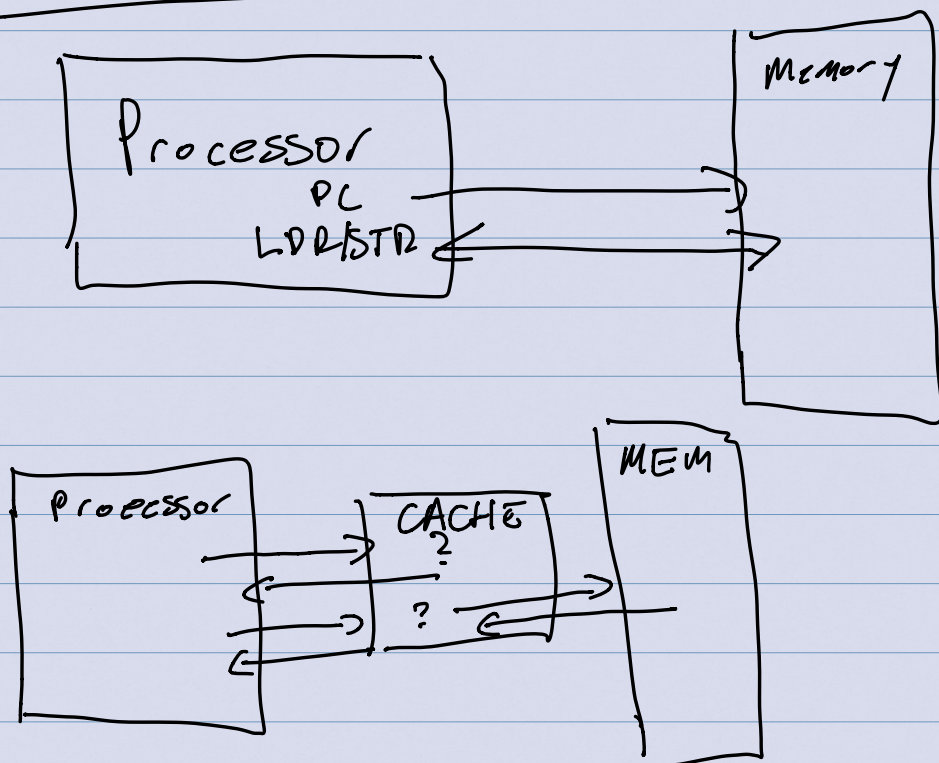
ldr r0, [r1, r2]

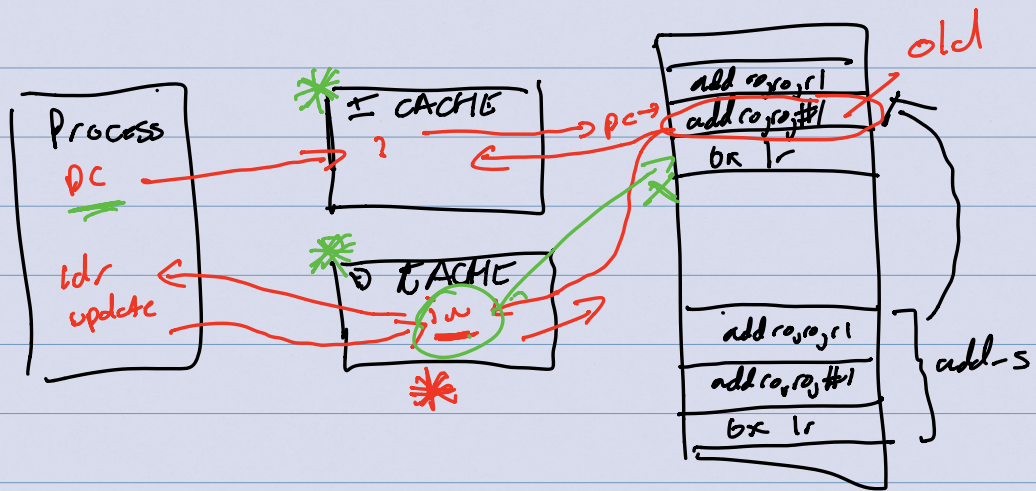
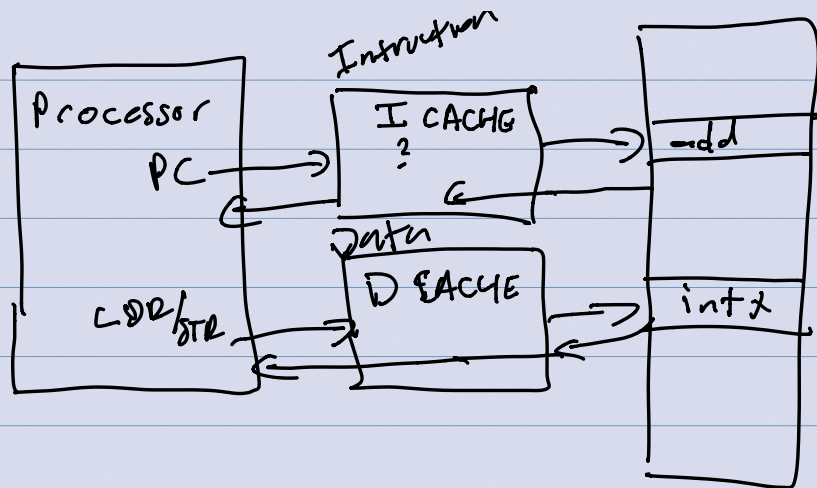
to add

decode \rightarrow dumps instruction word
as hex values

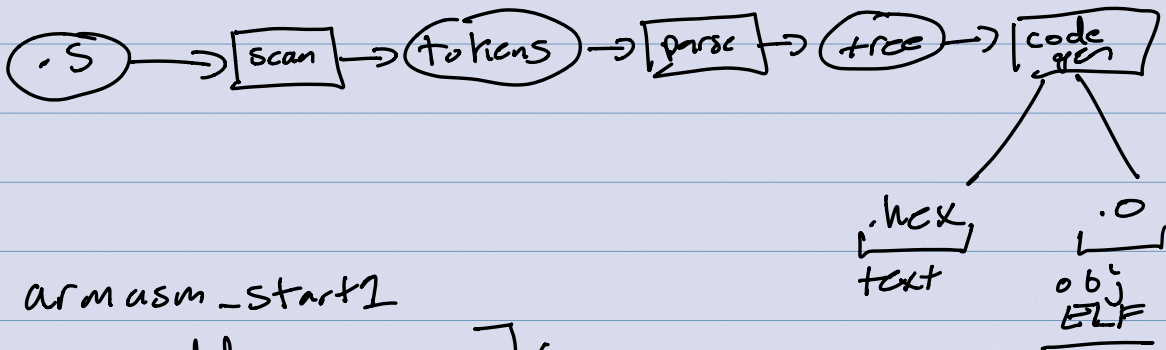
mod-dp-imm \rightarrow modifies imm field
of a dp inst.

execute \rightarrow ?





arm asm



arm asm_start1

```

add r0, r1, r2 } ←
bx lr          } ←
  
```

explain instruction format classes

sub
ldr

○ global foo

symbols

foo:

identifiers

add r1, r2, r3

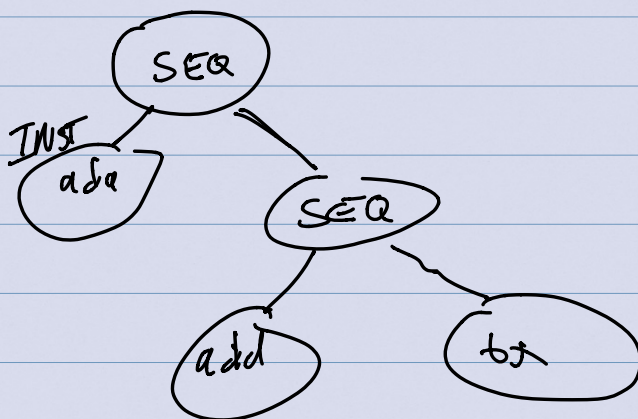
ldr r0, [r1]

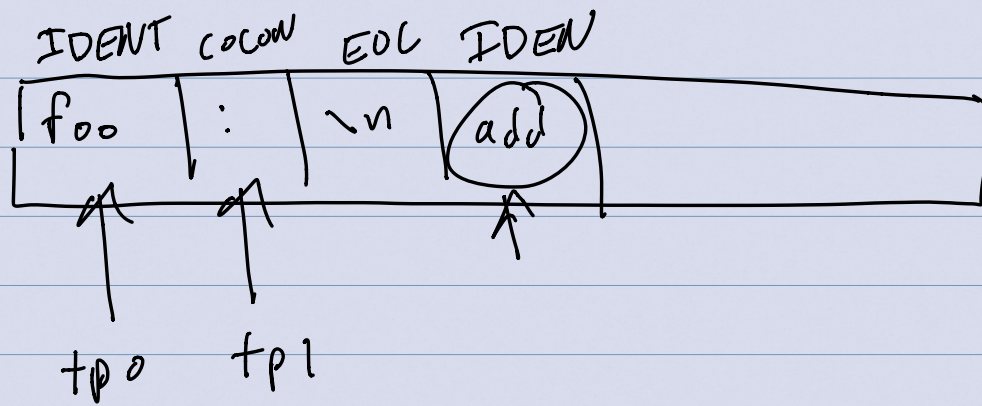
mov r1, #2

bx lr

integer
imm

mov r0, #-3 ⇒ imm





E 6 1 1 0 0 0 2
 0 1 1 0 0 0 0 1

E 7 9 1 0 0 0 0
 0 1 1 1 1 0 0 1