

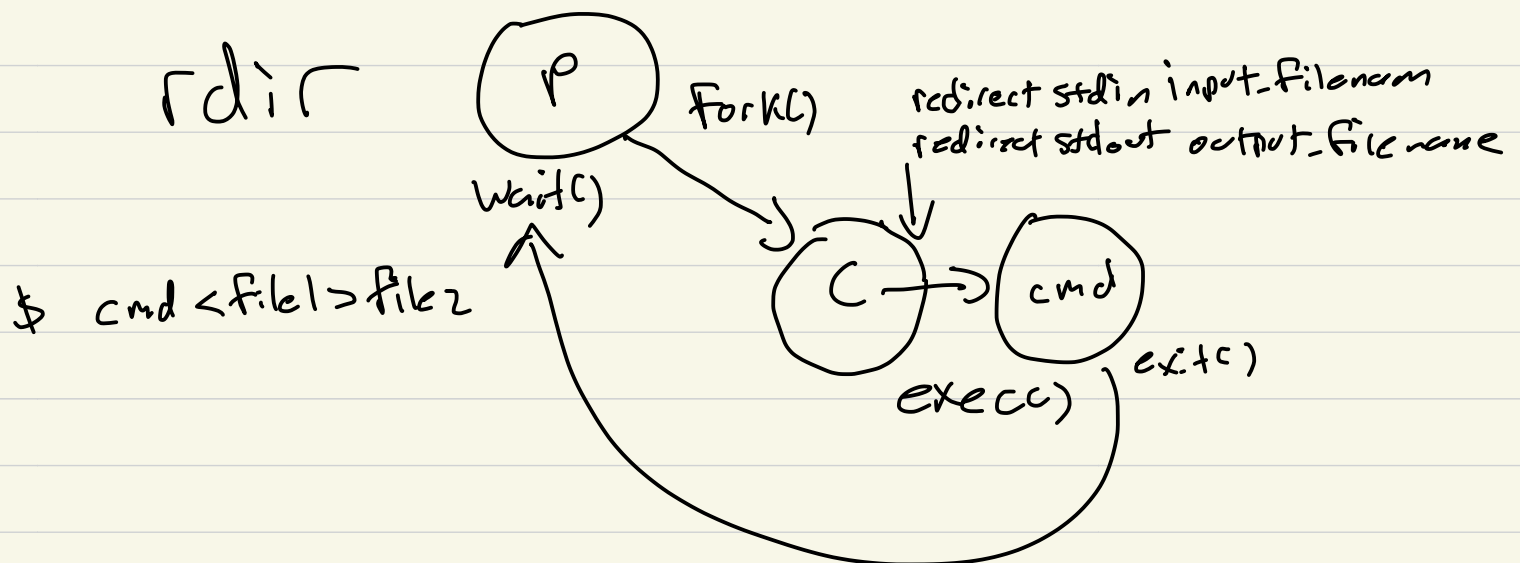
CS326-01 Pipes and Kernel Intro

Project 01 Fixes

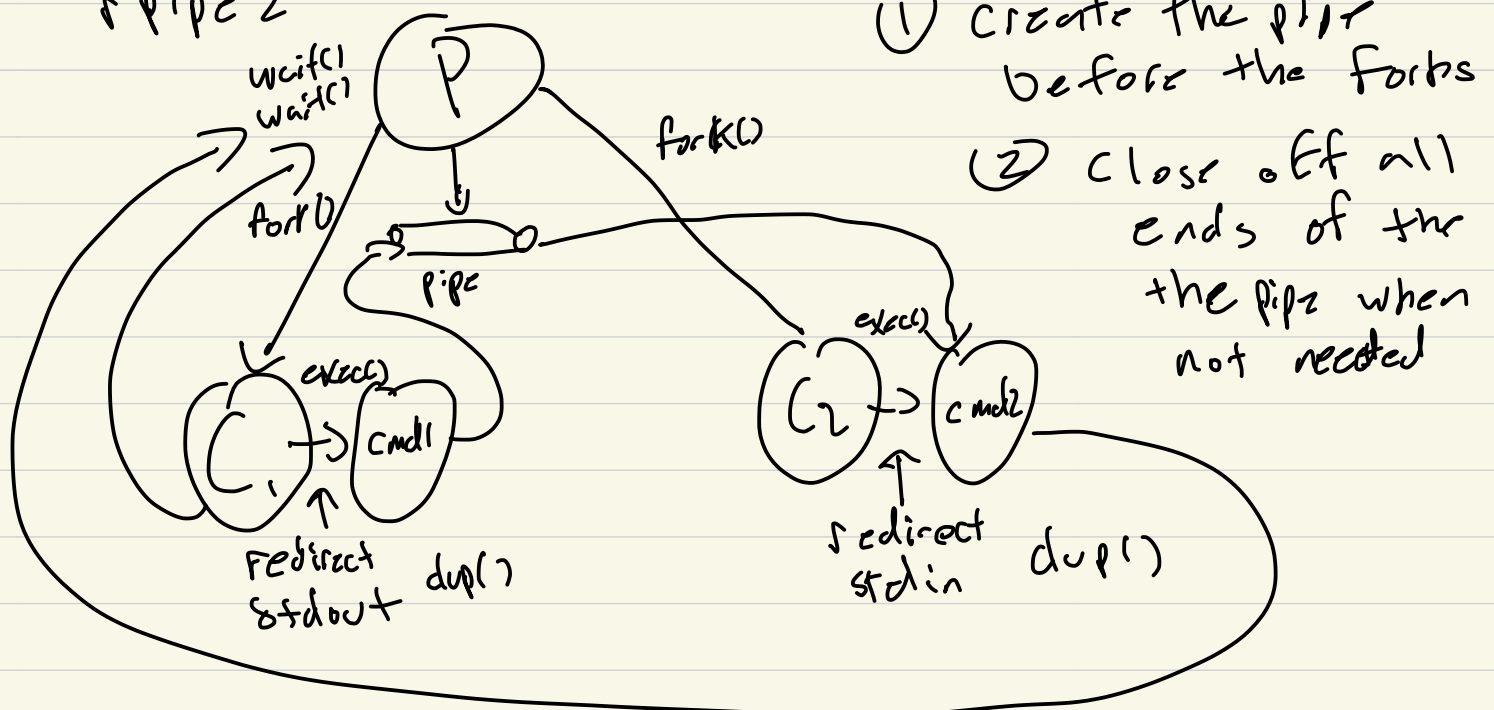
Project 02 Over spring break

lab 04

rdir



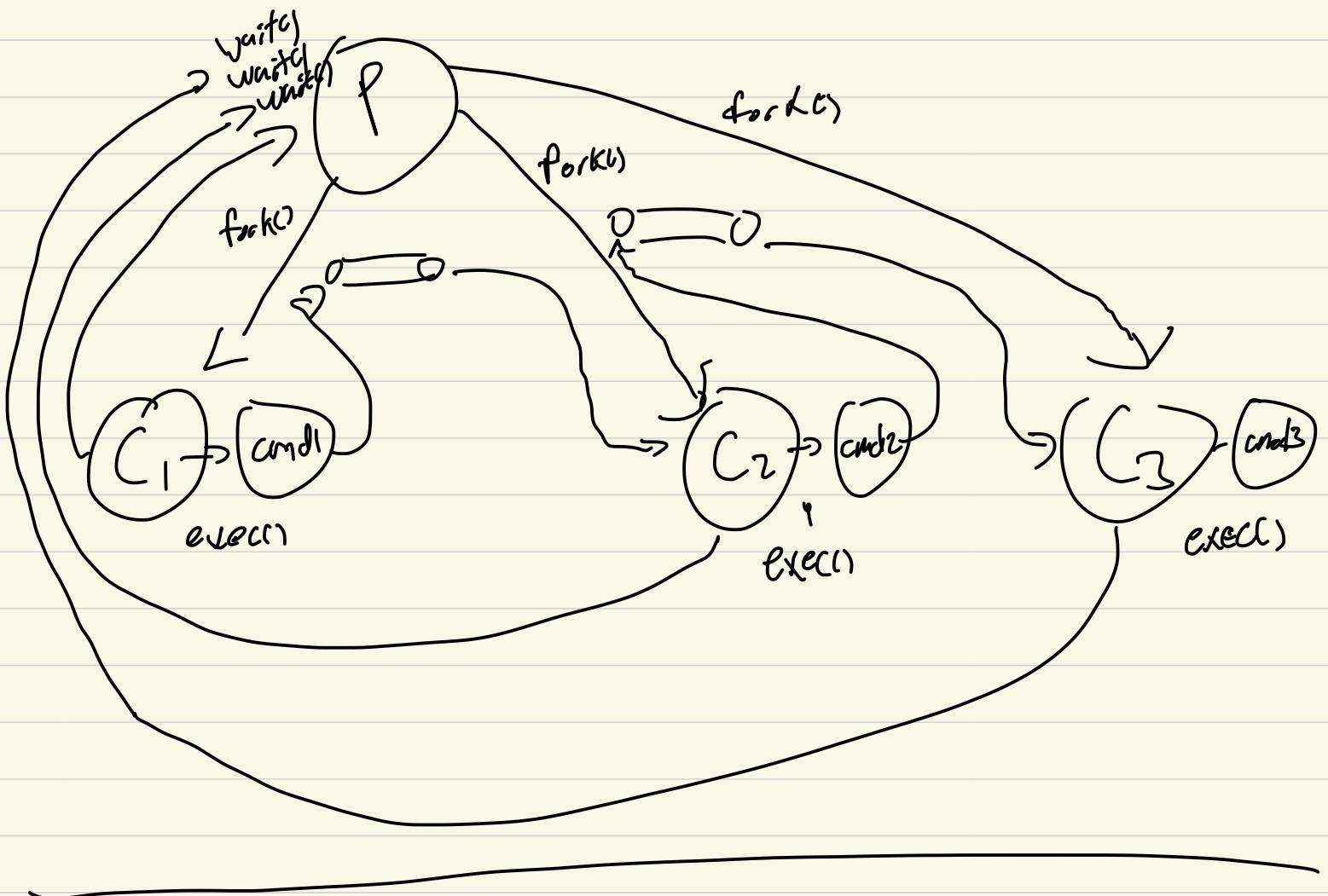
r pipe2



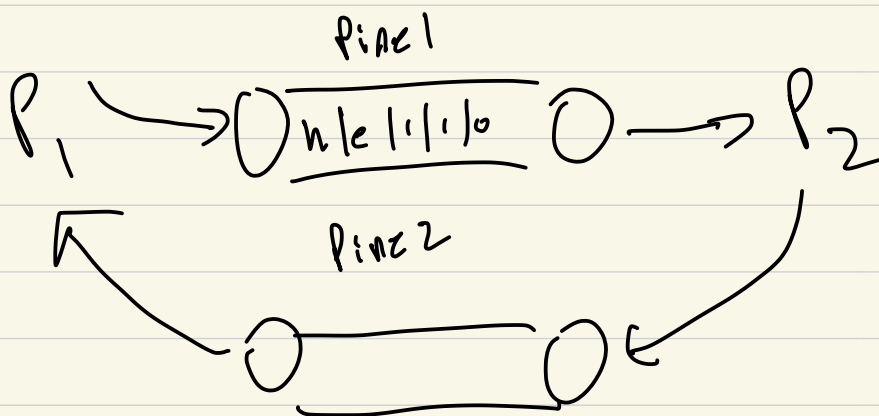
\$ cmd1 | cmd2

r p.c3

& cmd1 | cmd2 | cmd3

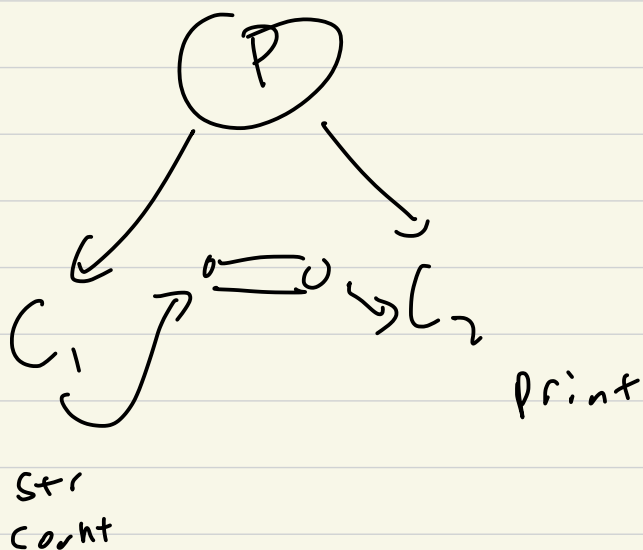


pipes are not bidirectional, use two pipes

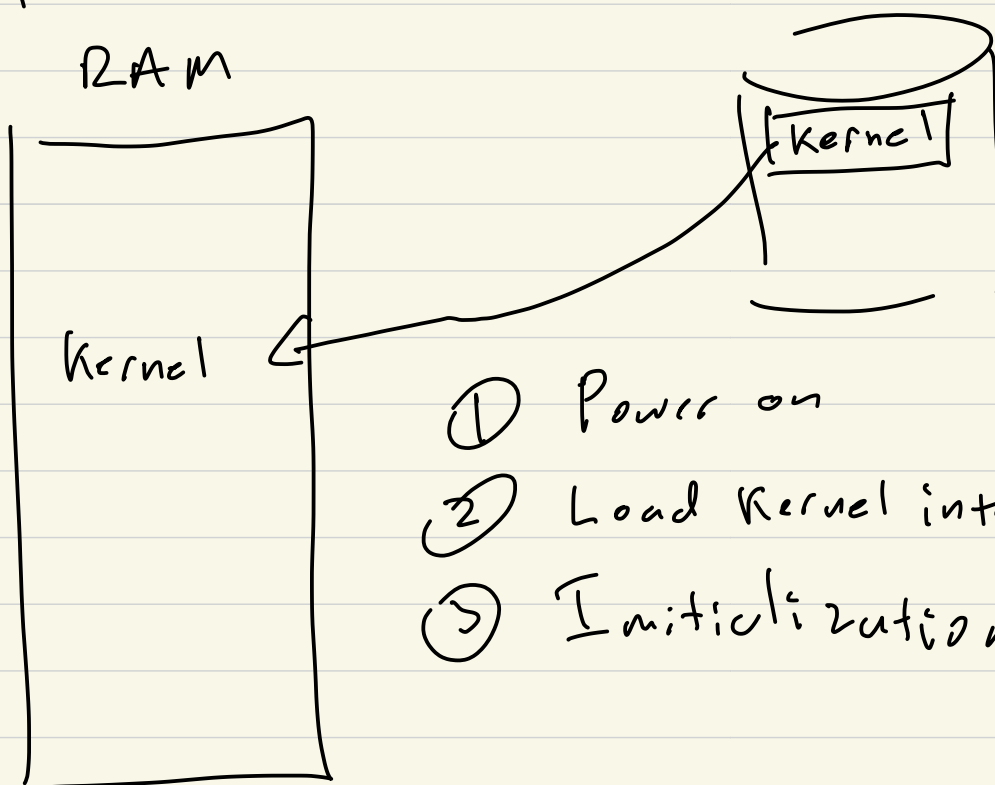


Pipe Flow control

pipesend str count

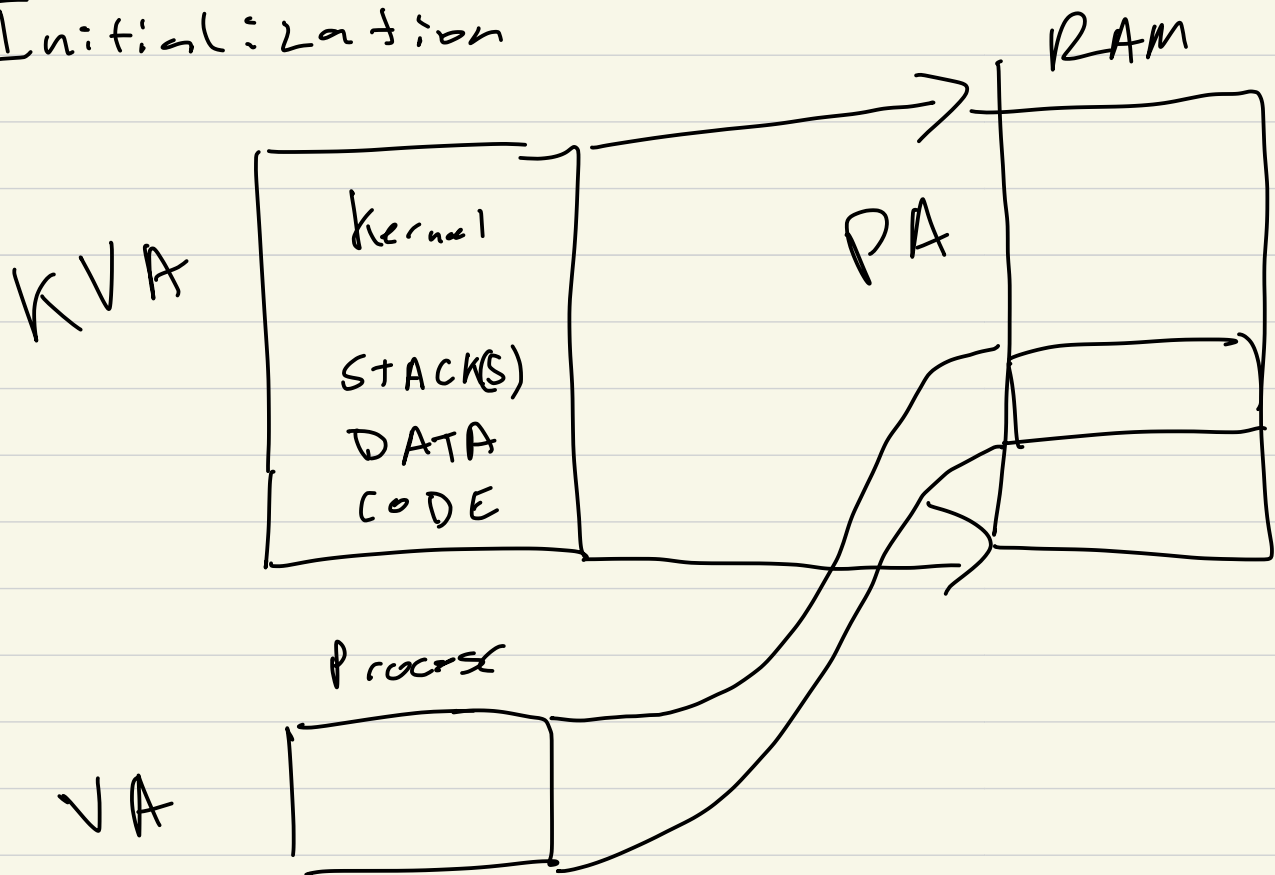


The Kernel



- ① Power on
- ② Load kernel into RAM
- ③ Initialization

Initialization



Single CPU

