Opcode	Instruction Type		Instruction	Description
0000	Full Address Instruction		LDm	Load Accumulator Immediate
0001			LDa	Load Accumulator Addressed
0010			LDn	Load Accumulator Indirect
0011			STa	Store Accumulator Addressed
0100			STn	Store Accumulator Indirect
0101			INa	Increment memory Address
0110			ANm	AND Accumulator Immediate
0111			ANa	AND Accumulator Addressed
1000			ADm	Add Accumulator Immediate
1001			ADa	Add Accumulator Addressed
1010			ADn	Add Accumulator Indirect
1011			MLa	Multiply Accumulator Addressed
1100			JMa	Jump Addressed (unconditional)
1101			JMn	Jump Indirect
1110			JSR	Jump Sub-routine
1111	No Address Instruction	0000	LOm	Load Offset register Immediate
		0001	INO	Increment Offset register
			LPO	Load PC to Offset register
			LOP	Load Offset register to PC
			ACZ	Accumulator Zero
			ACN	Accumulator NOT
			ACI	Accumulator Increment
			SLA	Shift Left Arithmetic
			SRA	Shift Right Arithmetic
			SLL	Shift Left Logical
			SRL	Shift Right Logical
		0010	$SKP_{Z,N,C,V}$	Skip Zero, Negative, Carry, Overflow (En, Value)
		0011	$SET_{Z,N,C,V}$	Set Zero, Negative, Carry, Overflow (En, Value)
	Short Address Instruction	0100		
		0101		
		0110		
		0111		
		1000		
		1001		
		1010		
		1011		
		1100		
		1101		
		1110		
		1111		