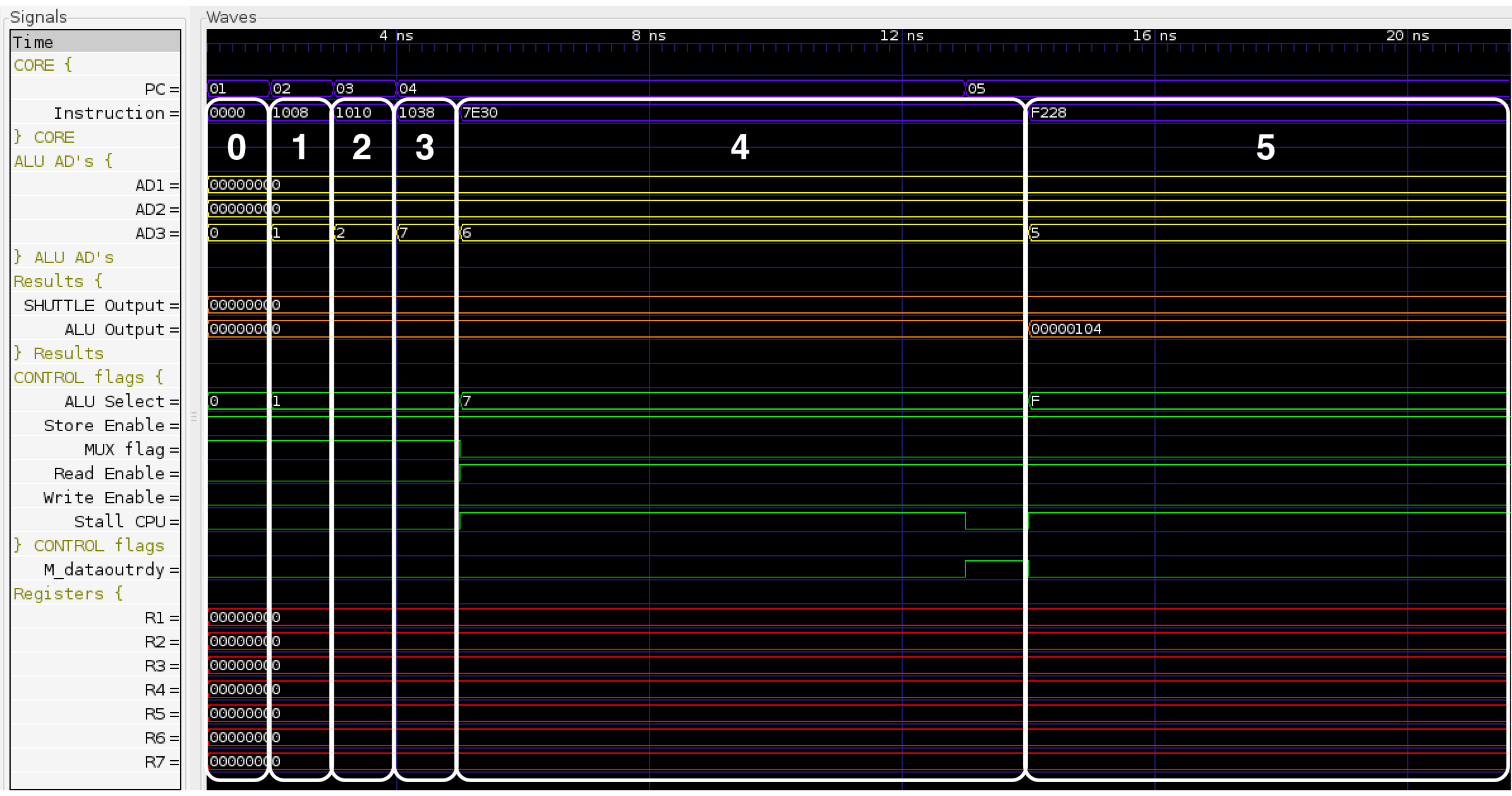
Signals
Waves
Time
CORE {
PC =
Instruction =
} CORE
ALU AD's {
AD1 =
AD2 =
AD3 =
} ALU AD's
Results {
SHUTTLE Output =
ALU Output =
} Results
CONTROL flags {
ALU Select =
Store Enable =
MUX flag =
Read Enable =
Write Enable =
Stall CPU =
} CONTROL flags
M_dataoutrdy =
Registers {
R1 =
R2 =
R3 =
R4 =
R5 =
R6 =
R7 =
4 ns
8 ns
12 ns
16 ns
20 ns
01
02
03
04
05
0000
1008
1010
1038
7E30
F228
0
1
2
3
4
5
00000104