



DELHI TECHNICAL CAMPUS
Greater Noida
Affiliated to GGSIPU and Approved by AICTE &
COA



INDEX

S.NO.	PROGRAM NAME	DATE OF EXPERIMENT	DATE OF SUBMISSION	SIGN.
1	Creating a Process Scheduler GUI to visually demonstrate how various CPU scheduling algorithms work (like FCFS, SJF, etc.).			
2	Design a process scheduling Visualizer which Simulate and visualize Round Robin, Priority Scheduling.			
3	Implement Priority Scheduling (Preemptive and Non-Preemptive).			
4	Design a Deadlock Detector using Resource Allocation Graph.			
5	Implement the Producer Consumer Problem using Semaphores.			
6	Design a Deadlock Detection and Simulation Demo tool showing deadlock detection and prevention.			
7	Design an Memory management visualizer that visually demonstrates how memory is allocated to processes using various memory allocation strategies.			
8	Design a Page Replacement Algorithm Simulator showing FIFO, LRU, Optimal replacement for demand paging.			
9	Simulate Disk Scheduling Algorithms (FCFS, SSTF, SCAN, C-SCAN).			
10	Design a User Login System with File Permissions.			