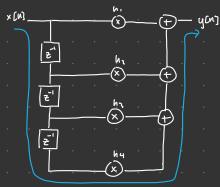
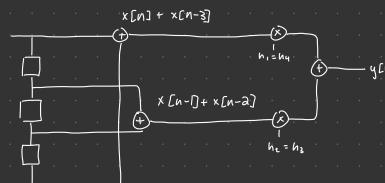
## 7: Uhn Structures

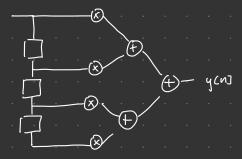




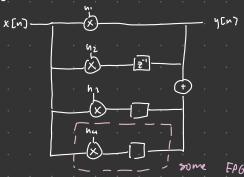
- symmetry based structure - used when coeff's have symmetry



structure



- systolic [FPGA - centric]



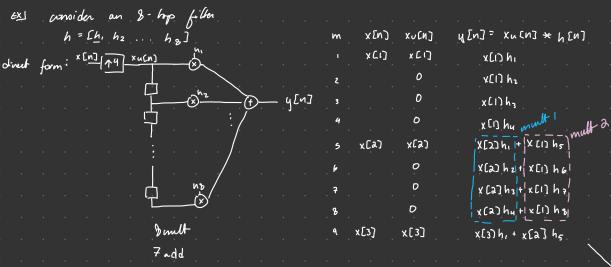
some FPGAS have a 'DSP block' so this design would me 3 DSP blocks some hardware cost, just maps milly

polyphase structure

- cont be used all the time - use when these so up/abun sampling right bytem the filter

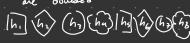


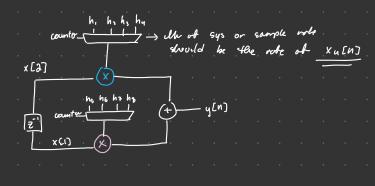
block or the other one; build the upsample



Lo but a factor of 4 in efficiency become of the upsampling the convolution only regure 2 mults at once to 3/4: 2 mults only

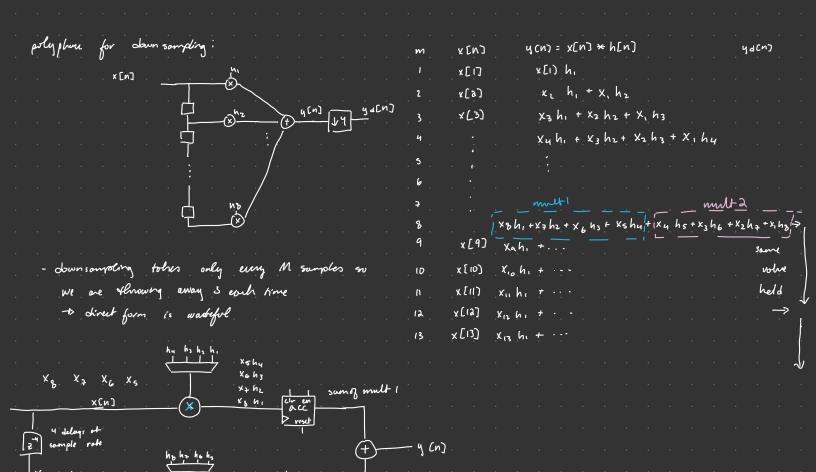
are doubled





ove delong cliked at symborote

u delays at surple robe



can dhink obt combing the symmetry structure and the polyphose structure