**UART Interrupts Definition UART Number of Stop Bits UART LIN Break Detection UART Local Interconnection** Network mode **UART Advanced Feature** Overrun Disable **UART Advanced Feature** RXTX Pins Swap **UART Advanced Feature** Binary Data Inversion **UART Advanced Feature** Mute Mode Enable **UART Interruptions** Flag Mask **UART Over Sampling UART DMA Tx** UART DMA Rx UART WakeUp Methods **UART One Bit Sampling** Method **UART Interruption Clear** Flags **UART Advanced Feature** DMA Disable On Rx Error **UART Advanced Feature** Auto BaudRate Enable **UART Advanced Feature** RX Pin Active Level Inversion **UART Driver Enable UART Exported Constants UART** DeAssertion Time LSB Position In CR1 Register **UART Advanced Feature** MSB First **UART Receiver Timeout UART Hardware Flow** Control **UART Parity UART** polling-based communications time -out value **UART Driver Enable** Assertion Time LSB Position In CR1 Register **UART Request Parameters UART Transfer Mode UART Advanced Feature** AutoBaud Rate Mode **UART State Code Definition** UART DriverEnable Polarity **UART Half Duplex Selection UART Advanced Feature** Initialization Type **UART State UART Error Definition UART Status Flags UART Address-matching** LSB Position In CR2 Register **UART** Advanced Feature TX Pin Active Level Inversion