

FACULTY OF ENGINEERING, UNIVERSITY OF JAFFNA
Timetable for Fifth Semester (E/22 Batch) - Computer and Electrical Engineering
 Effective from 17th March 2025 to 4th July 2025

Time	Monday	Tuesday	Wednesday	Thursday	Friday
8.00 - 8.55	Laboratory	EC5110 (Computer Architecture and Organization) [Com & Ele] [CO4]	Laboratory	Laboratory	Laboratory
8.55 - 9.50					
9.50 - 10.10					
10.10 - 11.05					
11.05 - 12.00	EC5080 (Software Construction)[Com] [CO2]/ EC5050 (Power Electronics and Design) [Ele] [EE1]	Laboratory	ESU hour	EC5080 (Software Construction) [Com] [CO2]/ EC5050 (Power Electronics and Design) [Ele] [EE1]	General Tutor Session
12.00 - 13.00	Lunch Break				
13.00 - 13.55	EC5030 (Control Systems) [Com & Ele] [CO4]	EC5080 (Software Construction)[Com] [CO2]/ EC5040 (Electric Machines) [Ele] [EE1]	EC5020 (Analogue and Digital Communication) [Com & Ele] [CO4]	EC5030 (Control Systems) [Com & Ele] [CO4]	EC5110 (Computer Architecture and Organization) [Com & Ele] [CO4]
13.55 - 14.50	EC5070 (Database Systems) [Com] [CO2]/ EC5050 (Power Electronics and Design) [Ele] [EE1]	EC5010 (Digital Signal Processing) [Com & Ele] [CO4]	EC5030 (Control Systems) [Com & Ele] [CO4]	EC5010 (Digital Signal Processing) [Com & Ele][CO4]	EC5110 (Computer Architecture and Organization) [Com & Ele] [CO4]
14.50 - 15.10	Tea Break	Tea Break	Tea Break	Tea Break	Tea Break
15.10 - 16.05	EC5020 (Analogue and Digital Communication) [Com & Ele] [CO4]	EC5070 (Database Systems) [Com] [CO2]/ EC5040 (Electric Machines) [Ele] [EE1]	EC5010 (Digital Signal Processing) [Com & Ele] [CO4]	EC5020 (Analogue and Digital Communication) [Com & Ele] [CO4]	EC5070 (Database Systems) [Com] [CO2]/ EC5040 (Electric Machines) [Ele] [EE1]

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