Student Names: Emilcan Arıcan, M. Mustafa Küçük

Student IDs: 2016400231,2016400111

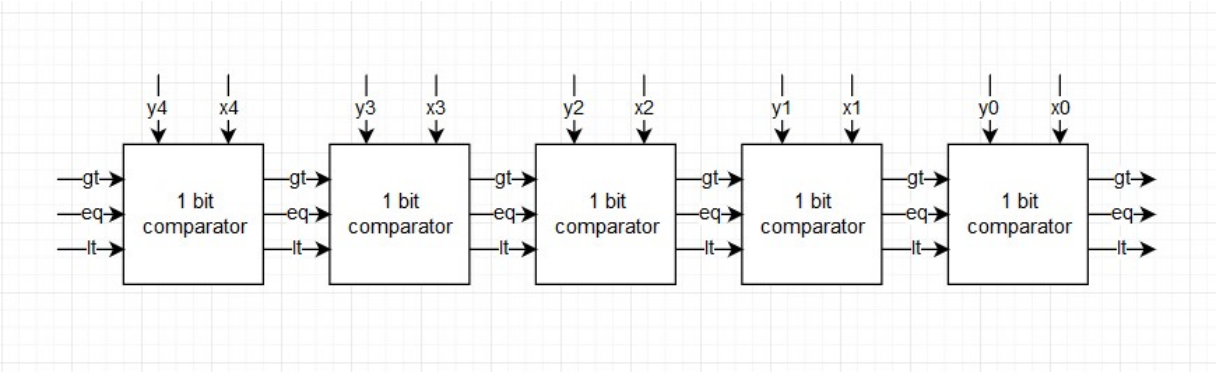
Session ID:21

Group ID:2

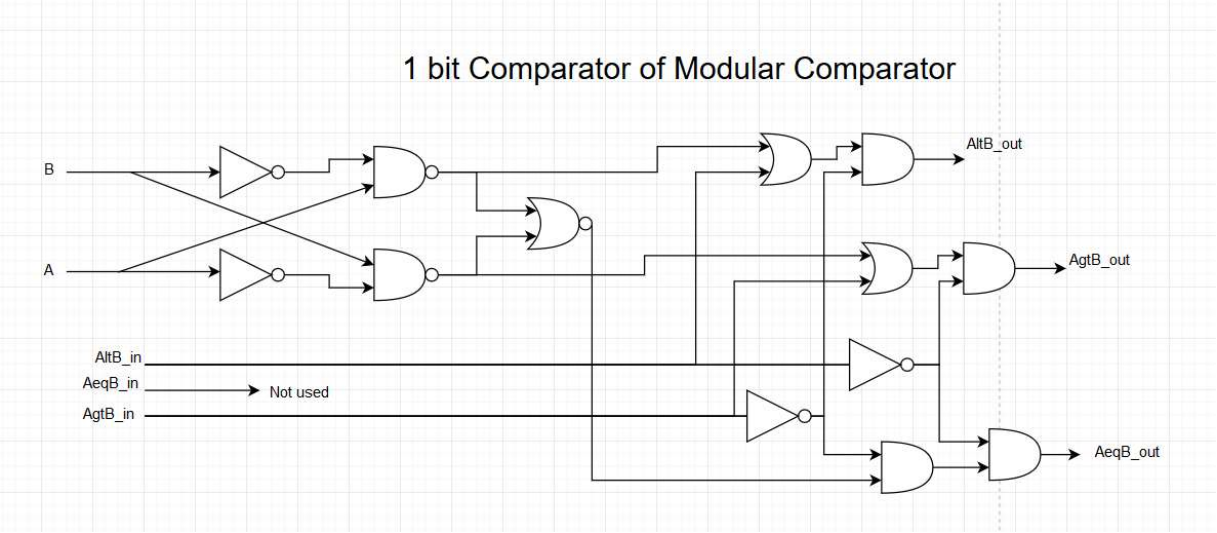
CMPE 240 Experiment 6 Preliminary Work

Step 1: Design circuits with minimum number of components for each function of the ALU

a)



Inside Designs:



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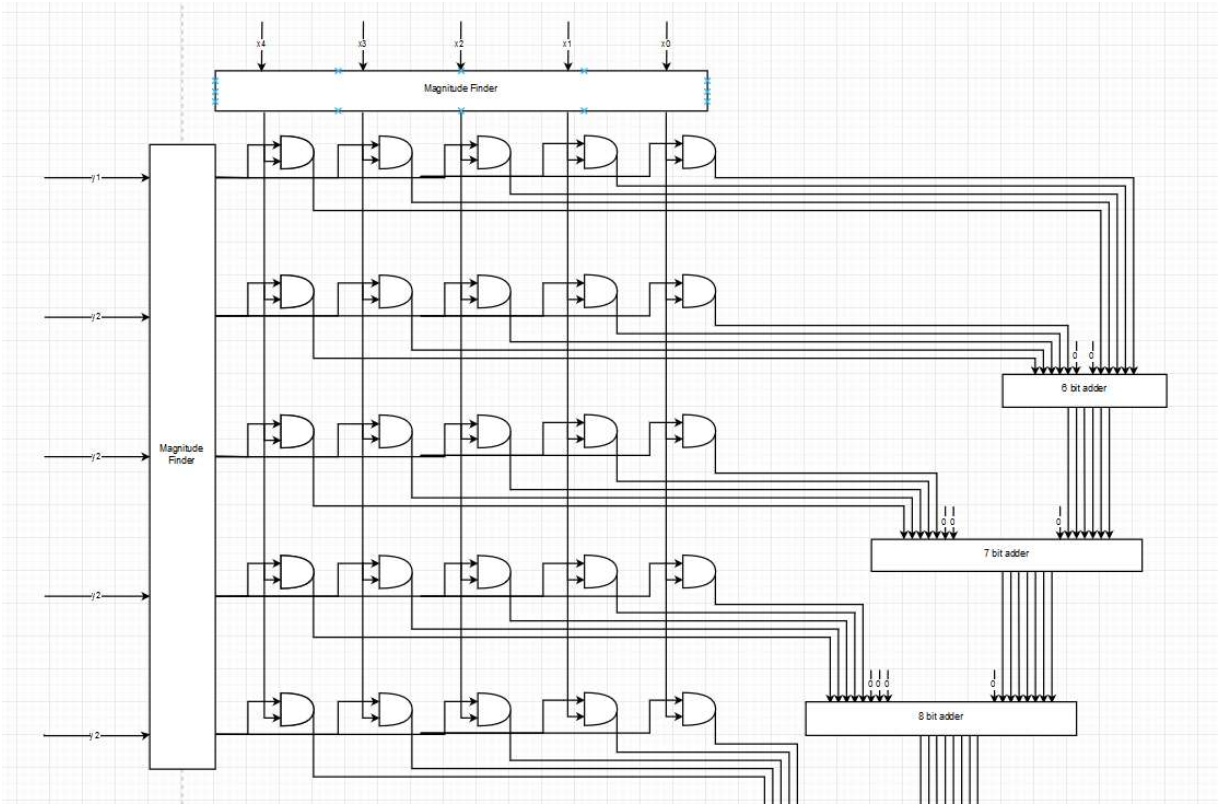
b)



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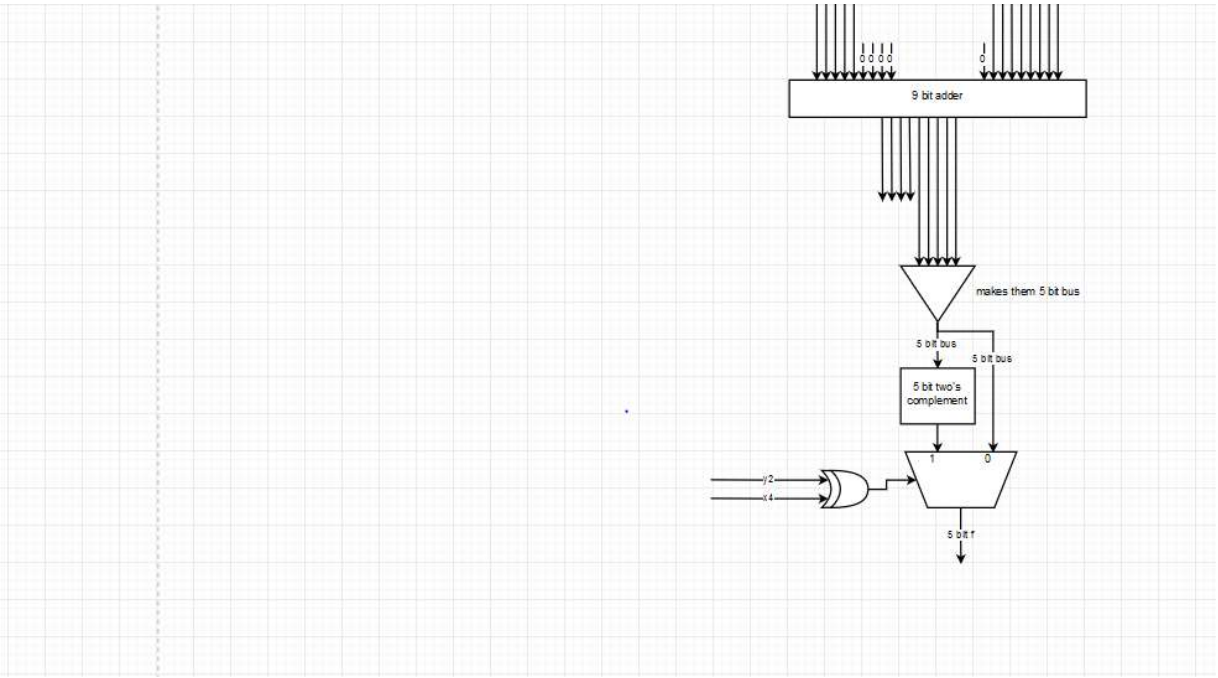
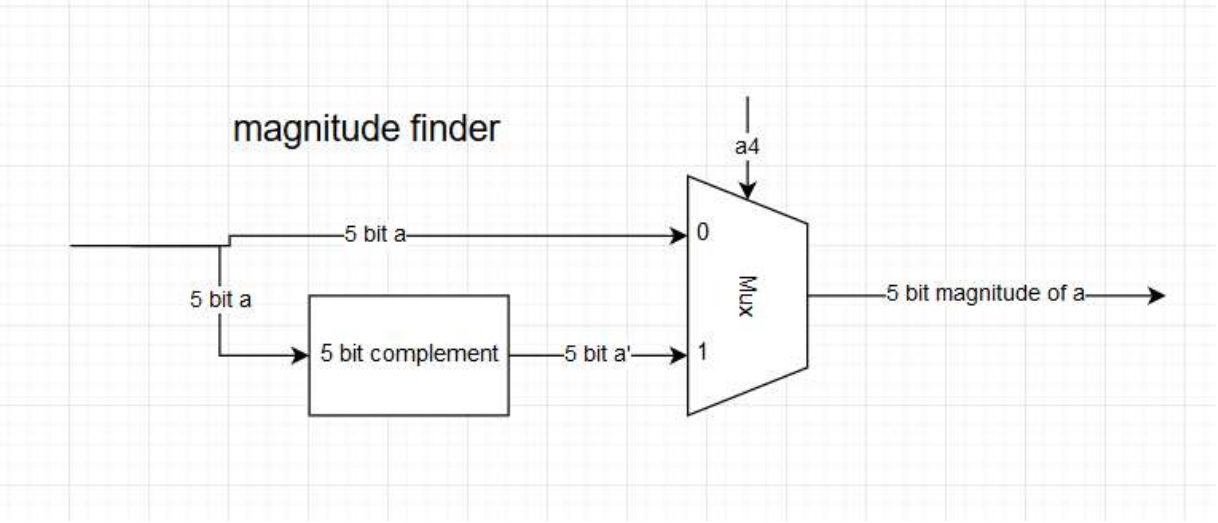


Inside Designs(multi bit adders are defined at section c)



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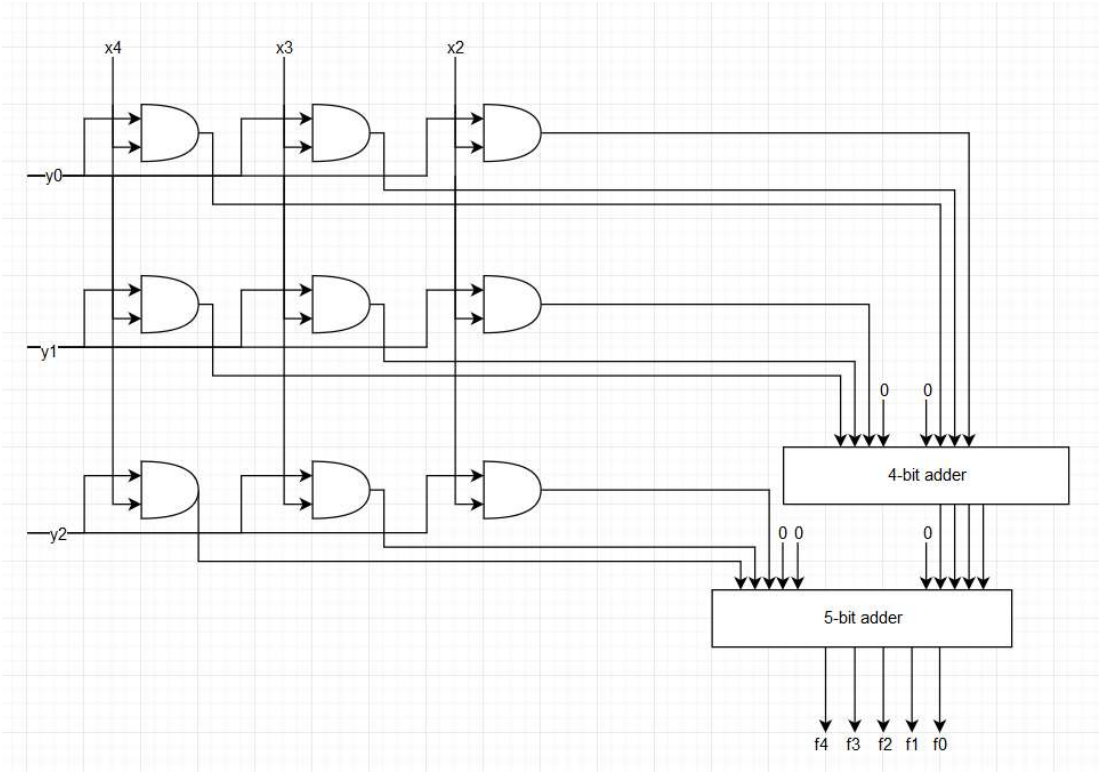
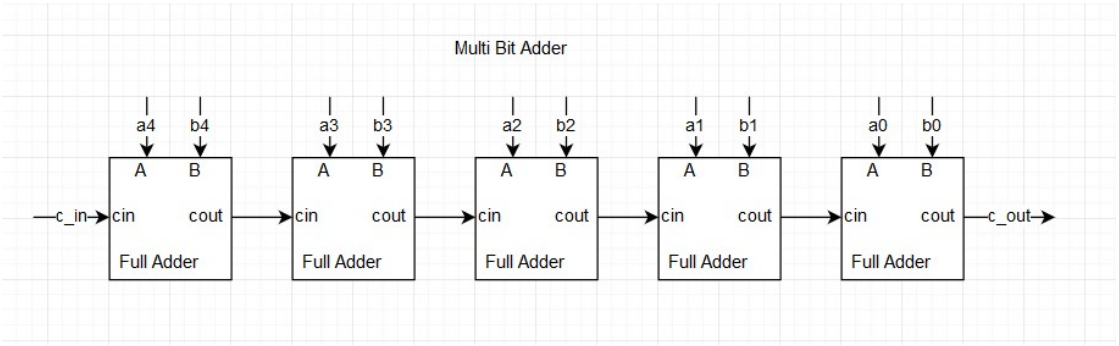
c)



Inside Designs:



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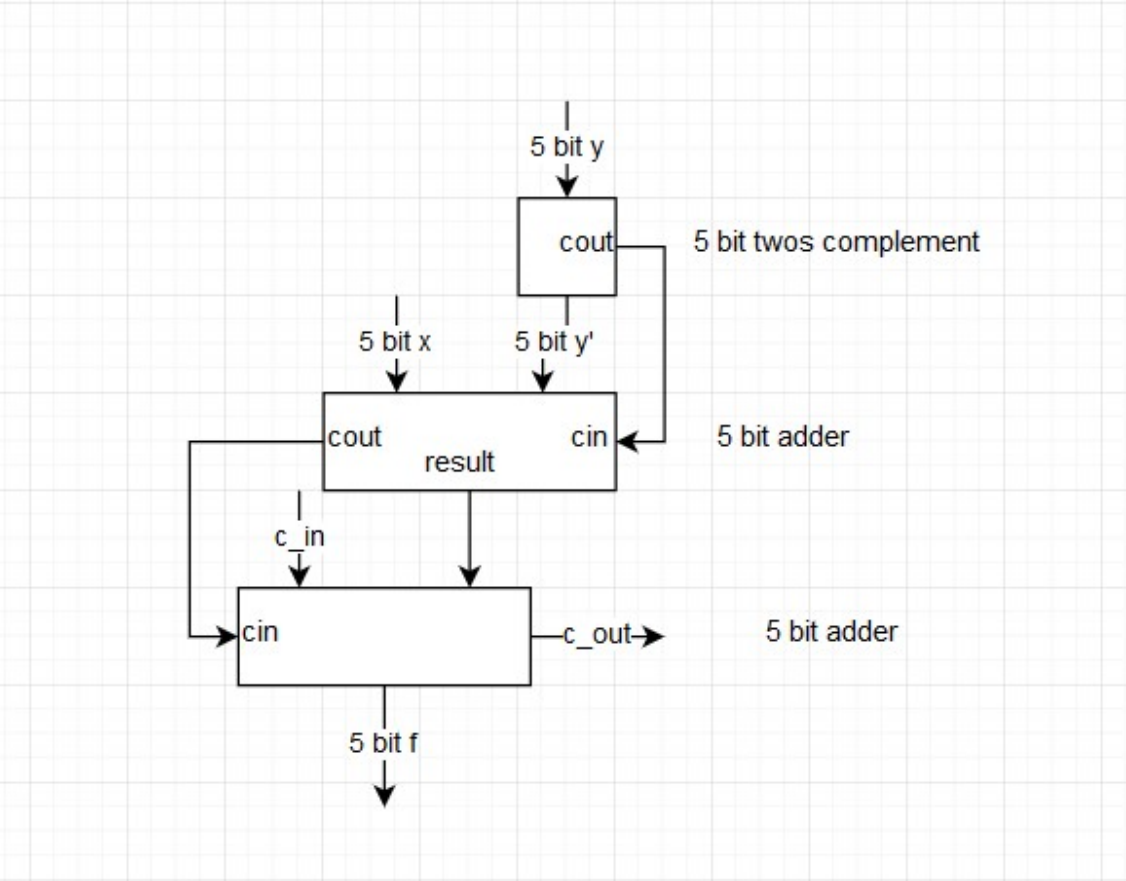
d)



Inside Designs: (5 bit adders mentioned before)

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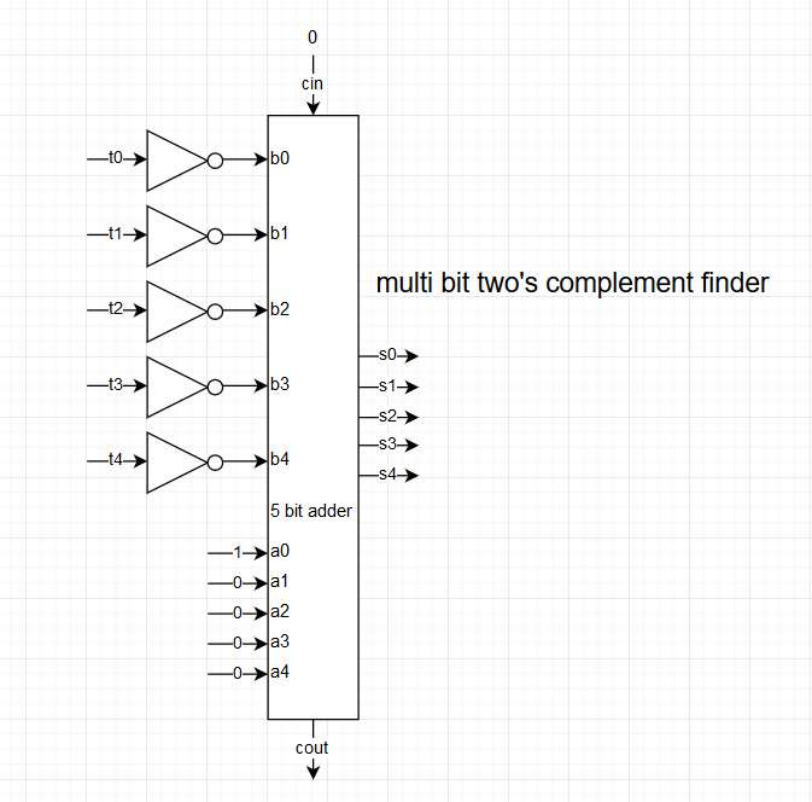

Step 2: **Merge all operations with select inputs and organize outputs, Try to minimize your implementation by using repetitions, draw final circuit as the minimized final design of the ALU.**

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