




A Division of 

Lumissil Microsystems Division of ISSI

IS31FL3737B vs. IS31FL3737A_3737

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History

Version	Date	Author	Description
Rev.A	2019.12.11		Initial

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Introduction

General Description

IS31FL3737B & IS31FL3737A are the advanced version of IS31FL3737, below table shows the different items:

ITEM	IS31FL3737	IS31FL3737A	IS31FL3737B																																																																																																																														
PWM Frequency	7.5kHz	7.5kHz (default) 25khz (PFS='1')	8.4kHz(default) 26.7kHz (PFS='010 ') 4.2kHz(PFS='001') 2.1kHz(PFS='011') 1.05kHz(PFS='100 ')																																																																																																																														
Auto Breath related Time	<table><tr><th>T1</th><th>T1 Setting</th></tr><tr><td>000</td><td>0.21s</td></tr><tr><td>001</td><td>0.42s</td></tr><tr><td>010</td><td>0.84s</td></tr><tr><td>011</td><td>1.68s</td></tr><tr><td>100</td><td>3.36s</td></tr><tr><td>101</td><td>6.72s</td></tr><tr><td>110</td><td>13.44s</td></tr><tr><td>111</td><td>26.88s</td></tr></table>	T1	T1 Setting	000	0.21s	001	0.42s	010	0.84s	011	1.68s	100	3.36s	101	6.72s	110	13.44s	111	26.88s	<table><tr><th>T1</th><th>T1 Setting</th><th>PFS='0'</th><th>PFS='1'</th></tr><tr><td>000</td><td>0.21s</td><td>0.07s</td><td></td></tr><tr><td>001</td><td>0.42s</td><td>0.14s</td><td></td></tr><tr><td>010</td><td>0.84s</td><td>0.28s</td><td></td></tr><tr><td>011</td><td>1.68s</td><td>0.56s</td><td></td></tr><tr><td>100</td><td>3.36s</td><td>1.12s</td><td></td></tr><tr><td>101</td><td>6.72s</td><td>2.24s</td><td></td></tr><tr><td>110</td><td>13.44s</td><td>4.48s</td><td></td></tr><tr><td>111</td><td>26.88s</td><td>8.96s</td><td></td></tr></table>	T1	T1 Setting	PFS='0'	PFS='1'	000	0.21s	0.07s		001	0.42s	0.14s		010	0.84s	0.28s		011	1.68s	0.56s		100	3.36s	1.12s		101	6.72s	2.24s		110	13.44s	4.48s		111	26.88s	8.96s		<table><tr><th>T1</th><th>T1 Setting</th><th>PFS</th><th>000(s)</th><th>010(s)</th><th>001(s)</th><th>011(s)</th><th>100(s)</th></tr><tr><td>000</td><td>0.21</td><td>0.07</td><td>0.42</td><td>0.84</td><td>1.68</td><td></td><td></td></tr><tr><td>001</td><td>0.42</td><td>0.14</td><td>0.84</td><td>1.68</td><td>3.36</td><td></td><td></td></tr><tr><td>010</td><td>0.84</td><td>0.28</td><td>1.68</td><td>3.36</td><td>6.72</td><td></td><td></td></tr><tr><td>011</td><td>1.68</td><td>0.56</td><td>3.36</td><td>6.72</td><td>13.44</td><td></td><td></td></tr><tr><td>100</td><td>3.36</td><td>1.12</td><td>6.72</td><td>13.44</td><td>26.88</td><td></td><td></td></tr><tr><td>101</td><td>6.72</td><td>2.24</td><td>13.44</td><td>26.88</td><td>53.76</td><td></td><td></td></tr><tr><td>110</td><td>13.44</td><td>4.48</td><td>26.88</td><td>53.76</td><td>107.52</td><td></td><td></td></tr><tr><td>111</td><td>26.88</td><td>8.96</td><td>53.76</td><td>107.52</td><td>215.04</td><td></td><td></td></tr></table>	T1	T1 Setting	PFS	000(s)	010(s)	001(s)	011(s)	100(s)	000	0.21	0.07	0.42	0.84	1.68			001	0.42	0.14	0.84	1.68	3.36			010	0.84	0.28	1.68	3.36	6.72			011	1.68	0.56	3.36	6.72	13.44			100	3.36	1.12	6.72	13.44	26.88			101	6.72	2.24	13.44	26.88	53.76			110	13.44	4.48	26.88	53.76	107.52			111	26.88	8.96	53.76	107.52	215.04		
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thermal shutdown function	None	None	<p>TSD_ADJ Thermal shutdown temperature adjust bit</p> <p>0 TSD = 160°C</p> <p>1 TSD = 160°C + 16°C</p> <p>TSD_SD Thermal shutdown function disable bit</p> <p>0 Thermal shutdown function enable</p> <p>1 Thermal shutdown function</p>																																																																																																																														

Replacing IS31FL3737 with IS31FL3737A/ IS31FL3737B should pay attention to the following points

Hardware

IS31FL3737 and IS31FL3737A/IS31FL3737B is pin to pin compatible,
IS31FL3737A can be fit in IS31FL3737 PCB layout.
IS31FL3737B can be fit in IS31FL3737A/IS31FL3737 PCB layout.

Software

IS31FL3737A keep the 7.5kHz as default (PFS='0'), same as IS31FL3737, but add 25kHz option (PFS='1').

IS31FL3737B keep the 8.4kHz as default (PFS='000'), but add 26.7kHz option (PFS='010') and more options.

Bit	D7:D6	D5:D3	D2	D1	D0
Name	SYNC	-	OSD	B_EN	SSD
Default	00	000	0	0	0

The Configuration Register sets operating mode of IS31FL3737.

Bit	D7:D6	D5	D4	D3	D2	D1	D0
Name	SYNC	-	PFS	-	OSD	B_EN	SSD
Default	00	0	0	0	0	0	0

The Configuration Register sets operating mode of IS31FL3737A.

Bit	D7:D6	D5:D3	D2	D1	D0
Name	SYNC	PFS	OSD	B_EN	SSD
Default	00	000	0	0	0

The Configuration Register sets operating mode of IS31FL3737B.

If PFS='1'(IS31FL3737A) or '010'(IS31FL3737B), all the auto breath related time(T1 T2 T3 T4) in IS31FL3737A/ IS31FL3737B will accelerate about 3.3 times base on time at 7.5kHz, which will affect ABM modes.

A. IS31FL3737A/ IS31FL3737B change some options to enhance pull up or down all the time

If IS31FL3737's setting is both in 32kΩ pull-up/pull-down in tNOL, no need to change the firmware.

ITEM	IS31FL3737	IS31FL3737A	IS31FL3737B
Frequency	FEh write 0xC5//unlock FDh write 0x03//write page 3 00h write 0x01//normal operation and default 7.5kHz	FEh write 0xC5//unlock FDh write 0x03//write page 3 00h write 0x11//normal operation and 25kHz	FEh write 0xC5//unlock FDh write 0x03//write page 3 00h write 0x21//normal operation and 26.7kHz
De-Ghost	FEh write 0xC5//unlock FDh write 0x03//write page 3 0Fh write 0x07//SWy 32kΩ pull-up in tNOL 10h write 0x07//CSx 32kΩ pull-down in tNOL	FEh write 0xC5//unlock FDh write 0x03//write page 3 0Fh write 0x07//SWy 32kΩ pull-up in tNOL 10h write 0x07//CSx 32kΩ pull-down in tNOL IS31FL3737A provide more enhanced options for de-ghost, check the PUR/PDR for more information.	FEh write 0xC5//unlock FDh write 0x03//write page 3 0Fh write 0x07//SWy 32kΩ pull-up in tNOL 10h write 0x07//CSx 32kΩ pull-down in tNOL IS31FL3737B provide more enhanced options for de-ghost, check the PUR/PDR for more information.

- B. IS31FL3737B has a Test Mode Data Register 4(same page as other Test Mode registers) stores thermal shutdown function bits and can raise the thermal shutdown temperature or shutdown the thermal shutdown function. This register is shown on IS31FL3737B datasheet.

Table 27_12h Test Mode Data Register 4

Bit	D7:D5	D4	D3:D1	D0
Name	-	TSD_ADJ	-	TSD_SD
Default	000	0	000	0

TSD_SD = “0”, thermal shutdown function is enabled,

TSD_SD = “1”, thermal shutdown function is disabled,

When TSD_ADJ = “0”, thermal shutdown temperature is 160°C,

When TSD_ADJ = “1”, thermal shutdown temperature is 160°C + 16°C.

Conclusion

IS31FL3737A is hardware/software compatible with IS31FL3737 but add additional option to speed up the PWM frequency to 25kHz and avoid the audible noise of MLCC.

IS31FL3737B is hardware/software compatible with IS31FL3737 but add additional option to speed up the PWM frequency to 26.7kHz and avoid the audible noise of MLCC, also, IS31FL3737B provide 4kHz, 2kHz and 1kHz PWM frequency options.

IS31FL3737B is hardware/software compatible with IS31FL3737A and provide 4kHz, 2kHz and 1kHz PWM frequency options.

IS31FL3737B can tune up thermal shutdown temperature 16°C, or shutdown the thermal shutdown function.