Midterm 1 - CIS 273 (Fall 2015) 2015 (5 points) 1. What does VHDL stand for? A. Virtual Hot Dog Lovers B. Very High Speed Integrated Circuit Hardware Description Language C. Voracious Hunters Denied Ladybugs 回常 **D.** Very High Downloads (7 points) 2. Which of the following is not a phase of the instruction cycle? A. Fetch B. Decode 回常回 X 等数 C. Execute 回常的 记录器 **D.** Potato (7 points) 3. Convert the decimal number 15 to binary.



回常回 答案符 A. 00001110



回常回 公司第 **B.** 00001111



回路回 系統 C. 00001001



**D.** 00001000

4. What is the number of this question?	(7 points)
回岸回 2500年 A. 1 回光行	
回常 <b>日</b> は表現 <b>B.</b> 2 回義状	
■常用 ※表示 C. 3 ■ 20:	
回新 10	
■ 1	