

## CSE 331L-1 - Introduction to Assembly Language

### Introduction:

: 2808 To assembly

In this session, you will be introduced to assembly language programming and to the emu8086 emulator software. The emu8086 will be used as both an editor and as an assembler for all your assembly language programming.

Steps required to run an assembly program:

1. Write the necessary assembly source code on 2808
2. Save the assembly source code.
3. Compile / Assembly source code to create machine code
4. Emulate / Run the machine code.

First, familiarize yourself with the software before you begin to write any code. Follow the in-class instructions regarding the layout of emu8086.

### Microcontrollers vs. Microprocessors:

- A microprocessor is a CPU on a single chip.
- If a microprocessor, its associated support circuitry, memory and peripheral I/O components are implemented on a single chip, it is a microcontroller.

### Features of 8086:

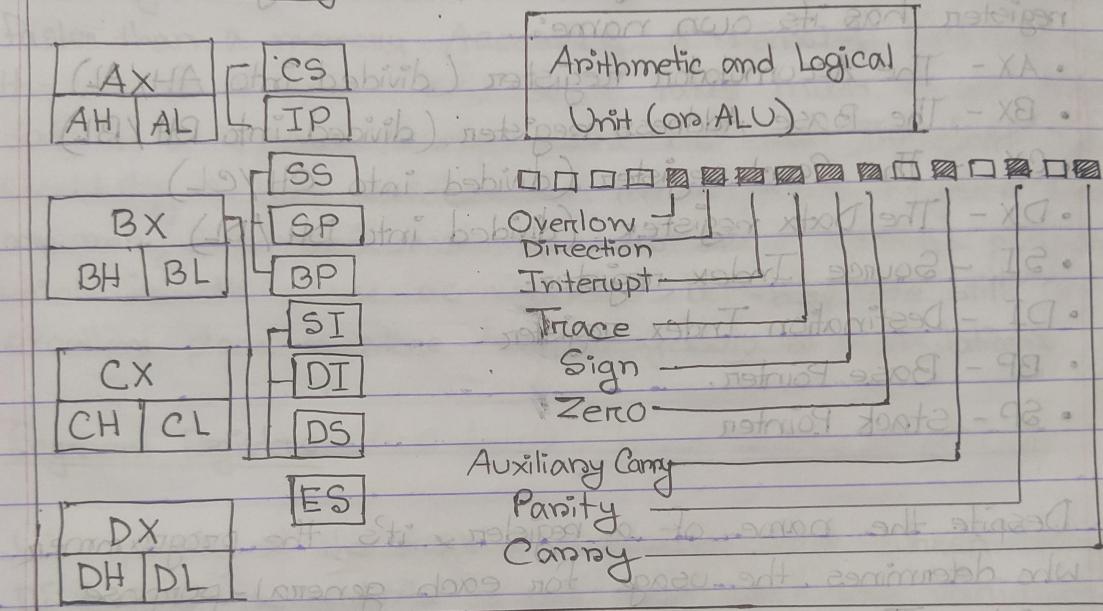
- 8086 is a 16bit processor. Its ALU, internal registers work with 16bit binary word.
- 8086 has a 16bit data bus. It can read or write data to a memory / port either 16bits or 8 bits at a time.
- 8086 has a 20bit address bus which means, it can address up to  $2^{20} = 1\text{MB}$  memory location.

### Registers - Register - Registers:

- Both ALU and FPU have a very small amount of super-fast private memory placed right next to them for their exclusive use. These are called registers.
- The ALU and FPU store intermediate and final results from their calculations in these registers.
- Processed data goes back to the data cache and then to the main memory from these registers.

Inside the CPU: Get to know the various registers:

### Central Processing Unit (on CPU)

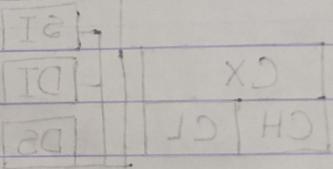


Registers are basically the CPU's own internal memory. They are used, among other purposes, to store temporary data while performing calculations. Let's look at each one in detail.

## General Purpose Registers (GPR)

The 8086 CPU has 8 general-purpose registers; each register has its own name:

- AX - The Accumulation register (divided into AH/AL)
- BX - The Base Address register (divided into BH/BL)
- CX - The Count register (divided into CH/CL)
- DX - The Data register (divided into DH/DL)
- SI - Source Index register
- DI - Destination Index register
- BP - Base Pointer
- SP - Stack Pointer



Despite the name of a register, it's the programmer who determines the usage for each general-purpose register. The main purpose of a register is to keep a number (variable). The size of the above registers

is 16 bits. The 4 general-purpose registers (AX, BX, CX, DX) are made of two separate 8-bit registers, for example if  $AX = 0011000011001b$ , then  $AH = 00110000b$  and  $AL = 0011001b$ . Therefore, when you modify any of the 8-bit registers, the 16-bit registers are also updated, and vice versa. The same is for other 3 registers.

"H" is for high and "L" is for low part not longer

Since registers are located inside the CPU, they are much faster than memory. Accessing a memory location requires the use of a system bus, so it takes much longer. Accessing data in a register usually takes no time. Therefore, you should try to keep variables in the registers. Register sets are very small and most registers have special purposes which limit their use as variables, but they are still an excellent place to store temporary data or calculations.

### Segment Registers.

CS - points at the segment containing the current program

DS - generally points at the segment where variables are defined.

ES - extra segment register, it's up to a coder to define its usage.

SS - points at the segment containing the stack.

Although it is possible to store any data in the segment registers, this is never a good idea. The segment registers have a very special purpose - pointing at accessible blocks of memory. This will be discussed further in upcoming classes.

### Special Purpose Registers

- IP - The Instruction Pointer, Points to the next location of instruction in the memory.
- Flags Register - Determines the current state of the microprocessor. Modified automatically by the CPU after some mathematical operations, determines certain types of results and determines how to transfer control of a program.

### Writing Your First Assembly Code

In order to write programs in assembly language, you will need to familiarize yourself with most, if not all of the instructions in the 8086-instruction set. This class will introduce two instructions and will serve as the basis for your first assembly program.

The following table shows the instruction name, the syntax of its use, and its description. The operands heading refers to the type of operands that can be used with the instruction along with their proper order.

CEE 3341 - 9 - Assembly Language

Instruction	Operands	Description
MOV	REG <sub>c</sub> , memory memory, REG <sub>c</sub> immediate, REG <sub>c</sub> immediate, memory	<ul style="list-style-type: none"> <li>• REG<sub>c</sub>: Any valid register.</li> <li>• Memory: Referring to a memory location in RAM.</li> <li>• Immediate: Using direct values.</li> </ul> <p>Copy Operand 2 to Operand 1</p> <p>for example: MOV AL, 50H</p> <ul style="list-style-type: none"> <li>• The MOV instruction cannot set the value of the CS and IP registers.</li> <li>• copy value of one segment register to another segment register (should copy to general register first)</li> <li>• copy an immediate value to segment register (should copy to general register first)</li> </ul>
ADD	REG <sub>c</sub> , memory memory, REG <sub>c</sub> REG <sub>c</sub> , memory immediate, REG <sub>c</sub> , immediate.	<ul style="list-style-type: none"> <li>• REG<sub>c</sub>, memory</li> <li>• memory, REG<sub>c</sub></li> <li>• REG<sub>c</sub>, memory</li> <li>• immediate, REG<sub>c</sub>, immediate.</li> </ul> <p>Adds two numbers.</p> <p>Algorithm: <math>Result = Operand1 + Operand2</math></p>