

## Continental Device India Limited

An ISO/TS16949 and ISO 9001 Certified Company



## NPN SILICON PLANAR SWITCHING TRANSISTORS



2N3903 2N3904 TO92 CBE

# **General Purpose Switching and Amplifier Applications**

ABSOLUTE MAXIMUM RATINGS(Ta=25 deg C unless otherwise specified)

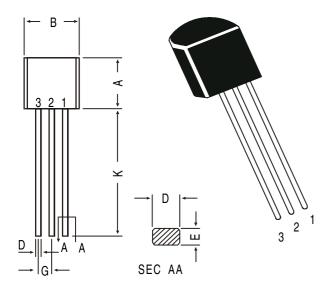
DESCRIPTION	SYMBOL	VALUE	UNITS	
Collector -Emitter Voltage	VCEO	40	V	
Collector -Base Voltage	VCBO	60	V	
Emitter -Base Voltage	VEBO	6.0	V	
Collector Current Continuous	IC	200	mA	
Power Dissipation@ Ta=25 degC	PD	625	mW	
Derate Above 25 deg C		5.0	mW/deg C	
Power Dissipation @ Tc=25 degC	PD	1.5	W	
Derate Above 25 deg C		12	mW/deg C	
Operating And Storage Junction	Tj, Tstg	-55 to +150	deg C	
Temperature Range	, ,		Ç	
THERMAL RESISTANCE				
Junction to Case	Rth(j-c)	83.3	deg C/W	
Junction to Ambient	Rth(j-a)	200	deg C/W	

**ELECTRICAL CHARACTERISTICS (Ta=25 deg C Unless Otherwise Specified)** 

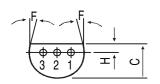
DESCRIPTION	SYMBOL	TEST CONDITION	2N3903	2N3904	UNITS
Collector -Emitter Voltage	VCEO	IC=1mA,IB=0	>40	>40	V
Collector -Base Voltage	VCBO	IC=10uA.IE=0	>60	>60	V
Emitter-Base Voltage	VEBO	IE=10uA, IC=0	>6.0	>6.0	V
Collector-Cut off Current	ICEX	VCE=30V, VEB=3V	<50	<50	nA
Base-Cut off Current	IBL	VCE=30V, VEB=3V	<50	<50	nA
DC Current Gain	hFE*	IC=0.1mA,VCE=1V	>20	>40	
		IC=1mA,VCE=1V	>35	>70	
		IC=10mA,VCE=1V	50-150	100-300	
		IC=50mA,VCE=1V	>30	>60	
		IC=100mA,VCE=1V	>15	>30	
<b>Collector Emitter Saturation Voltage</b>	VCE(Sat)*	IC=10mA,IB=1mA	<0.20	<0.20	V
		IC=50mA,IB=5mA	< 0.30	< 0.30	V
Base Emitter Saturation Voltage	VBE(Sat) *	IC=10mA,IB=1mA	0.65-0.85	0.65-0.85	V
		IC=50mA,IB=5mA	< 0.95	< 0.95	V

<b>ELECTRICAL CHARACTERISTICS (T</b>	a=25 deg C	Unless Otherwise Specified)	2N3903/2N		
DESCRIPTION	SYMBOL	TEST CONDITION	2N3903	2N3904	UNITS
SMALL SIGNAL CHARACTERISTICS					
Transistors Frequency	ft	IC=10mA, VCE=20V	>250	>300	MHz
		f=100MHz			
Out-Put Capacitance	Cob	VCB=5V, IE=0	<4.0	<4.0	pF
		f=1MHz			
Input Capacitance	Cib	VBE=0.5V, IC=0	<8.0	<8.0	pF
		f=1MHz			
		ALL f=1kHz			
Small Signal Current Gain	hfe	IC=1mA, VCE=10V	50-200	100-400	
Input Impedance	hie	IC=1mA, VCE=10V	1.0-8.0	1.0-10	khoms
Out put Adimttance	hoe	IC=1mA, VCE=10V	1.0-40	1.0-40	umhos
Voltage Feedback Ratio	hre	IC=1mA, VCE=10V	0.1-5.0	0.5-8.0	x10-4
Noise Figure	NF	IC=100uA, VCE=5V	<6.0	< 5.0	dB
		Rs=1kohm, f=1kHZ			
SWITCHING Time					
Delay time	td	VCC=3V,VBE=0.5V	<35	<35	ns
Rise time	tr	IC=10mA, IB1=1mA	<35	<35	ns
Storage time	ts	VCC=3V,IC=10mA	<175	<200	ns
Fall time	tf	IB1=1B2=1mA	<50	<50	ns

## **TO-92 Plastic Package**

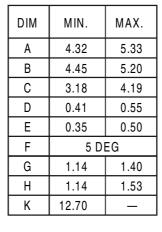


All diminsions in mm.

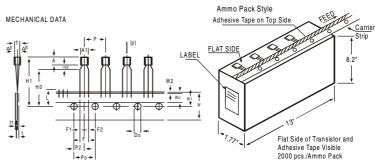


PIN CONFIGURATION

- 1. COLLECTOR
- 2. BASE
- 3. EMITTER



### **TO-92 Transistors on Tape and Ammo Pack**



#### All dimensions in mm unless specified otherwise

ITEM		SPECIFICATION					
ITEM	SYMBOL	MIN.	NOM.	MAX.	TOL.	REMARKS	
BODY WIDTH	A1	4.0		4.8			
BODY HEIGHT	Α	4.8		5.2			
BODY THICKNESS	T	3.9		4.2			
PITCH OF COMPONENT	Р		12.7		±1		
FEED HOLE PITCH	Po		12.7		±0.3	CUMULATIVE PITCH ERROR 1.0 mm/20 PITCH	
FEED HOLE CENTRE TO COMPONENT CENTRE	P2		6.35		±0.4	TO BE MEASURED A BOTTOM OF CLINCH	
DISTANCE BETWEEN OUTER					+0.6		
LEADS	F		5.08		-0.2		
COMPONENT ALIGNMENT	Δh		0	1		AT TOP OF BODY	
TAPE WIDTH	W		18 6		±0.5		
HOLD-DOWN TAPE WIDTH HOLE POSITION	Wo W1		9		±0.2 +0.7		
HOLL FOSITION	VV 1		9		-0.5		
HOLD-DOWN TAPE POSITION	W2		0.5		±0.2		
LEAD WIRE CLINCH HEIGHT	Ho		16		±0.5		
COMPONENT HEIGHT	H1			23.25			
LENGTH OF SNIPPED LEADS	L			11.0			
FEED HOLE DIAMETER	Do		4		±0.2		
TOTAL TAPE THICKNESS	t			1.2		t1 0.3 - 0.6	
LEAD - TO - LEAD DISTANCEF1,	F2		2.54		+0.4 -0.1		
CLINCH HEIGHT	H2			3	-0.1		
PULL - OUT FORCE	(P)	6N					

- NOTES

  1. MAXIMUM ALIGNMENT DEVIATION BETWEEN LEADS NOT TO BE GREATER THAN 0.2 mm.

  2. MAXIMUM NON-CUMULATIVE VARIATION BETWEEN TAPE FEED HOLES SHALL NOT EXCEED 1 mm IN 20 PITCHES.
- HOLDDOWN TAPE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NO EXPOSURE OF ADHESIVE.
- EXPOSURE OF ADHESIVE.

  4. NO MORE THAN 3 CONSECUTIVE MISSING COMPONENTS ARE PERMITTED.

  5. A TAPE TRAILER, HAVING AT LEAST THREE FEED HOLES ARE REQUIRED AFTER THE LAST COMPONENT.

  6. SPLICES SHALL NOT INTERFERE WITH THE SPROCKET FEED HOLES.

# **Packing Detail**

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-92 Bulk TO-92 T&A	1K/polybag 2K/ammo box	200 gm/1K pcs 645 gm/2K pcs	3" x 7.5" x 7.5" 12.5" x 8" x 1.8"	5.0K 2.0K	17" x 15" x 13.5" 17" x 15" x 13.5"	80.0K 32.0K	23 kgs 12.5 kgs

#### **Notes**

#### **Disclaimer**

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered Trademark of

Continental Device India Limited

C-120 Naraina Industrial Area, New Delhi 110 028, India.
Telephone + 91-11-2579 6150, 5141 1112 Fax + 91-11-2579 5290, 5141 1119

email@cdil.com www.cdilsemi.com