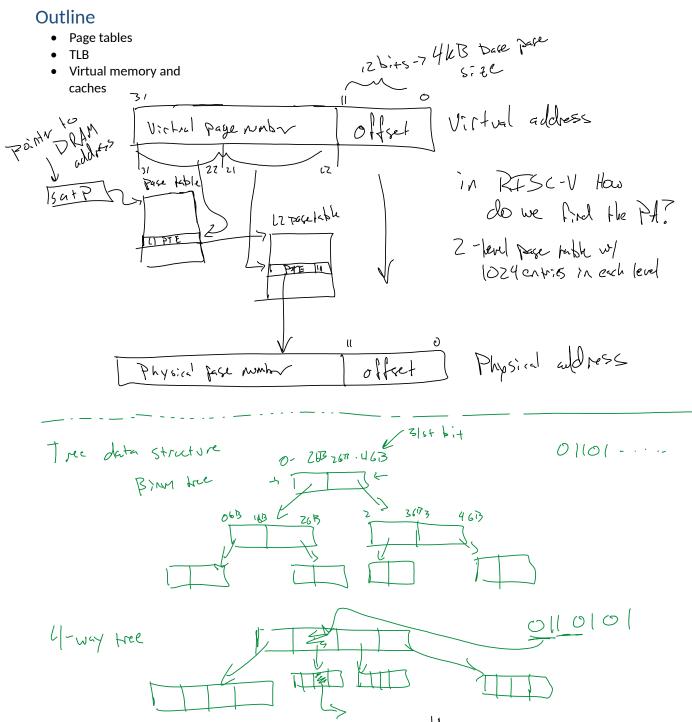
## Lecture 23: Virtual Memory 2

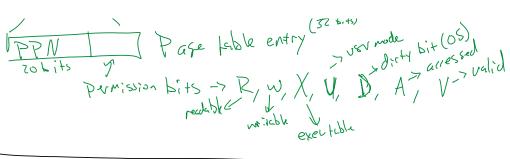
Monday, March 4, 2019 10:56 AM



1024-way tree 32th OI 23 CI Pau take 1025 & 10 bits

1024-way tree 32th OI 23 CI Pau take 1025 & 10 bits

10 bits take 1/evel 4 the 1/e



With virtual addressing > 3 Menony accesse pr W/st

1564 bit Virtual address spaces have 3-5 leads "pormi cache

How to improve performence?

15 CACHES

Address translation cache

Wary small and fast

Indivired

WPN PPN -> TLB (translation
lookaside
by Har)

16-32 entries

Ware to minimize missue-> highly associative often by associative

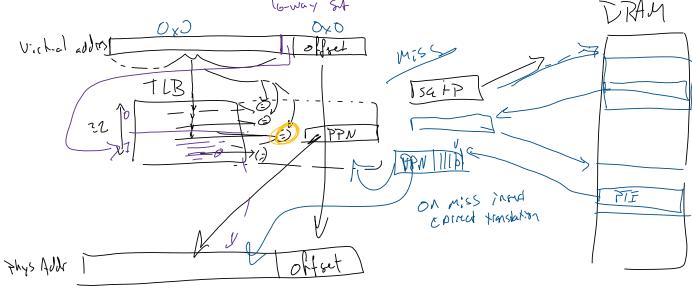
16-32 Oxo

Oxo

Oxo

Oxo

Oxo



if PTE is includer prission doesn't netch > page fault exception