HDD Controller

ADDR   IRQ5
DATA   DRQ3
RESET   DACK3
IOR
IOW
AEN

HDD.sch

U2C
4001

RESET

$\overline{\text{RESET}}$

8
9
10

+5V

U1E
4081
14
VDD
VSS
7

U4E
4081
14
VDD
VSS
7

U2E
4001
14
VDD
VSS
7

GND

GND

8

DATA

ADDR

U1A
4081
A19   1
A18   2
        3

U1B
4081
5
6
4

U2A
4001
A17   1
A16   2
        3

U1D
4081
12
13
11   ADDR_HI

U2B
4001
A15   5
A14   6
        4

U1C
4081
8
9
10

A13

A0
A1

U4A
4081
A9   1
A8   2
       3

U4B
4081
5
6
4

U2D
4001
A7   12
A6   13
       11

U4C
4081
8
9
10   ADDR_LO

A5

U3
ATmega328-PU

PD7   13
PD6   12
PD5   11
PD4   6
PD3   5
PD2   4
PD1   3
PD0   2

$\overline{\text{RESET}}$ 1 $\overline{\text{RESET}}$/PC6
A0   28   PC5
A1   27   PC4
ADDR_LO   26   PC3
ADDR_HI   25   PC2
$\overline{\text{IOR}}$ 24   PC1
$\overline{\text{IOW}}$ 23   PC0

AEN   10   XTAL2/PB7
IRQ5   9   XTAL1/PB6
19   PB5
18   PB4
17   PB3
16   PB2
15   PB1
14   PB0

DACK3   AVCC   20
DRQ3   VCC   7

AREF   21   +5V

+5V

+5V

RN1
50k

J1
SD_Card

DAT1   8
DAT0   7
VSS   6   GND
CLK   5
VDD   4   +5V
VSS   3   GND
CMD   2
CD/DAT3   1
DAT2   9

SHELL2   13
SHELL1   12

WRITE_PROTECT   11
CARD_DETECT   10

Sheet: /HDD Controller/
File: HDD.sch

Title:

Size: A4   Date:

Rev:

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