

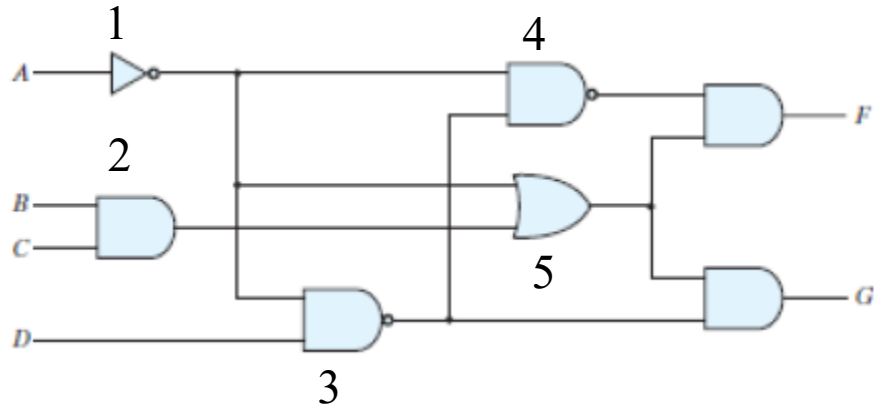
FALL 2020 COMP 206

Digital Logic Design

Assignment 2

Due on 9th Nov, 2020, 0900 am via Moodle

1. Obtain expression for the following circuit and obtain the truth table:
(Hint: Write expressions of every gate for simplicity)



2. Draw the circuit diagram for the following expression and obtain the truth table:

$$Y = (A + C)(AD + AD') + AC + C$$