Department of Computer Science
Forman Christian College
(A Chartered University)
Lahore

Digital Logic Design
COMP 206

DIGITAL LOGIC DESIGN
COMP 206
LAB 10- RUBRIX

DESCRIPTION	MARKS ALLOCATED
Attendance	5%
Proper handling of components, ICs and wiring	20%
Hardware wired completely(for all circuits)	30%
Veryfication	45%

Marks will be deducted in case if students have not completely and correctly filled the data tables.
Note that these marks are max in each category. We may assign less than the given percentage of marks in case students have not successfully completed all the requirements.
This lab is time constrained. Please note that you must finish your work and submitted duly filled handout to the lab engineer within given time.

LAB 10
MULTIPLEXER

Name:

Roll No:

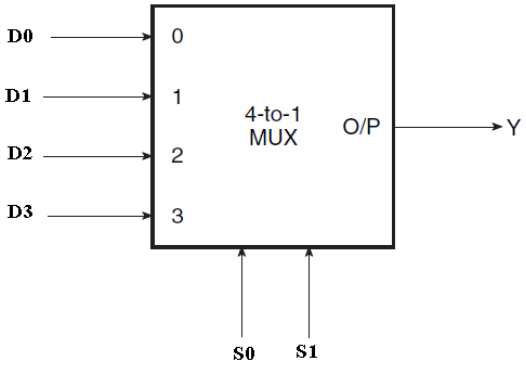
Date:

Learning Objectives:

By the end of this lab, you will be able to verify working of a 4x1 multiplexer and use it to implement a Boolean function

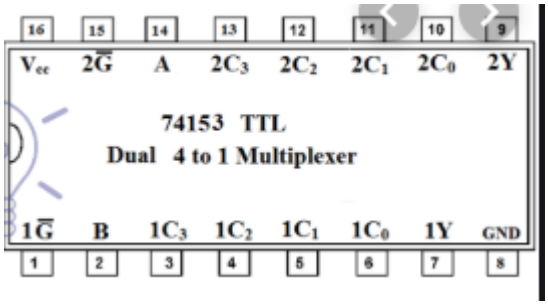
Background:

A multiplexer takes in several inputs but selects only a specific input to display at the output based on the select switches. The block diagram of a 4x1 MUX is as following.



X_1	X_0	Y
0	0	D0
0	1	D1
1	0	D2
1	1	D3

74153 is a dual 4x1 Multiplexer IC.



TASK1: Verify working of this multiplexer