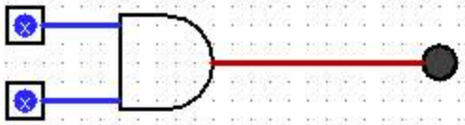
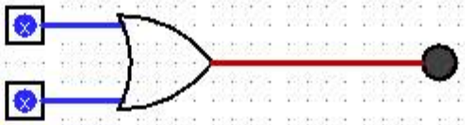


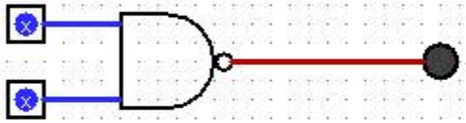
AND

Input A	Input B	Output Q
0	0	0
0	1	0
1	0	0
1	1	1



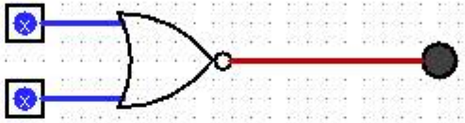
OR

Input A	Input B	Output Q
0	0	0
0	1	1
1	0	1
1	1	1



NAND

Input A	Input B	Output Q
0	0	1
0	1	1
1	0	1
1	1	0

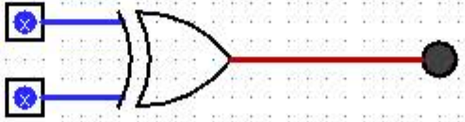


NOR

Input A	Input B	Output Q
0	0	1
0	1	0
1	0	0
1	1	0

NOT

Input A	Output A'
0	1
1	0



XOR

Input A	Input B	Output Q
0	0	0
0	1	1
1	0	1
1	1	0