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RUN1819 CPU by

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Functionality of the CPU:

We tested our implementation with different assembly programs, some more simple, some more complex.

Programs written by ourselves returned expected results

Programs by different authors sometimes presented unexpected behaviour when running with our CPU. We aren't entirely sure where the error lies. We suspect an issue with the flag system, more specific: the flag register bank.

Design choices:

We manage the PC with a constant (arithmetic instruction: $PC + 4$) in the instruction decoder. When initialising the CPU the PC increments, when it shouldn't, skipping the first instructions in the RAM. We fixed this by adding another constant (arithmetic instruction: $PC + 0$) that is only utilised during the first moments.