

PUSH-PULL FOUR CHANNEL DRIVERS

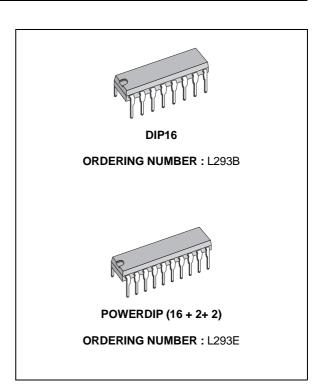
- OUTPUT CURRENT 1A PER CHANNEL
- PEAK OUTPUT CURRENT 2A PER CHANNEL (non repetitive)
- INHIBIT FACILITY
- HIGH NOISE IMMUNITY
- SEPARATE LOGIC SUPPLY
- OVERTEMPERATURE PROTECTION

DESCRIPTION

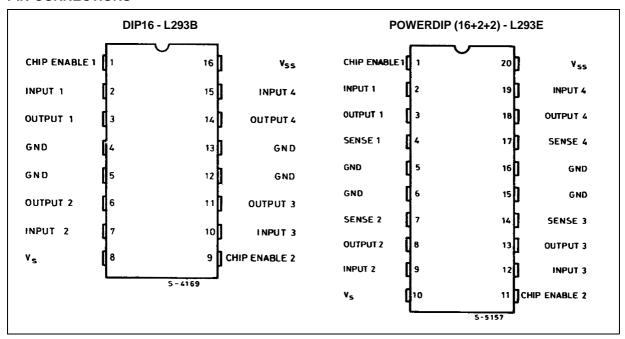
The L293B and L293E are quad push-pull drivers capable of delivering output currents to 1A per channel. Each channel is controlled by a TTL-compatible logic input and each pair of drivers (a full bridge) is equipped with an inhibit input which turns off all four transistors. A separate supply input is provided for the logic so that it may be run off a lower voltage to reduce dissipation.

Additionally, the L293E has external connection of sensing resistors, for switchmode control.

The L293B and L293E are package in 16 and 20-pin plastic DIPs respectively; both use the four center pins to conduct heat to the printed circuit board.

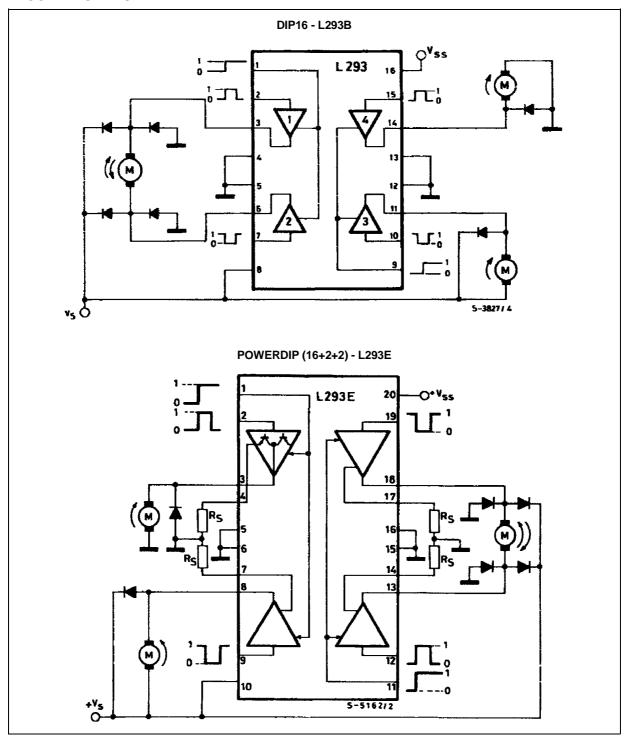


PIN CONNECTIONS

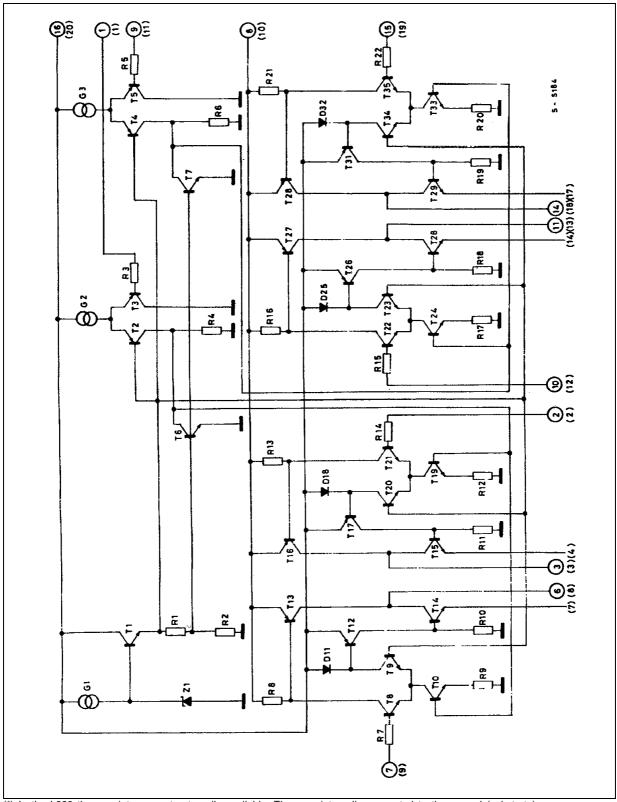


April 1993

BLOCK DIAGRAMS



SCHEMATIC DIAGRAM



(*) In the L293 these points are not externally available. They are internally connected to the ground (substrate). O Pins of L293 () Pins of L293E.

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
Vs	Supply Voltage	36	V
V _{ss}	Logic Supply Voltage	36	V
Vi	Input Voltage	7	V
V _{inh}	Inhibit Voltage	7	V
lout	Peak Output Current (non repetitive t = 5ms)	2	Α
P _{tot}	Total Power Dissipation at T _{ground-pins} = 80°C	5	W
T_{stg}, T_{j}	Storage and Junction Temperature	-40 to +150	°C

THERMAL DATA

Symbol	Parameter	Value	Unit
R _{th j-case}	Thermal Resistance Junction-case Max.	14	°C/W
R _{th j-amb}	Thermal Resistance Junction-ambient Max.	80	°C/W

ELECTRICAL CHARACTERISTICS

For each channel, V_S = 24V, V_{SS} = 5V, T_{amb} = 25°C, unless otherwise specified

Symbol	Parameter	Test Conditions	Min.	TYp.	Max.	Unit
Vs	Supply Voltage		V _{ss}		36	V
V_{ss}	Logic Supply Voltage		4.5		36	V
I _s	Total Quiescent Supply Current	$ \begin{array}{cccc} V_i = L & I_o = 0 & V_{inh} = H \\ V_i = H & I_o = 0 & V_{inh} = H \\ & V_{inh} = L \end{array} $		2 16	6 24 4	mA
I _{ss}	Total Quiescent Logic Supply Current	$ \begin{array}{cccc} V_i = L & I_o = 0 & V_{inh} = H \\ V_i = H & I_o = 0 & V_{inh} = H \\ & V_{inh} = L \end{array} $		44 16 16	60 22 24	mA
V_{iL}	Input Low Voltage		-03.		1.5	V
V_{iH}	Input High Voltage	$V_{SS} \le 7V$ $V_{SS} > 7V$	2.3 2.3		V _{ss} 7	V
I_{iL}	Low Voltage Input Current	V _{iI} = 1.5V			-10	μΑ
I_{iH}	High Voltage Input Current	$2.3V \le V_{IH} \le V_{ss} - 0.6V$		30	100	μΑ
V_{inhL}	Inhibit Low Voltage		-0.3		1.5	V
V_{inhH}	Inhibit High Voltage	$V_{SS} \le 7V$ $V_{SS} > 7V$	2.3 2.3		V _{ss} 7	V
I_{inhL}	Low Voltage Inhibit Current	$V_{inhL} = 1.5V$		-30	-100	μΑ
I_{inhH}	High Voltage Inhibit Current	$2.3V \le V_{inhH} \le V_{ss} - 0.6V$			±10	μΑ
V_{CEsatH}	Source Output Saturation Voltage	I _o = -1A		1.4	1.8	V
VcEsatL	Sink Output Saturation Voltage	I ₀ = 1A		1.2	1.8	V
V_{SENS}	Sensing Voltage (pins 4, 7, 14, 17) (**)				2	V
t _r	Rise Time	0.1 to 0.9 V _o (*)		250		ns
t _f	Fall Time	0.9 to 0.1 V _o (*)		250		ns
t _{on}	Turn-on Delay	0.5 V _i to 0.5 V _o (*)		750		ns
t _{off}	Turn-off Delay	0.5 V _i to 0.5 V _o (*)		200		ns

See figure 1

TRUTH TABLE

V _i (each channel)	Vo	V _{inh} ^(∞)
Н	Н	Н
L	L	Н
Н	X (°)	L
L	X (°)	L

Referred to L293E

^(*) High output impedance (**) Relative to the considerate channel

Figure 1: Switching Timers

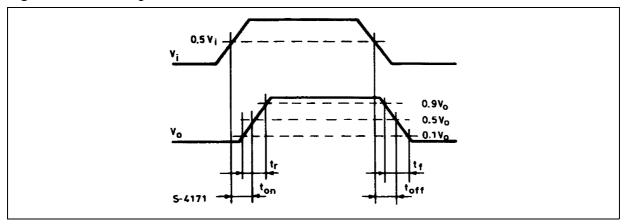


Figure 2: Saturation voltage versus Output Current

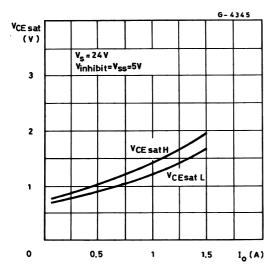


Figure 4: Sink Saturation Voltage versus Ambient Temperature

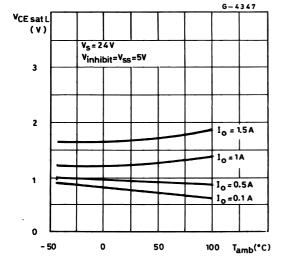


Figure 3: Source Saturation Voltage versus Ambient Temperature

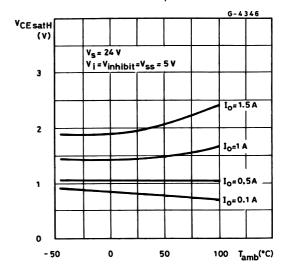


Figure 5: Quiescent Logic Supply Current versus Logic Supply Voltage

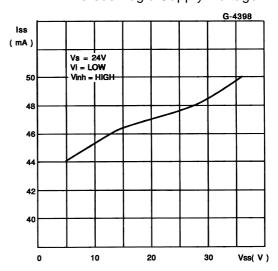


Figure 6 : Output Voltage versus Input Voltage

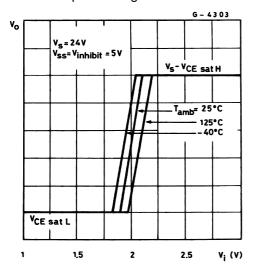
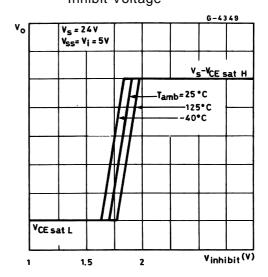
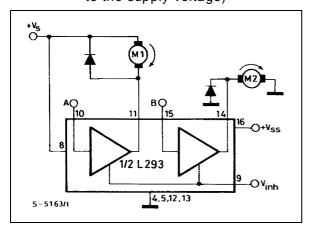


Figure 7: Output Voltage versus Inhibit Voltage



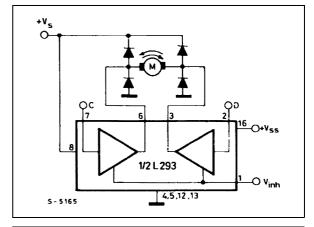
APPLICATION INFORMATION

Figure 8: DC Motor Controls (with connection to ground and to the supply voltage)



V _{inh}	Α	M1	В	M2
Н	Н	Fast Motor Stop	Н	Run
Н	L	Run	L	Fast Motor Stop
L	L X Free Running Motor Stop		Χ	Free Running Motor Stop
L = Low		H = High)	X = Don't Care

Figure 9: Bidirectional DC Motor Control



Inputs	Fund	ction
$V_{inh} = H$	C = H ; D = L	Turn Right
	C = L ; D = H	Turn Left
	C = D	Fast Motor Stop
V _{inh} = L	C = X ; D = X	Free Running Motor Stop
L = Low	H = High	X = Don't Care

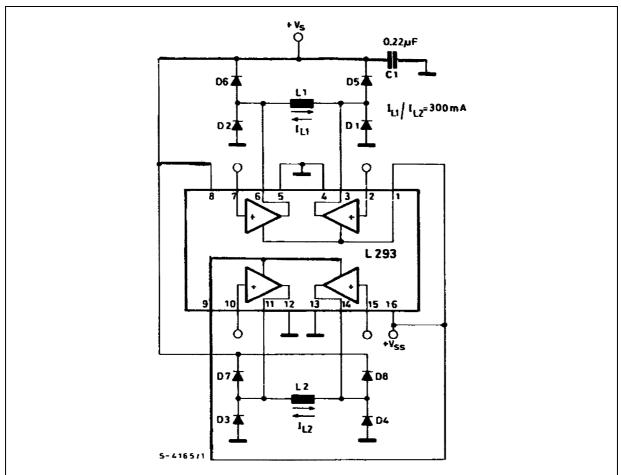


Figure 10 : Bipolar Stepping Motor Control

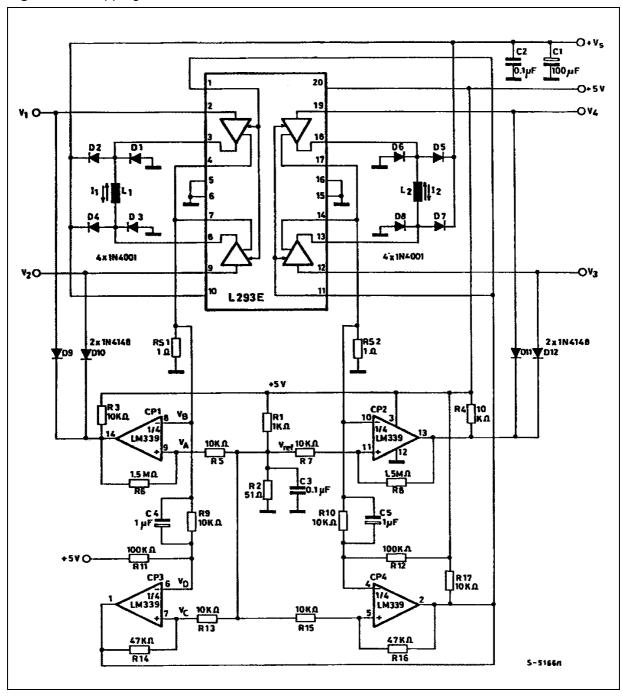
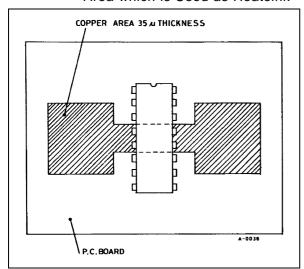


Figure 11: Stepping Motor Driver with Phase Current Control and Short Circuit Protection

MOUNTING INSTRUCTIONS

The R_{th j-amb} of the L293B and the L293E can be reduced by soldering the GND pins to a suitable copper area of the printed circuit board as shown in figure 12 or to an external heatsink (figure 13).

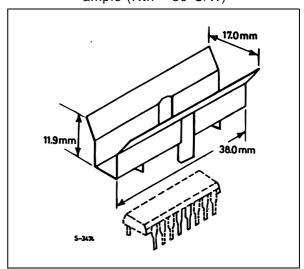
Figure 12 : Example of P.C. Board Copper Area which is Used as Heatsink



During soldering the pins temperature must not exceed 260°C and the soldering time must not be longer than 12 seconds.

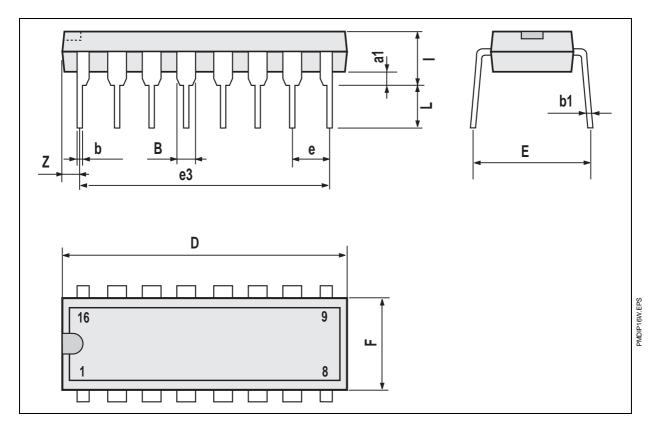
The external heatsink or printed circuit copper area must be connected to electrical ground.

Figure 13 :External Heatsink Mounting Example (Rth = 30°C/W)



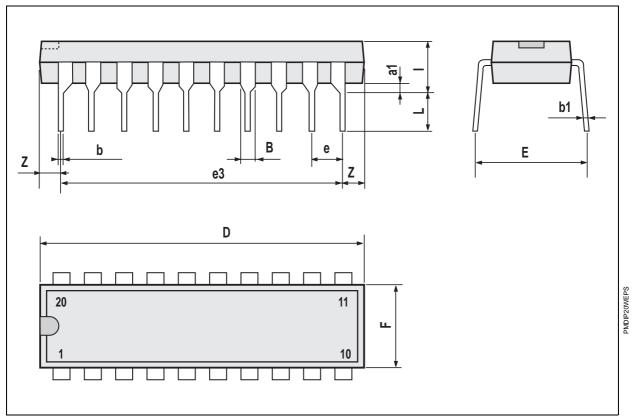
DIP16 PACKAGE MECHANICAL DATA

Dimensions	Millimeters			Inches			
	Min.	Тур.	Max.	Min.	Тур.	Max.	
a1	0.51			0.020			
В	0.77		1.65	0.030		0.065	
b		0.5			0.020		
b1		0.25			0.010		
D			20			0.787	I GE WIGS FOIL
Е		8.5			0.335		200
е		2.54			0.100		
e3		17.78			0.700		
F			7.1			0.280	
i			5.1			0.201	
L		3.3			0.130		
Z			1.27			0.050	



POWERDIP (16+2+2) PACKAGE MECHANICAL DATA

Dimensions	Millimeters			Inches			
	Min.	Тур.	Max.	Min.	Тур.	Max.	
a1	0.51			0.020			
В	0.85		1.4	0.033		0.055	
b		0.5			0.020		
b1	0.38		0.5	0.015		0.020	
D			24.8			0.976	DIP20PW TRI
Е		8.8			0.346		gucalu
е		2.54			0.100		
e3		22.86			0.900		
F			7.1			0.280	
i			5.1			0.201	
L		3.3			0.130		
Z			1.27			0.050	



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