

Lecture 05 - Signed Numbers / Character Codes / Float / BCD

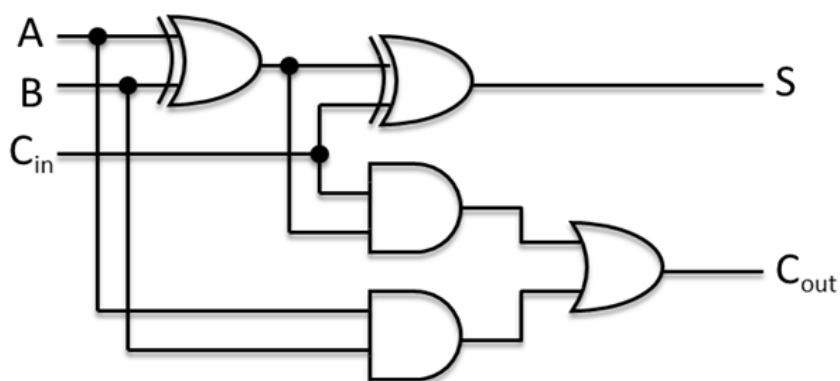
Representation of signed numbers (Chapter 2)

Logic Table for Addition:

A	B	C _i	s	Co
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1

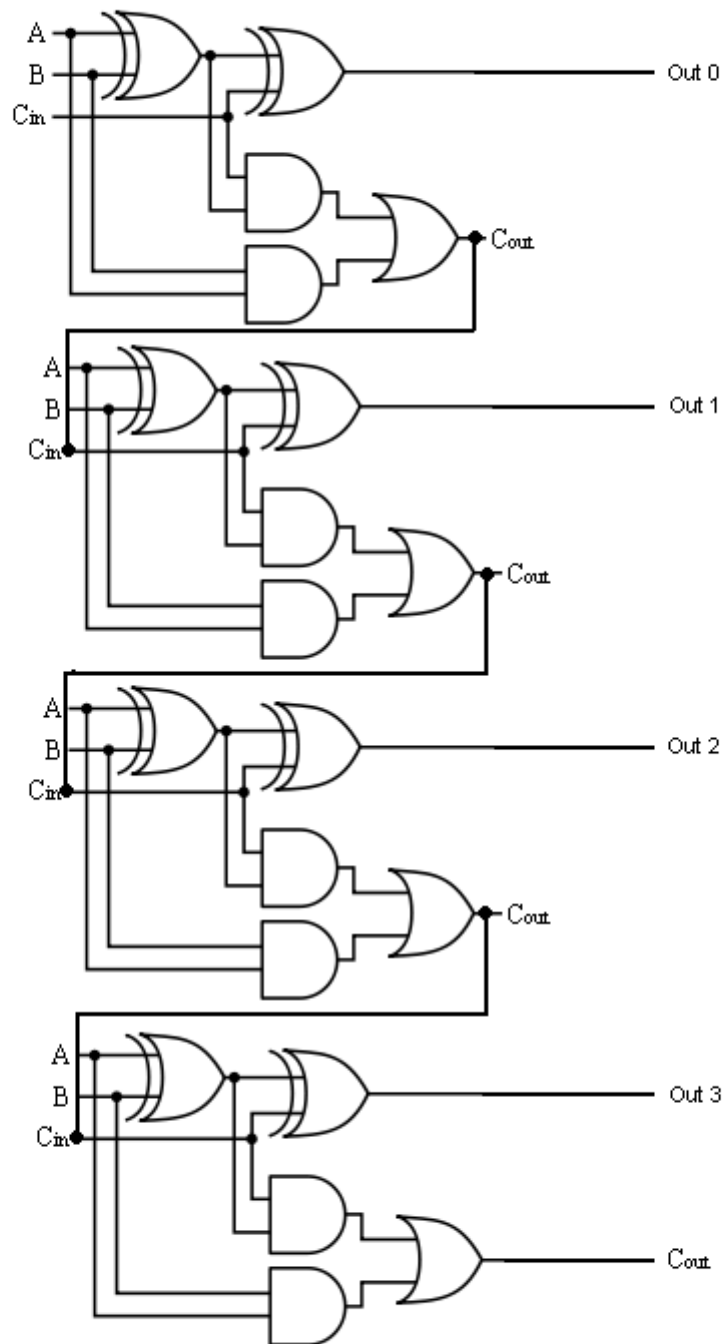
1 bit addr.

Implementable with some logic gates or a small lookup table.



Or with some small bit of memory to "lookup" the result. 3 addresses in - 2 bits out.

This can then be cascaded into:



Ones Compliment

Example 2.18 (from the textbook, p75):

23 + (-9)

```

0 0 0 0 1 0 0 1
1 1 1 1 0 1 1 0

```

9
-9

```

1 1 1      1 1
0 0 0 1 0 1 1 1
1 1 1 1 0 1 1 0
-----

```

Add

```

0 0 0 0 1 1 0 1
                1
0 0 0 0 1 1 1 0

```

Carry -- Carry out used later.
23[10]
-9
Result
(from carry above)
14[10]

Disadvantages - Ones Complement has a negative 0 value. Take the Ones Complement of 0

```

0[10] = 0 0 0 0 0 0 0 0
       = 1 1 1 1 1 1 1 1

```

In the land of IoT lots of Digital to Analog converters still use ones compliment.

RFC 791 p.14 defines the IP header checksum as:

"The checksum field is the 16 bit one's complement of the one's complement sum of all 16 bit words in the header. For purposes of computing the checksum, the value of the checksum field is zero."

A bunch of the Intel 64 and IA-32 instructions (SSE, SSE2, SS3 etc) have data "in 1's compliment form".

2's Complement

AMD's x86-64 architecture (known as AMD64, x86_64) extension to Intel's IA-32 brought 64 bit companion to the x86 world. Mac / Linux today run on this. Windows sort of runs on this - but - still has a throwback to 32 bit that is slow and incompatible. The x86-64 is 2's complement based. The only 1's complement based are the special Intel extensions.

2's complement solves the 2 zero problem by adding one.

23[10] is the same as before, now to represent -23 we compliment and add 1.

```

0 0 0 1 0 1 1 1
1 1 1 0 1 0 0 0
1 1 1 0 1 0 0 1

```

23[10]

compliment(23[10])
Add 1 (-23[10])

Let's do the same for -13

```

0 0 0 0 1 1 0 1
1 1 1 1 0 0 1 0
1 1 1 1 0 0 1 1

```

13[10]
compliment
Add 1 - with carry (-13[10])

Now add the 2 notative numbers:

```

  1 1      1
1 1 1 0 1 0 0 1
1 1 1 1 0 0 1 1
----- Add
1 1 0 1 1 1 0 0

```

Carry out 1
-23[10]
-13[10]

```

1 1 0 1 1 1 0 0
0 0 1 0 0 0 1 1
      1
0 0 1 0 0 1 0 0

```

Take compliment
23 hex
Add one
Hex 0x24 = 36 decimal, add back the Minus
giving us -36 or a crre

ZigZag Encoding

Google claims that it is faster to do this than other forms of encoding - and uses it in Protocol Buffers.

With ZigZag you map signed integers to a set of unsigned values(numbers) - alternating between positive and negative numbers. Assume that 0 is a positive number. Then zig-zag back and forth between 0, positive, -1, negative etc.

Value	Encoded as
0	0
-1	1
1	2
-2	3
2147483647	4294967294
-2147483648	4294967295

The computation to get from a number n to it's zig-zag form is (for 32 bit number):

IEEE-754 - 32 bit representation

1 Bit sign

8 bits exponent

23 significand

Error Prone: Comparision is never exact.

```
if ( 1 != 1.0 ) {  
    printf ( "True\n" )  
}  
if ( ( 1.0 / 10 ) != 0.1 ) {  
    printf ( "True\n" )  
}
```

The Correct way to compare floats is:

```
if ( abs(a-b) < epsilon ) {  
    printf ( "True\n" )  
}
```

Other floating formats

Oracle - Number(Size,Decimals) - Stored as a big float. All float calculations are done in software.

Packed Decimal

Gain accuracy - Loose in storage. Examples are the most common databases in existence, PosgreSQL, DB/2 Universal. Most banks also still use COBOL.

Store each digit in a byte - Example.

Store each digit in 4-bits - wasting 0xA .. 0xF or 40%.

Copyright

Copyright © University of Wyoming, 2019-2020.