Flex, Bison and the ACSE compiler suite

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COND statement

- Define tokens, syntax/semantic rules translating a COND statement
 - Default
 - meet-first: first case having positive condition is executed

```
cond {
   case x: x=0;
   case y>0: y=y+x; x=1;
   case x+y: x=0; y=0;
   default: y=0;
}
```

COND statement

```
cond {
   case x: x=0;
   case y>0: y=y+x; x=1;
   case x+y: x=0; y=0;
   default: y=0;
}
```

```
exp1
            beq L2
            cb_i1
            bt L_end
       L2:
            exp2
            beq L3
            cb_i2
            bt L_end
       L3:
            exp3
L_default:
            cb
             •••
```

L_end:

COND data structure

- Two labels
 - L_end: defined immediately when COND is recognized

 L_next: labels the next case block where to jump if the current exp is false

COND data structure

```
typedef struct
{
    t_axe_label *L_end;
    t_axe_label *L_next;
} t_cond_statement;
```

Axe_struct.h

COND syntactic

```
cond_statement: COND LBRACE
     cond_block RBRACE
cond_block: case_statements
     case_statements default_statement
case_statements: case_statement case_statement
       | case_statement
case_statement: CASE exp COLON {...}
       statements {...}
default_statement: DEFAULT COLON
   statements
```

COND stack

COND structures may occur in nested form

Each rule must know which is the current one

- As for SWITCH, we use a stack
 - condStack

COND statement

```
exp1
                                                                         beq L2
                                                                         cb i1
cond_statement: COND LBRACE {
           $1 = (t_cond_statement *)malloc(sizeof(t_cond_statement));
                                                                         bt L_end
                                                                   L2:
                                                                         exp2
           $1->label_end = newLabel(program);
                                                                         beq L3
           condStack = addFirst(condStack, $1);
                                                                         cb i2
   cond_block RBRACE
                                                                         bt L_end
           assignLabel(program,$1->label_end);
                                                                   L3:
           condStack = removeFirst(condStack);
                                                                         exp3
                                                           L_default:
                                                                         cb
                                                                         000
```

L end:

COND case

```
case_statement: CASE exp COLON {
  if ($2.expression_type == IMMEDIATE)
                                                                                    exp1
    gen_load_immediate(program, $2.value);
  else
                                                                                    beq L2
    gen andb instruction(program, $2.value, $2.value, $2.value, CG DIRECT ALL);
                                                                                   cb i1
  t axe label* l = newLabel(program);
  ((t_cond_statement *)LDATA(getElementAt(condStack,0)))->label_next = l;
                                                                                    bt L_end
  gen_beq_instruction (program, I, 0);
                                                                             L2:
                                                                                   exp2
statements {
  gen bt instruction(program,
                                                                                    beq L3
       ((t_cond_statement *)LDATA(getElementAt(condStack,0)))->label_end, 0);
                                                                                   cb i2
  assignLabel(program,
       ((t_cond_statement *)LDATA(getElementAt(condStack,0)))->label_next);
                                                                                    •••
                                                                                    bt L end
                                                                             L3:
                                                                                   exp3
                                                                                    •••
```

COND case

```
case_statement: CASE exp COLON {
  if ($2.expression_type == IMMEDIATE)
                                                                                    exp1
    gen_load_immediate(program, $2.value);
  else
                                                                                    beq L2
    gen andb instruction(program, $2.value, $2.value, $2.value, CG DIRECT ALL);
                                                                                    cb i1
  t axe label* I = newLabel(program);
  ((t_cond_statement *)LDATA(getElementAt(condStack,0)))->label_next = I;
                                                                                    bt L_end
  gen_beq_instruction (program, I, 0);
                                                                             L2:
                                                                                    exp2
statements {
  gen bt instruction(program,
                                                                                    beq L3
       ((t_cond_statement *)LDATA(getElementAt(condStack,0)))->label_end, 0);
                                                                                    cb i2
  assignLabel(program,
       ((t_cond_statement *)LDATA(getElementAt(condStack,0)))->label_next);
                                                                                    ...
                                                                                    bt L end
                                                                             L3:
                                                                                    exp3
                                                                                    ...
```

COND case

```
case_statement: CASE exp COLON {
  if ($2.expression_type == IMMEDIATE)
                                                                                    exp1
    gen_load_immediate(program, $2.value);
  else
                                                                                    beq L2
    gen andb instruction(program, $2.value, $2.value, $2.value, CG DIRECT ALL);
                                                                                    cb i1
  t axe label* I = newLabel(program);
                                                                                    •••
  ((t_cond_statement *)LDATA(getElementAt(condStack,0)))->label_next = I;
                                                                                    bt L_end
  gen_beq_instruction (program, I, 0);
                                                                             L2:
                                                                                    exp2
statements {
  gen_bt_instruction(program,
                                                                                    beq L3
       ((t_cond_statement *)LDATA(getElementAt(condStack,0)))->label_end, 0);
                                                                                    cb i2
  assignLabel(program,
       ((t cond statement *)LDATA(getElementAt(condStack,0)))->label next);
                                                                                    ...
                                                                                    bt L_end
                                                                             L3:
                                                                                    exp3
                                                                                    ...
```

COND

COND type

```
%union {
 int intval;
 char *svalue;
 t_axe_expression expr;
 t_axe_declaration *decl;
 t_list *list;
 t_axe_label *label;
 t_while_statement while_stmt;
 t_cond_statement *cond_stmt;
%token <cond_stmt> COND
```

COND lexer

"cond" {return COND;}

 Define tokens, syntax/semantic rules translating array equal operator over arrays

```
int v1[3];
int v2[3];
Int v3[5];
...
if (v1 =a= v2) ...
If (v1 =a= v3) ... // false!
```

 =a= is an operator then it must be defined as an expression exp

```
exp: ... | IDENTIFIER EQARRAY IDENTIFIER { ... }
```

First verify that both the IDs are array, otherwise error

exp:

```
| IDENTIFIER EQARRAY IDENTIFIER {
   t_axe_variable* id1 = getVariable(program, $1);
   if(!id1->isArray) exit(-1);
   t axe variable* id2 = getVariable(program, $3);
   if(!id2->isArray) exit(-1);
   if (id1->arraySize != id2->arraySize)
   $$ = create_expression(0, IMMEDIATE);
   else { ... }
```

```
int array size = id1->arraySize;
int i = getNewRegister(program);
int r = getNewRegister(program);
gen_addi_instruction(program, i, REG_0, 0);
t_axe_label* lcond = assignNewLabel(program);
gen_subi_instruction(program, r, i, id1->arraySize);
t_axe_label* lend = newLabel(program);
gen bge instruction(program, lend, 0);
t_axe_expression i_exp = create_expression(i, REGISTER);
int x = loadArrayElement(program, $1, i_exp);
int y = loadArrayElement(program, $3, i exp);
gen_sub_instruction(program, r, x, y, CG_DIRECT_ALL);
gen bne instruction(program, lend, 0);
gen_addi_instruction(program, i, i, 1);
gen bt instruction(program, lcond, 0);
assignLabel(program, lend);
gen notl instruction(program, r, r);
$$ = create expression(r, REGISTER);
```

 $i \leftarrow 0$

Lcond:

 $r \leftarrow i - arrSize$

bge Lend

 $x \leftarrow loadArrEl$

y ← loadArrEl

 $r \leftarrow x - y$

bne Lend

 $i \leftarrow i+1$ bt Lcond

 $| r \leftarrow notl(r)$

Lend:

Lexer

Token:

%token ... GTEQ EQARRAY

- If we use == instead of =a= a shift/reduce conflict is produced
 - %expect ... (sets the number of S/R to tolerate)

 Define construct allowing to assign each element of the destination array the result of an expression evaluated on the value of the corresponding element of the source array

```
int i, x[5], y[7];

x[0] = 1; x[1] = 2; x[2] = 3;
x[3] = 4; x[4] = 5;

// y = {-2, 1, 6, 13, 22, undef, undef}
y = [i * i - 3 for i in x];
```

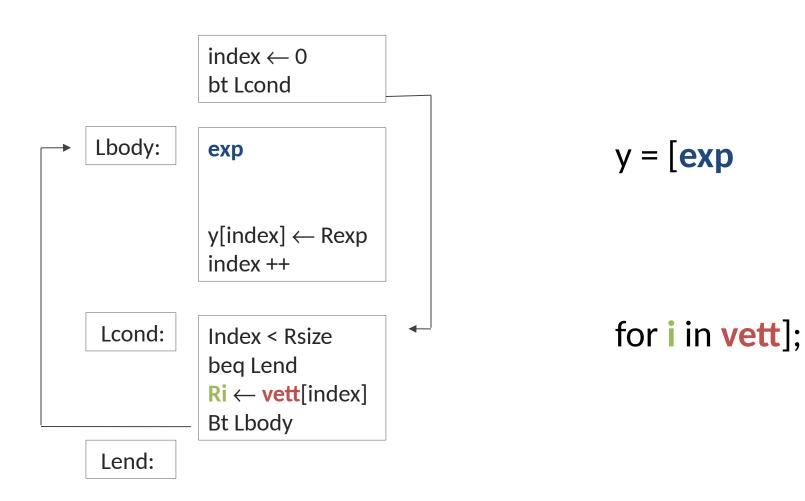
The vector ID is in the last position

$$y = [i * i - 3 \text{ for } i \text{ in } vett];$$

 To retrieve its size, jump to the end to initialize the loop

Lbody: ...

Rsize \leftarrow min{size(y), size(x)} $R \leftarrow x[0]$ Bt Lbody



- We use a global variable to store
 - Lcond, Lbody
 - The index register

```
struct {
  t_axe_label *Lbody;
  t_axe_label *Lcond;
  int index_reg;
} ac;
```

%token IN

```
y = [exp for i in vett];
assign statement: ...
 IDENTIFIER ASSIGN
   ac.index_reg = gen_load_immediate(program, 0);
   ac.Lcond = newLabel(program);
                                                                           index \leftarrow 0
   gen_bt_instruction(program, ac.Lcond, 0);
                                                                           bt Lcond
   ac.Lbody = assignNewLabel(program);
                                                                Lbody:
                                                                           exp
 LSQUARE exp
   int index_reg = array_compr.index_reg;
   t_axe_expression index_expr = create_expression(index_reg, REGISTER);
   storeArrayElement(program, $1, index expr, $5);
                                                                           y[index] \leftarrow Rexp
   gen_addi_instruction(program, index_reg, index_reg, 1);
                                                                           index ++
```

```
y = [exp for i in vett];
assign_statement: ...
 IDENTIFIER ASSIGN
   ac.index_reg = gen_load_immediate(program, 0);
   ac.Lcond = newLabel(program);
                                                                           index \leftarrow 0
   gen_bt_instruction(program, ac.Lcond, 0);
                                                                           bt Lcond
   ac.Lbody = assignNewLabel(program);
                                                                Lbody:
                                                                           exp
 LSQUARE exp
   int index_reg = array_compr.index_reg;
   t_axe_expression index_expr = create_expression(index_reg, REGISTER);
   storeArrayElement(program, $1, index expr, $5);
                                                                           y[index] \leftarrow Rexp
   gen_addi_instruction(program, index_reg, index_reg, 1);
                                                                           index ++
```

```
y = [exp for i in vett];
assign statement: ...
 IDENTIFIER ASSIGN
   ac.index_reg = gen_load_immediate(program, 0);
   ac.Lcond = newLabel(program);
                                                                             index \leftarrow 0
   gen_bt_instruction(program, ac.Lcond, 0);
                                                                             bt Lcond
   ac.Lbody = assignNewLabel(program);
                                                                 Lbody:
                                                                             exp
 LSQUARE exp
   t_axe_expression index_expr = create_expression(ac.index_reg, REGISTER);
   storeArrayElement(program, $1, index_expr, $5);
   gen addi instruction(program, index reg, index reg, 1);
                                                                             y[index] \leftarrow Rexp
                                                                             index ++
```

```
y = [exp for i in vett];
FOR IDENTIFIER IN IDENTIFIER RSQUARE
 t axe variable *dest = getVariable(program, $1);
 t_axe_variable *iv = getVariable(program, $8);
 t axe variable *src = getVariable(program, $10);
 if (!dest->isArray | | iv->isArray | | !src->isArray)
  exit(-1);
 t_axe_expression min_size = create_expression(dest->arraySize < src->arraySize ?
                                                 dest->arraySize : src->arraySize,
                                                 IMMEDIATE);
 int iv_reg = get_symbol_location(program, $8, 0);
 t axe expression index expr = create expression(ac.index reg, REGISTER);
```

```
index \leftarrow 0
t_axe_label *Lend = newLabel(program);
                                                                         bt Lcond
assignLabel(program, ac.Lcond);
                                                             Lbody:
                                                                         exp
t_axe_expression cmp =
  handle_binary_comparison(program, index_expr, min_size, _LT_);
                                                                         y[index] \leftarrow Rexp
                                                                         index ++
gen_beq_instruction(program, Lend, 0);
int elem = loadArrayElement(program, $10, index_expr);
gen_addi_instruction(program, iv_reg, elem, 0);
gen_bt_instruction(program, ac.Lbody, 0);
                                                                         Index < Rsize
                                                              Lcond:
assignLabel(program, Lend);
                                                                         beg Lend
free($1);
                                                                         Rel←vett[index]
free($8);
                                                                         Ri \leftarrow Rel + 0
free($10);
                                                                         bt Lbody
                                                              Lend:
```

Map applies in-place transformation to the elements of an array. For each element, the code block representing the transformation, which is suffixed to **as** keyword is executed and finally the processed array element is written back to its location.

```
int vett[100];
int elem;

// vett = {2, -10, 9}
map elem on vett as {
    elem = elem * 2;
}
// now vett = {4, -20, 18}
```

map elem on vett as

 $Ri \leftarrow size(vet)$

Lbody:

 $\textbf{Relem} \leftarrow \textbf{vett}[Ri]$

code_block

cb_i1

cb_in

vett[Ri]← Relem Ri ← Ri-1 bge Lbody

```
map_statement : MAP IDENTIFIER ON IDENTIFIER AS {
 t_axe_variable *var_elem = getVariable(program, $2);
 t axe variable *var arr = getVariable(program, $4);
 int elem_reg = get_symbol_location(program, $2, 0);
                                                                       R1 \leftarrow size(vet)
 $1 = gen_load_immediate(program, var_arr->arraySize - 1);
 $3 = assignNewLabel(program);
                                                            Lbody:
                                                                       Rtmp \leftarrow vet[ive]
 t axe expression ive = create expression($1, REGISTER);
                                                                       Relem ← Rtmp+0
 int tmp = loadArrayElement(program, $4, ive);
                                                                       cb_i1
 gen_add_instruction(..., elem_reg, REG_0, tmp, CG_DIRECT_ALL);
} code block {
 int elem_reg = get_symbol_location(program, $2, 0);
                                                                       cb_in
 t axe expression ive = create expression($1, REGISTER);
 t axe expression elem = create expression(elem reg, REGISTER);
                                                                       vet[ive]← Relem
 storeArrayElement(program, $4, ive, elem);
                                                                       Ri \leftarrow Ri-1
 gen_subi_instruction(program, $1, $1, 1);
                                                                       bge Lbody
 gen_bge_instruction(program, $3, 0);
```

free(\$2): free(\$4): }

```
map_statement : MAP IDENTIFIER ON IDENTIFIER AS {
t_axe_variable *var_elem = getVariable(program, $2);
 t axe variable *var arr = getVariable(program, $4);
 int elem_reg = get_symbol_location(program, $2, 0);
                                                                       R1 \leftarrow size(vet)
 $1 = gen_load_immediate(program, var_arr->arraySize - 1);
 $3 = assignNewLabel(program);
                                                            Lbody:
                                                                       Rtmp \leftarrow vet[ive]
t_axe_expression ive = create_expression($1, REGISTER);
                                                                       Relem ← Rtmp+0
 int tmp = loadArrayElement(program, $4, ive);
                                                                       cb_i1
 gen_add_instruction(..., elem_reg, REG_0, tmp, CG_DIRECT_ALL);
} code block {
 int elem_reg = get_symbol_location(program, $2, 0);
                                                                       cb in
 t axe expression ive = create expression($1, REGISTER);
 t axe expression elem = create expression(elem reg, REGISTER);
                                                                       vet[ive]← Relem
 storeArrayElement(program, $4, ive, elem);
                                                                       Ri \leftarrow Ri-1
 gen_subi_instruction(program, $1, $1, 1);
                                                                       bge Lbody
 gen_bge_instruction(program, $3, 0);
```

free(\$2): free(\$4): }

```
map_statement : MAP IDENTIFIER ON IDENTIFIER AS {
 t_axe_variable *var_elem = getVariable(program, $2);
 t axe variable *var arr = getVariable(program, $4);
 int elem_reg = get_symbol_location(program, $2, 0);
                                                                       Rive \leftarrow size(vet)
 $1 = gen_load_immediate(program, var_arr->arraySize - 1);
 $3 = assignNewLabel(program);
                                                            Lbody:
                                                                       Rtmp \leftarrow vet[ive]
 t_axe_expression ive = create_expression($1, REGISTER);
                                                                       Relem ← Rtmp+0
 int tmp = loadArrayElement(program, $4, ive);
                                                                       cb_i1
 gen_add_instruction(..., elem_reg, REG_0, tmp, CG_DIRECT_ALL);
} code block {
 int elem_reg = get_symbol_location(program, $2, 0);
                                                                       cb_in
 t axe expression ive = create expression($1, REGISTER);
 t_axe_expression elem = create_expression(elem_reg, REGISTER);
                                                                       vet[ive]← Relem
 storeArrayElement(program, $4, ive, elem);
                                                                       Rive \leftarrow Rive-1
 gen_subi_instruction(program, $1, $1, 1);
                                                                       bge Lbody
 gen_bge_instruction(program, $3, 0);
```

free(\$2); free(\$4); }

- %token <intval> MAP
- %token AS

```
control_statement : ...| map_statement.
```

Reduce applies a function reducing the elements of an array into a single scalar (e.g. computing their average). The result, held in the support variable specified after **into** keyword, is updated computing the reduction expression, enclosed between double square braces, for each element of the array.

```
int vett[100];
int elem, t, sum;

read(t);
sum = 0;

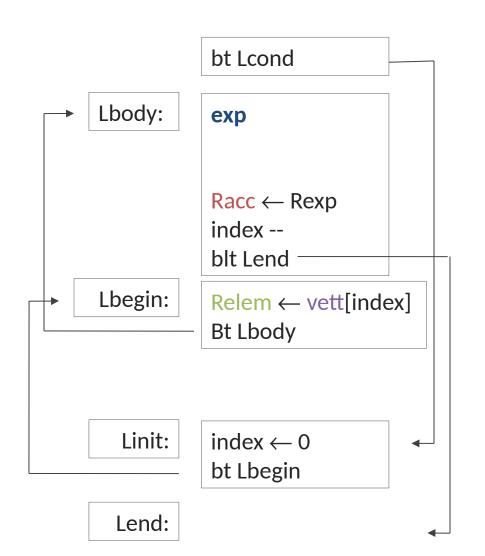
// vett = {2, 6, 13}
reduce elem into sum
    as [[ sum + t * elem ]]
    on vett;
// sum = 21
```

Similar to array comprehension

The vector ID is in the last position of the stmt

```
reduce elem into sum as [[ sum + t * elem ]] on vett;
```

 To retrieve its size jump to the end to initialize the loop



REDUCE elem INTO acc AS [[

exp]] ON vett

Three labels are stored on tokens

%token < label > ON REDUCE INTO

- Lbegin is local in the last semantic action
- control_statement : ...

| reduce_statement SEMI

,