

1. DESCRIPTION

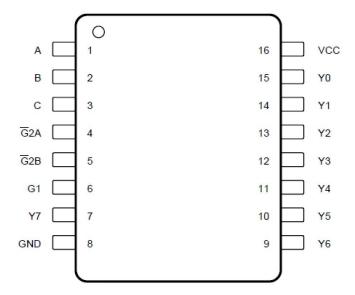
These Schottky-clamped TTL MSI circuits are designed to be used in high-performance memory decoding or data-routing applications requiring very propagation delay times. In high-performance memory systems, these docoders can be used to minimize the effects of system decoding. When employed with high-speed memories utilizing a fast enable circuit, the delay times of the memory are usually less than the typical access time of the memory. This means that the effective system delay introduced by the Schottky-clamped system delay introduced by the Schottky-clamped system decoder is negligible.

The XL/XDLS138 decode one of eight lines dependent on the conditions at the three binary select inputs and the three binary select inputs reduce the need for external gates or inverters when expending.A 24-line decoder can be implemented without external inverters and a 32-line decoder requires only one inverter.An enable input can be used as a data input for demultiplexing applications.

All of these decoder/demultiplexers feature fully buffered inputs, each of which represents only one normalized load to its driving circuit. All inputs are clamped with high-performance Schottky diodes to suppress line-ringing and to simplify system design.

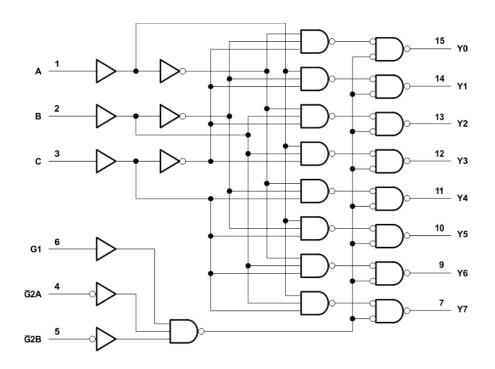
The XL74LS138,XD74LS138 are characterized for operation from 0° C to 70° C.

2. PIN CONFIGURATIONS





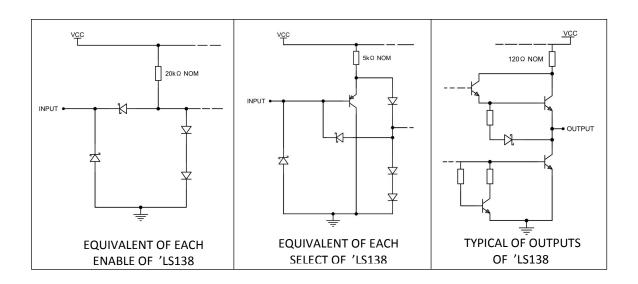
3. LOGIC DIAGRAM



INPUTS												
ENABLE SELECT			OUTPUTS									
<u>G</u> 1	G2*	С	В	Α	Y0	Y1	Y2	Y3	Y4	Y5	Y6	Y7
Х	Н	Х	Х	Х	Н	Н	Н	Н	Η	Н	Н	Н
L	Х	Х	Х	Х	Н	Н	Н	Н	Η	Н	Н	Н
Н	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н
Н	L	L	L	Н	Н	L	Н	Н	Η	Н	Н	Н
Н	L	L	Н	L	Н	Н	L	Н	Η	Н	Н	Н
Н	L	L	Н	Н	Н	Н	Н	L	Н	Н	Н	Н
Н	L	Н	L	L	Н	Н	Н	Н	اــ	Н	Н	Н
Н	L	Н	L	Н	Н	Н	Н	Н	Η	L	Н	Н
Н	L	Н	Η	L	Н	Н	Н	Н	Η	Н	L	Н
Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L



4. SCHEMATICS OF INPUTS AND OUTPUTS



5. ABSOLUTE MAXIMUM RATINGS OVER OPERATING FREE-AIR TEMPERATURE RANGE (UNLESS OTHERWISE NOTES)

Supply voltage, V _{CC} (see Note 1)		7V
Input voltage, VI: 74LS138		7V
Operating free-air temperature rang	ge: SOP package	0°C to 70°C
	DIP package	0°C to 70°C
Storage temperature range, Tstg		65°C to 150°C

NOTE 1: Volltage values are with respect to network ground terminal.

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RECOMMENDED OPERATING CONDITIONS

		XL/XD74LS138			
		MIN	NOM	MAX	UNIT
VCC	Supply voltage	4.75	5	5.25	V
VIH	High-level input voltage	2			V
V _{IL}	Low-level input voltage			0.8	V
lOH	High-level output current			-0.4	mA
lOL	Low-level output current			8	mA
Тд	Operating free-air temperature	0		70	°C

7. ELECTRICAL CHARACTERISTICS OVER RECOMMENDED OPERATING FREE-AIR **RANGE (UNLESS OTHERWISE NOTED)**

	TEST CONDITIONS [†]			XL/XD74LS138			
PARAMETER				MIN	TYP [‡]	MAX	UNIT
VIK	V _{CC} = MIN,	I _I = -18 mA				-1.5	V
VOH	V _{CC} = MIN,	V _{IL} = MAX,	I _{OH} = -0.4 mA	2.7	3.4		V
	V _{CC} = MIN,	V _{IH} = 2 V	I _{OL} = 4 mA		0.25	0.4	V
VOL			I _{OL} = 8 mA		0.35	0.5	
lj	V _{CC} = MAX,	V _I = 7 V				0.1	mA
lіН	V _{CC} = MAX,	V _I = 2.7 V				20	μΑ
IIL	V _{CC} = MAX,	V _I = 0.4 V				-0.4	mA
l _{OS} §	V _{CC} = MAX					-0.2	mA
ICCH	V _{CC} = MAX,	V _I = 4.5 V		-20	·	-100	mA
ICCL	V _{CC} = MAX,	V _I = 0 V			6.3	10	mA

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

‡ All typical values are at V_{CC} = 5 V, T_A = 25°C.

§ Not more than one output should be shorted at a time, and the duration of the short-circuit should

8. SWITCHING CHARACTERISTICS, VCC = 5 V, TA = 25° C

PARAMETER	FROM	TO (OUTPUT)	TEST CON	XL/XD74LS138			UNIT	
	(INPUT)			MIN	TYP	MAX		
t _{PLH}						11	20	ns
t _{PHL}	Binary	Any				18	41	ns
t _{PLH}	Select		RL = $2 k\Omega$, CL = 15			21	27	ns
t _{PHL}				CL = 15 pF		20	39	ns
^t PLH			See Note 2			12	18	ns
t _{PHL}	Enable	Any				20	32	ns
t _{PLH}						14	26	ns
t _{PHL}						13	38	ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.

not exceed one second.



9. ORDERING INFORMATION

Ordering Information

Part Number	Device Marking	Package Type	Body size (mm)	Temperature (°C)	MSL	Transport Media	Package Quantity
XL74LS138	XL74LS138	SOP16	10.00 * 3.95	-0 to 70	MSL3	T&R	2500
XD74LS138	XD74LS138	DIP16	19.05 * 6.35	-0 to 70	MSL3	Tube 25	1000

10. DIMENSIONAL DRAWINGS

