Report generated on 2025-10-09 05:38 GMT by riscof v

Environment

Riscof Version	1.25.3
Riscv-arch-test Version/Commit Id	-
DUT	uemu
Reference	sail c simulator
ISA	RV64IMASUZicsr_Zifencei
User Spec Version	2.3
Privilege Spec Version	1.10

Yaml

Show all details / Hide all details

Name
/home/sn/workspace/uemu/verif/riscof_work/uemu_isa_checked.yaml *(show details)*
/home/sn/workspace/uemu/verif/riscof_work/uemu_platform_checked.yaml *(show details)*

Please visit YAML specifications for more information.

Summary

☑ 101Passed, ☑ 0Failed

Results

Show all details / Hide all details

Test	Result	Path
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/A/src/amoadd.d-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/A/src/amoadd.d-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/A/src/amoadd.w-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/A/src/amoadd.w-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/A/src/amoand.d-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/A/src/amoand.d-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/A/src/amoand.w-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/A/src/amoand.w-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/A/src/amomax.d-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/A/src/amomax.d-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/A/src/amomax.w-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/A/src/amomax.w-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/A/src/amomaxu.d-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/A/src/amomaxu.d-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/A/src/amomaxu.w-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/A/src/amomaxu.w-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/A/src/amomin.d-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/A/src/amomin.d-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/A/src/amomin.w-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/A/src/amomin.w-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/A/src/amominu.d-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/A/src/amominu.d-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/A/src/amominu.w-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/A/src/amominu.w-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/A/src/amoor.d-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/A/src/amoor.d-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/A/src/amoor.w-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/A/src/amoor.w-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/A/src/amoswap.d-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/A/src/amoswap.d-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/A/src/amoswap.w-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/A/src/amoswap.w-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/A/src/amoxor.d-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/A/src/amoxor.d-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/A/src/amoxor.w-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/A/src/amoxor.w-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/add-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/add-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/addi-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/addi-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/addiw-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/addiw-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/addw-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/addw-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/and-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/and-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/andi-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/andi-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/auipc-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/auipc-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/beq-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/beq-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/bge-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/bge-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/bgeu-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/bgeu-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/blt-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/blt-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/bltu-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/bltu-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/bne-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/bne-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/fence-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/fence-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/jal-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/jal-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/jalr-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/jalr-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/lb-align-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/lb-align-01.S

Test	Result	Path
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/lbu-align-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/lbu-align-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/ld-align-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/ld-align-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/lh-align-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/lh-align-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/lhu-align-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/lhu-align-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/lui-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/lui-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/lw-align-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/lw-align-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/lwu-align-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/lwu-align-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/misalign1-jalr-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/misalign1-jalr-0:
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/or-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/or-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/ori-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/ori-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/sb-align-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/sb-align-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/sd-align-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/sd-align-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/sh-align-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/sh-align-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/sll-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/sll-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/slli-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/slli-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/slliw-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/slliw-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/sllw-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/sllw-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/slt-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/slt-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/slti-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/slti-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/sltiu-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/sltiu-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/sltu-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/sltu-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/sra-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/sra-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/srai-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/srai-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/sraiw-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/sraiw-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/sraw-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/sraw-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/srl-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/srl-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/srli-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/srli-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/srliw-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/srliw-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/srlw-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/srlw-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/sub-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/sub-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/subw-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/subw-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/sw-align-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/sw-align-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/xor-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/xor-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/I/src/xori-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/I/src/xori-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/M/src/div-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/M/src/div-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/M/src/divu-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/M/src/divu-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/M/src/divuw-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/M/src/divuw-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/M/src/divw-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/M/src/divw-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/M/src/mul-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/M/src/mul-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/M/src/mulh-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/M/src/mulh-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/M/src/mulhsu-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/M/src/mulhsu-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/M/src/mulhu-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/M/src/mulhu-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/M/src/mulw-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/M/src/mulw-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/M/src/rem-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/M/src/rem-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/M/src/remu-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/M/src/remu-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/M/src/remuw-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/M/src/remuw-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/M/src/remw-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/M/src/remw-01.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/Zifencei/src/Fencei.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/Zifencei/src/Fencei.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/privilege/src/ebreak.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/privilege/src/ebreak.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/privilege/src/ecall.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/privilege/src/ecall.S
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/privilege/src/misalign-beq-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/privilege/src/misalign
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/privilege/src/misalign-bge-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/privilege/src/misalign
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/privilege/src/misalign-bgeu-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/privilege/src/misalign
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/privilege/src/misalign-blt-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/privilege/src/misalign
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/privilege/src/misalign-bltu-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/privilege/src/misalign
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/privilege/src/misalign-bne-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/privilege/src/misalign
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/privilege/src/misalign-jal-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/privilege/src/misalign

Test	Result	Path
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/privilege/src/misalign-ld-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/privilege/src/misalign
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/privilege/src/misalign-lh-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/privilege/src/misalign
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/privilege/src/misalign-lhu-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/privilege/src/misalign
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/privilege/src/misalign-lw-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/privilege/src/misalign
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/privilege/src/misalign-lwu-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/privilege/src/misalign
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/privilege/src/misalign-sd-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/privilege/src/misalign
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/privilege/src/misalign-sh-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/privilege/src/misalign
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/privilege/src/misalign-sw-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/privilege/src/misalign
/home/sn/workspace/uemu/riscv-arch-test/riscv-test-suite/rv64i_m/privilege/src/misalign2-jalr-01.S	Passed *(show details)*	/home/sn/workspace/uemu/verif/riscof_work/rv64i_m/privilege/src/misalign
Test	**Result**	**Path**