16	15	14	13	12	11	10	9
VCC	²Y2	²Y1	²Z	²Y0	²Y3	S0	S1

74HC4052 *DUAL 4-CH ANALOG* **MUX**

²Y0	²Y2	²Z	²Y3	²Y1	E	VEE	GND
1	2	3	4	5	6	7	8

(pinout diagram grid of 74HC4052 / 74LS4052 DUAL 4-CH ANALOG MUX ICs, repeated across 4 columns and 5 rows)