

HC LS

Figure 1 displays 16 circuit diagrams arranged in a 4x4 grid, showing the internal logic of the 74HC590 and 74ALS590 counters. Each diagram includes a 4-bit BCD output (Q3, Q2, Q1, Q0) and a carry output (C). The diagrams are arranged in pairs, with 74HC590 on the left and 74ALS590 on the right of each row. The top row shows the internal logic with various gates and flip-flops. The bottom row shows the same logic with different gate types (e.g., NAND, NOR, OR, AND, XOR, XNOR, NOT, BUF, MUX, DEMUX, etc.).

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[illegible]