| STANDARD  | NARROW  | ALTERNATE   | ALT. NARROW  |
|---|---|---|--|
| 16 15 14common12 11 10 9 VCC A2 A1 A A0 A3 S0 S1 CD4052 4cm analogMU) B0 B2 B B3 B1 EN VEE GN 1 2 Common 4 5 6 7 8  |   | 16 D D Common D D X X VCC A2 A1 A A0 A3 S0 S1 CD4052 & analog MU X B0 B2 B B1 EN VEG GN | E A2 A1 A A0 A3 S0 S1 (CD4052 & analogMUX)  BB B B B B B B EN VEE S B C 7 B                |
| 1 2 Common 4 5 6 7 8  | X 15 14Common12 11 10 9   | BO B2 B B3 B1 EN VEE GN   | Common III 6 7 B   |
| 16 15 14common12 11 10 9<br>VCC A2 A1 A A0 A3 S0 S1<br>CD4052 4 analogMU)<br>B0 B2 B B3 B1 EN VEE GN<br>1 2 Common 4 5 6 7 8                                | (CD4052 analogMUX) DB0 B2 B B3 B1 EN VEE § 1 2 Common 4 5 6 7 B                               | CD4052  | X II Common II II X XI X X X X X X X X X X X X X X   |
| 1 2 Common 4 5 6 7 8  | X 15 14Common12 11 10 9   | 16  | CD4052 analogMUX DB0 B2 B B3 B1 EN VEE S DB0 B2 Common B B 6 7 B                           |
| 16 15 14Common12 11 10 9 VCC A2 A1 A A0 A3 S0 S1 CD4052 4% analogMU)  B0 B2 B B3 B1 EN VEE GN 1 2 Common 4 5 6 7 8  | DB0 B2 B B3 B1 EN VEE S<br>1 2 Common 4 5 6 7 B   | □ Common ■ 6 7 8  | DB0 B2 B B3 B1 EN VEE S  |
| BO B2 B B3 B1 EN VEE GN<br>1 2 Common 4 5 6 7 8   | CD4052@analogMUX  | 16  | CD4052 analogMUX DB0 B2 B B3 B1 EN VEE § B1 Common B                                       |
| BO B2 B B3 B1 EN VEE GN<br>1 2 Common 4 5 6 7 8   | (CD4052 analogMUX) DB0 B2 B B3 B1 EN VEE § 1 2 Common 4 5 6 7 B                               | CD4052 a analogMU) B0 B2 B B3 B1 EN VEE GN B Common # 6 7 8                             | X Q Al Common Al Al Al So Si (CD4052 analogMUX)  BB B2 B B3 B1 EN VEE §  B Common B B6 7 B |
| 16 15 14common12 11 10 9<br>VCC A2 A1 A A0 A3 S0 S1<br>CD4052 4 an analogMU)<br>B0 B2 B B3 B1 EN VEE GN<br>1 2 common 4 5 6 7 8<br>16 15 14common12 11 10 9 |   | 16 II   | X  |
| VCC A2 A1 A A0 A3 S0 S1<br>CD4052 A analogMU)<br>B0 B2 B B3 B1 EN VEE GN<br>1 2 common 4 5 6 7 8<br>16 15 14 common 12 11 10 9                              | CD4052 analogMUX  | VCC A2 A1 A A0 A3 S0 S1<br>CD4052 43 analogMU)  | E A2 A1 A A0 A3 S0 S1<br>(CD4052 analogMUX)  BB0 B2 B B3 B1 EN VEE S1<br>Common 6 7 X      |
| CD4052 A analogMU   | E A3 A1 A A0 A3 S0 S1<br>(CD4052analogMUX)<br>DB0 B2 B B3 B1 EN VEE S<br>1 2 Common 4 5 6 7 B | CD4052 4 analogMUX  | C A2 A1 A A0 A3 S0 S1  |