

Figure 1 displays 20 circuit diagrams arranged in a 4x5 grid, showing various LATCH and LATCH2 components. Each diagram includes a title, a schematic, and a truth table.

The components shown are:

- LATCH
- LATCH2
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Each diagram includes a schematic showing inputs (G, D, S, D1, D2, D3, GDN) and outputs (Q, QN). The truth tables show the logic for Q and QN based on the inputs.