| 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 |
| VCC | Y0 | Y1 | Y2 | Y3 | Y4 | Y5 | Y6 |

74HC138 *3 to 8 LINE DECODER* **MUX**

| A | B | C | G2A | G2B | G1 | Y7 | GND |
| 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 |

(pattern of 74HC138 / 74LS138 "3 to 8 LINE DECODER MUX" pinout diagrams repeated across a grid)