STANDARD
HCMOS   LS TTL

NARROW 1/2 END PINS

A sheet of repeated chip-labeling strips for the 74HC165 (HCMOS) and 74LS165 (LS TTL) 8-bit parallel-in/serial-out shift registers. Each label repeats the following pinout:

```
16 INHIBIT  14  13  12  11   IN  OUT
Vcc CLOCK  D3  D2  D1  D0     SERIAL
74HC165  P/S SHIFT OUT/OUT  REGISTER
SHFT/LD  D4  D5  D6  D7  S-OUT  GND
  1  CLK  3   4   5   6   7    8
```

```
16 INHIBIT  14  13  12  11   IN  OUT
Vcc CLOCK  D3  D2  D1  D0     SERIAL
74LS165  P/S SHIFT OUT/OUT  REGISTER
SHFT/LD  D4  D5  D6  D7  S-OUT  GND
  1  CLK  3   4   5   6   7    8
```