



“Computer Architecture”

“Lab#8”

Name: Urba Naseem

Sap-Id: 52920

Department: BS-CS 4

Submitted to: Ma'am Ayesha Akram

Date: 9th April ,2025

Task 1:

```
START:
INP
STA NUM
INP
CMA
INC
ADD NUM
STA NUM
INP
CMA
INC
ADD NUM
OUT
HLT
```

```
NUM: .data 1 0
```

```
EXECUTING...
Enter Inputs, the first of which must be an Integer: 6
Enter Inputs, the first of which must be an Integer: 3
Enter Inputs, the first of which must be an Integer: 1
Output: 2
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]
```

Task 2:

START:

ADD NUM2

CMA

INC

ADD NUM1

OUT

HLT

NUM1: .data 1 5

NUM2: .data 1 8

EXECUTING...

Output: -3

EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]

Task 3:

```
START:
INP
STA NUM
INP
CMA
INC
ADD NUM
STA RESULT
OUT
HLT
NUM: .data 1 0
RESULT: .data 1 0
```

```
EXECUTING...
Enter Inputs, the first of which must be an Integer: 5
Enter Inputs, the first of which must be an Integer: 7
Output: -2
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]
```