

50.002 Computation Structure

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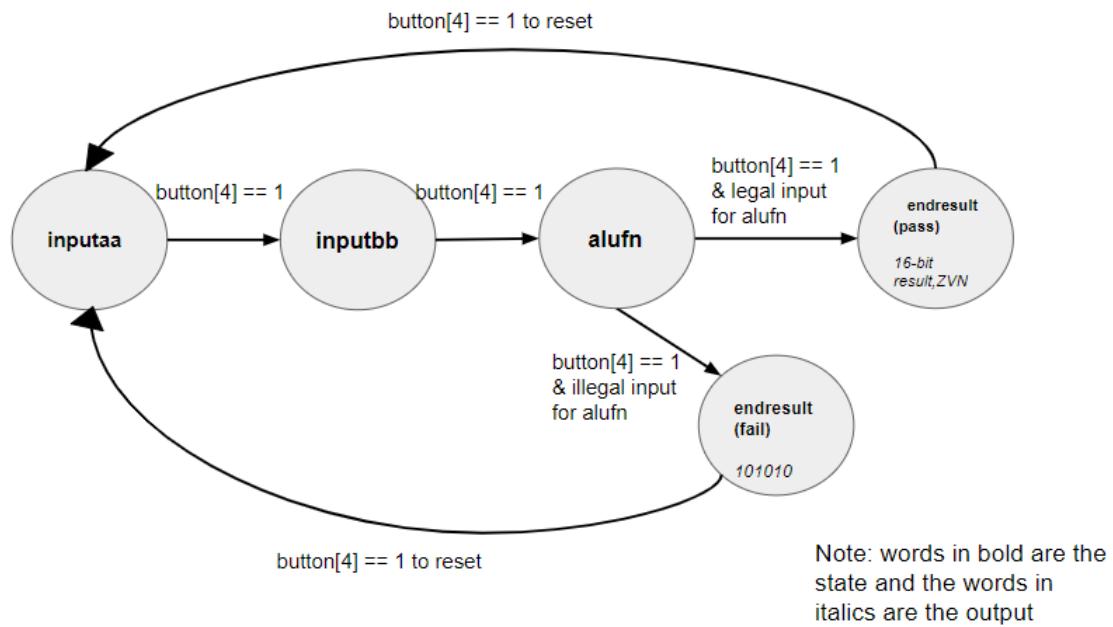
ALUFN table

Operation	ALUFN[5:0]	Hex
ADD	000000	0x00
SUB	000001	0x01
<u>MULTIPLY</u>	001000	0x08
<u>DIVIDE</u>	001001	0x09
AND	011000	0x18
OR	011110	0x1E
XOR	010110	0x16
“A” LDR	011010	0x1A
SHL	100000	0x20
SHR	100001	0x21
SRA	100011	0x23
CMPEQ	110011	0x33
CMPLT	110101	0x35
CMPLE	110111	0x37
<u>INC</u>	000011	0x03
<u>DEC</u>	000111	0x07
ERROR/ILLEGAL	All other ALUFN implementations	All other ALUFN implementations

Our implementation follows the LAB 3 ALU handout and the underlined operations are the extra features we implemented.

Schematics

The diagram below is a broad overview of the design of our Finite State Machine. Each state is explained in detail on the next page.



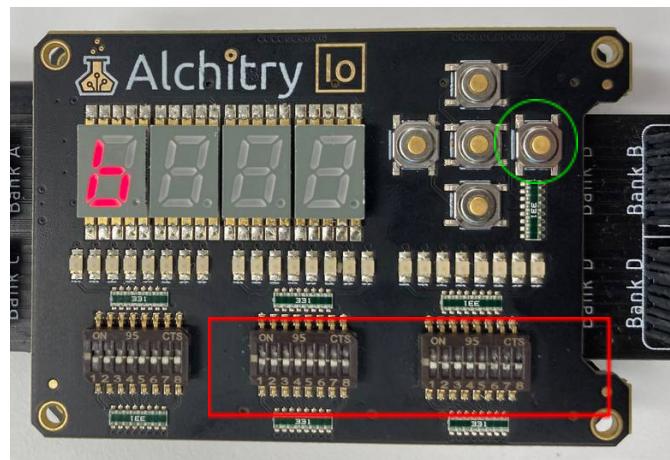
Manual Testing

- When the Alchitry device is powered up, A will be displayed in the first segment which indicates State “INPUTAA”.

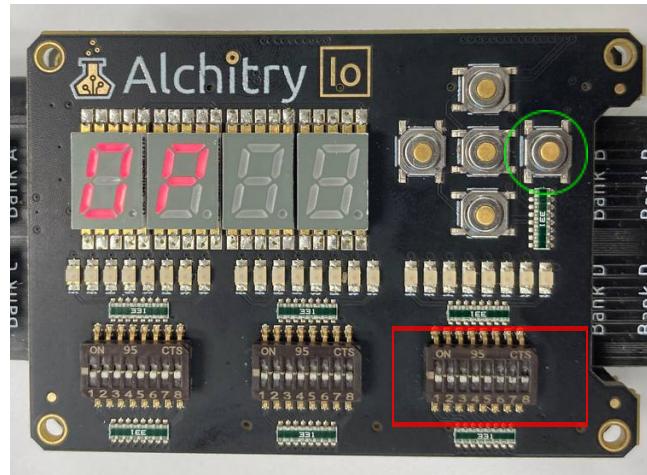
The user should input the 16 bits A values at this stage on io_dip[0] and io_dip[1]. After doing so, press the right button to proceed to input the values of B.



- B will then be displayed in the first segment, which indicates State “INPUTBB”. The user should then input the 16 bits B values at this stage on io_dip[0] and io_dip[1]. Press the right button to proceed to input the values of ALUFN.



- Next, OP will be displayed on the first and second segment, the user should input the 6 bits ALUFN values in io_dip[0][5:0].



4. If the OPCODE typed inside is invalid (not included in our ALUFN table), ERR would be shown on the segments, otherwise the segments will display R, followed by the Z V N values. The output value of the operation between A and B will be displayed in the Io_led.



Above picture shows a valid case which returns all different outputs from ALU after computing a and b with the specific operation. Purple box indicates “PASS” which means all inputs are valid and there is a result. Green box refers to the output z, blue box refers to the output v and orange box refers to the output n.



Above picture shows a invalid case which returns a “FAIL”. This occurs when an invalid opcode is entered.

Special Case

However, it also happens when the result of the operation of a and b is 1010101010101010. This makes 1010101010101010 a simulated error, which is unavoidable due to the design of our code, although the original alu result is correct.