

Job Notification Form, IIT Delhi

Company Overview

Name:	Micron Technologies India Operations LLP
Website:	www.micron.com
Company Type:	Core (Technical)
Description:	Micron is a world leader in innovative memory solutions that transform how the world uses information. For over 40 years, our company has been instrumental to the world's most significant technology advancements, delivering optimal memory and storage systems for a broad range of applications.

Job Details

Designation:	CAD Engineer
Type:	Core (Technical)
Place of Posting:	Hyderabad
Job Details:	<p>As a CAD Development Engineer at Micron Technology, Inc., you will be working in a collaborative team focused on development of custom software solutions to semiconductor design challenges. Design Automation tool development requires a collaborative team working to understand Design Engineering challenges, write software to automate solutions, integrate commercial Electronic Design Automation (EDA) tools – and validate tools put in production design flows.</p> <p>Responsibilities</p> <ul style="list-style-type: none">• Work closely with circuit design, layout implementation and design verification teams understand key challenges• Customize commercial EDA (Electronic Design Automation) tools• Software programming to implement automation• Integrate developed tools into design environments and flows• Test and validate delivered tools and methodologies <p>Requirements</p> <ul style="list-style-type: none">• Hands-on experience required, preferably in Python (but structured Perl, Tcl, Skill or similar languages may be considered)• Experience in object-oriented software design (Python, C++, Java or other OO language)• Experience in test-driven software development (pyunit, or other unit testing frameworks)• Experience in source code management and release systems• Excellent software programming fundamentals are required• Good knowledge of CMOS circuit and deep submicron physical design, as well as CMOS process manufacturing and layout rules preferred• Experience developing tools/applications and integrating into Cadence DF2 environments is preferred• In depth experience with Python and Python QT language is preferred• Recent and relevant experience in C, C++, JAVA, Visual Basic, Perl, or Lisp will be considered• Experience in design simulation areas preferred - Verilog, Verilog-A, ncverilog, VCS, Spice/Fast Spice, fineSim, hsim, hspice• Good communication and problem-solving skills are required• Fluent in English and able to communicate effectively with global teams.

Education

- Bachelors or Post Graduate Degree in Electronics Engineering or related engineering field required

Joining By: 25 July 2022

Salary Details

CTC: 2,065,000 INR Per Annum

Gross: 1,430,000 INR Per Annum

CTC Breakup: Bachelors Masters
Base Pay 1430000 Base Pay 1540000
IPP*** 143000 IPP*** 154000
Discretionary Allowance 56000 Discretionary Allowance 56000
Provident Fund 85824 Provident Fund 92400
Relocation amount** 200000 Relocation amount** 200000
Joining Bonus* 150000 Joining Bonus* 150000

Selection Process

Resume Shortlist: Yes

Written Test: No

Online Test: Yes

Group Discussion: No

Medical Test: No

Personal Interview: Yes

No. of Rounds: 2

No. of Offers: 2

Minimum CGPA: 7

Eligibility

Recruiting PHDs: No

Eligible Departments: B.Tech in Computer Science & Engineering, B.Tech in Electrical Engineering, B.Tech in Electrical Engineering (Power and Automation), B.Tech and M.Tech in

Computer Science & Engineering, M.Tech in Communications Engineering,
M.Tech in Computer Science & Engineering, M.Tech in Computer Technology,
M.Tech in Control & Automation, M.Tech in Integrated Electronics & Circuits,
M.Tech in Radio Frequency Design & Technology