Job Notification Form, IIT Delhi

Company Overview

Name: Micron Technologies India Operations LLP

Website: www,micron.com

Company Type:

Core (Technical)

Description:

Micron is a world leader in innovative memory solutions that transform how the world uses information. For over 40 years, our company has been instrumental to the world's most significant technology advancements, delivering optimal memory and storage systems for a broad range of applications.

Job Details

Designation: Memory Circuit Design Verification Engineer

Type: Core (Technical)

Place

of Hyderabad

Posting:

Job Details:

Memory Circuit Design Verification Engineer

Micron Technology's vision is to transform how the world uses the information to enrich life and our dedication to people, innovation, tenacity, partnership, and customer focus allows us to fulfill our mission to be a global leader in memory and storage solutions. This means conducting business with integrity, accountability, and teamwork while supporting our global community.

Job Description

Our vision is to transform how the world uses information to enrich life.

Join an inclusive team focused on one thing: using their expertise in the relentless pursuit of innovation for customers and partners. The solution we create help make everything from virtual reality experiences to breakthroughs in neural networks possible. We do it all while committing to integrity, sustainability, and giving back to our communities. Because doing so can spark the very innovation we are pursuing.

As a Memory Circuit Design Verification Engineer, you will work in a highly innovative, motivated, young and dynamic design team capable of verifying complete products using state of the art memory technologies. You will need to have the ability to evaluate full chip and/or block level functionality and provide solutions to help the timely delivery of a functionally correct design.

Unique Opportunities

- Complete ownership of verification and end to end analysis of complex full chip gate level custom designs with advanced low power and power management technologies spread across multiple categories such as DDR4, LPDDR4, DDR5 and LPDDR5 that are capable of operating at high speeds of up to 6400MT/s.
- Collaborate closely with design and verification team members spread across the globe, many of whom have decades of experience in memory design.
- Work on cross functional tasks that can widen your skill set.

About Team

For nearly 40 years, Micron Technology, Inc. has redefined innovation with the world's most advanced memory and semiconductor technologies. We're an international team of visionaries and scientists, developing groundbreaking technologies that are transforming how the world uses information to enrich life. Our Global team is growing and we are looking for a passionate Memory Circuit Design & Verification Engineer in Micron Technology's DRAM and Emerging Memory Group (DEG) in India.

Responsibilities

- Participate in simulating, analyzing and debugging pre-silicon full chip designs by applying leading edge verification technique
- Develop Test cases/Stimulus to increase the functional coverage for all DRAM and emerging memory architectures and features.
- Develop and maintain test benches and test vectors using simulation tools and run regressions for coverage analysis and improvements.
- Co-work with international colleagues on developing new verification flows to take on the challenges in DRAM and emerging memory design.
- Participate in developing verification methodology and verification environments for advanced DRAM and emerging memory products.

Core Requirements

- Analytical capability for complex gate level circuit designs
- Basic understanding of CMOS circuit designs
- Familiar with both SPICE and Verilog simulations

Good communication skills and ability to work well in a team

Joining By: 25 July 2022

Salary Details

CTC: 2,065,000 INR Per Annum

Gross: 1,486,000 INR Per Annum

CTC Bachelors Masters

Breakup: Base Pay 1430000 Base Pay 1540000

IPP*** 143000 IPP*** 154000

Discretionary Allowance 56000 Discretionary Allowance 56000

Provident Fund 85824 Provident Fund 92400

Relocation amount** 200000 Relocation amount** 200000

Joining Bonus* 150000 Joining Bonus* 150000

Selection Process

Resume Shortlist: Yes

Written Test: No

Online Test: Yes

Group
Discussion:

No

Discussion.

Medical Test: No

Personal Yes

Interview:

No. of 2

Rounds:

No. of 3

Offers:

Minimum 7

CGPA:

Eligibility

Recruiting PHDs:

No

Eligible Departments:

B.Tech in Electrical Engineering, B.Tech in Electrical Engineering (Power and Automation), B.Tech in Engineering Physics, M.Tech in Integrated Electronics & Circuits, M.Tech in Radio Frequency Design & Technology, M.Tech in VLSI Design Tools & Technology, M.S.(R) in Electrical Engineering, M.S.(R) in VLSI Design

Tools and Technology