Job Notification Form, IIT Delhi

Company Overview

Name: Aura Semiconductor Pvt Ltd

Website: www.aurasemi.com

Company Type:

Information Technology

Description:

Aura is a fabless semiconductor company innovating high performance IC

products for IoT Radios, Timing and Portable Audio markets.

Job Details

Designation: Design Engineer - Analog

Type: Information Technology

Place

of Bangalore

Posting:

Job Details:

Analog Design Engineer – Junior

Job Overview Duties include

- · As an Analog design engineer, the designer will design analog/mixed signal circuits in the fields of RF, Audio, Power Management, Data Converters, Clock synthesizers and Jitter Attenuators.
- · Run extensive simulations on the design to fully verify functionality and performance before fabrication and will validate the chip in the lab.
- · Explore alternative design techniques to meet the chip specifications while minimizing device costs with ample opportunities to innovate during the design cycle that can lead to patentable ideas.
- Detailed design cycle that spans not only circuit design and simulation but also layout support, test, evaluation and debugging of prototype ICs and design support for mass production.
- Plays a lead role in product definition and be responsible for designing significant blocks of the chip, including chip architecture and chip top integration.
- · Will be working with small, nifty, focused, world class design team at Aura in a creative role.

ESSENTIAL FUNCTIONS

- Be thorough with circuit design and signal processing fundamentals.
- Is a good team player and works well with internal and external partners, shares knowledge with the team and always eager to stay up to date with recent design
- Provides guidance to layout designers for sensitive custom layouts.
- Simulates for the block-level and top-level specifications, design and layout.
- · Be hands on in post-silicon debug, lab testing, characterization and troubleshooting.
- Does project management and technical sharing. (for senior candidates with 5+ yrs of exp only)
- · Supports the product definition, the test program development and the manufacturing/production/customer needs. (for senior candidates with 5+ yrs of exp only)

REQUIREMENTS

- Candidates preferred from premier institutes for fresher and all experience levels
- A technology-related Bachelor's degree with 3-5 years of related experience for senior positions.
- Relevant background and experience in Analog/mixed-signal and/or RF IC design.
- Knowledge in behavioural modelling, Verilog-AMS & Matlab programming.
- Knowledge in Mixed mode simulation and SOC integration.
- Advanced system knowledge is preferable.
- Excellent written and verbal presentation skills.

Joining By: 1 July 2022

Salary Details

CTC: 2,200,000 INR Per Annum

Gross: 1,700,000 INR Per Annum

CTC 1700000+100000

Breakup:

Perks / 400000

Bonus:

Selection Process

Resume Yes Shortlist:

Written Test: No

Online Test: No

Group No Discussion:

Medical Test: No

Personal Yes Interview:

No. of 2

Rounds:

No. of 4

Offers:

Eligibility

Recruiting PHDs:

Yes

Eligible Departments:

M.Tech in Applied Optics, M.Tech in Communications Engineering, M.Tech in Computer Science & Engineering, M.Tech in Computer Technology, M.Tech in Control & Automation, M.Tech in Energy & Environment Technologies and

Management, M.Tech in Energy Studies, M.Tech in Instrument Technology, M.Tech in Integrated Electronics & Circuits, M.Tech in Optoelectronics & Optical Communication, M.Tech in Power Electronics, Electrical Machines & Drives, M.Tech in Power Systems, M.Tech in Radio Frequency Design & Technology, M.Tech in Solid State Materials, M.Tech in Telecommunication Technology & Management, M.Tech in VLSI Design Tools & Technology, M.Sc in Mathematics, M.Sc in Physics, M.S.(R) in Telecommunication Technology and Management, M.S.(R) in Computer Science & Engineering, M.S.(R) in Electrical Engineering, M.S.(R) in Sensors, Instrumentation and Cyber-physical System Engineering, M.S. (R) in VLSI Design Tools and Technology, M.S.(R) in Information Technology