Job Notification Form, IIT Delhi

Company Overview

Name:	Mediatek Bangalore Pvt Ltd
Website:	https://www.mediatek.com/
Company Type:	Core (Technical)
Description:	We lead the market in chipset sales for Smartphone, Smart TVs, Voice Assistant Devices (VAD), Android tablets, feature phones, and optical disc products. We build chips that are less about connecting people to their devices and more about connecting your devices to what matters. Connecting to the things that shape our lives, makes us smarter, healthier and improve everyday life.

Job Details

Designation:	Senior Analog Design Engineer
Type:	Core (Technical)
Place of Posting:	BANGALORE
Job Details:	MS with a major in EE. Solid background in high-speed analog CMOS circuit design. Familiar with SerDes mixed signal design and verification. Strong educational or industrial background in power management circuit design (EX: switching regulators, low dropout linear regulators, battery management circuits, and power-on-reset circuits) Proficient with Cadence design environment and mixed-signal simulation.
Joining By:	1 July 2022

Salary Details

CTC:	2,500,000 INR Per Annum
Gross:	1,610,000 INR Per Annum
CTC Breakup:	Base Pay : 1610000/- Variable Pay : upto 4,00,000/-
Perks / Bonus:	Bonus : 5,00,000/-

Selection Process

Resume Shortlist:	Yes
Written Test:	No
Online Test:	Yes
Group Discussion:	No
Medical Test:	No
Personal Interview:	Yes
No. of Rounds:	2
No. of Offers:	5
Minimum CGPA:	7

Eligibility

Recruiting PHDs:	Yes
Eligible Departments:	M.Tech in Instrument Technology, M.Tech in Integrated Electronics & Circuits, M.Tech in Radio Frequency Design & Technology, M.Tech in VLSI Design Tools & Technology, M.S.(R) in Electrical Engineering, M.S.(R) in VLSI Design Tools and Technology