Job Notification Form, IIT Delhi

Company Overview

Name:	Espressif Systems (India) Private Limited
Website:	www.espressif.com
Company Type:	Core (Technical)
Description:	Espressif Systems (688018) is a public multinational, fabless semiconductor company established in 2008, with headquarters in Shanghai and offices in Greater China, India, and Europe. We have a passionate team of engineers and scientists from all over the world, focused on developing cutting- edge WiFi-and-Bluetooth, low-power IoT solutions. We have created the popular ESP8266 and ESP32 series of chips, modules, and development boards. By leveraging wireless computing, we provide green, versatile, and cost-effective chipsets. We have always been committed to offering IoT solutions that are secure, robust, and power-efficient. By open-sourcing our technology, we aim to enable developers to use Espressif's technology globally and build smart connected devices. In July 2019, Espressif made its Initial Public Offering on the Sci-Tech Innovation Board (STAR) of the Shanghai Stock Exchange (SSE).
	Espressif has a technology centre in Pune. The focus is on embedded software engineering and IoT solutions for our growing customers.

Job Details

Designation:	Engineer
Type:	Core (Technical)
Place of Posting:	Pune
Job Details:	Job Description	Digital Design

We are seeking a nerd to build design of the next generation of our chip.
The candidate is expected to design and verify circuits, logic, systems, and algorithms to meet product requirements. Individual will determine the proper method and procedures to be used in the RTL/Digital system development and determine the best method for verifying the digital system before the complete design is committed to silicon.

You're someone who wants to influence your own development. You're looking for a company where you have the opportunity to pursue your interests across functions and geographies. Where a job title is not considered the final definition of who you are, but the starting point

Responsibilities:
1. Be responsible for the development of digital SoCs and IPs
2. Complete the design and verification of modules and chips, and fix any design defects;
3. Cooperate with FPGA engineers and software engineers to complete FPGA prototype verification;

4. Assist with module timing constrains and STA checks;
5. Be responsible for writing relevant technical documents.

Basic Qualifications
• Bachelor's degree, or above, in Microelectronic Engineering, Electronic Engineering, Communications Engineering or other related disciplines.
• We will provide senior or junior positions based on candidates' work experience and competence.
• Proficiency in using Verilog/System Verilog for RTL design and verification;
• Digital state machine architecture and logic design;
• Performing synthesis and/or static timing analysis on complex designs;
• Familiarity with communication algorithms (Baseband engineers);
• Familiarity with design of digital MAC/baseband/high-speed interface/CPU or DSP is a plus;
• Familiarity with FPGA/Silicon validation using C based tests and usage of standard debugging tools is a plus.

Joining By:	1 July 2022

Salary Details

CTC:	2,450,000 INR Per Annum
Gross:	1,850,000 INR Per Annum
CTC Breakup:	Base :15,00,000 Performance Bonus :3,00,000 Joining Bonus : 2,00,000 Retention Bonus: 4,50,000 Total CTC - 24,50,000

Selection Process

Resume Shortlist:	Yes
Written Test:	No
Online Test:	Yes
Group Discussion:	No
Medical Test:	No
Personal Interview:	Yes
No. of Rounds:	3 to 4
No. of Offers:	3
Minimum CGPA:	8

Eligibility

Recruiting PHDs: No

Eligible Departments: M.Tech in Integrated Electronics & Circuits, M.Tech in Radio Frequency Design & Technology, M.Tech in VLSI Design Tools & Technology