

# Job Notification Form, IIT Delhi

## Company Overview

<b>Name:</b>	Quasistatics Inc. (10xAR)
<b>Website:</b>	<a href="http://10xar.com">10xar.com</a>
<b>Company Type:</b>	Core (Technical)
<b>Description:</b>	10xAR is developing Google Glass 2.0: all-day wearable Augmented Reality (AR) headsets that have 20 hr battery life and 50g weight despite running cutting-edge computer vision algorithms.

## Job Details

<b>Designation:</b>	Silicon on Chip (SoC) Design Engineer II
<b>Type:</b>	Core (Technical)
<b>Place of Posting:</b>	Bengaluru
<b>Job Details:</b>	<p>We are seeking an Engineer to join our RF/Mixed-Signal team. You will work with a team of engineers designing IPs for advanced communications SOC's. These chips integrate complete PHY and MAC layers (including RF/analog circuits, digital ASIC, and CPU subsystems) on a single die, and are being designed in deep-submicron CMOS processes for use in next-generation wireless systems.</p> <p>Working in a fast-growing startup will offer you an oversight of the entire product development cycle, while fully challenging your ability to develop complex analog &amp; mixed-signal solutions for a completely new communication protocol that has 50X lower power than Bluetooth, and collaborating closely with other engineering teams to make the overall product successful.</p>
<b>Joining By:</b>	1 August 2022

## Salary Details

<b>CTC:</b>	3,000,000 INR Per Annum
<b>Gross:</b>	2,500,000 INR Per Annum
<b>CTC Breakup:</b>	- INR 20 lpa base compensation, paid monthly - Up to INR 1 lpa insurance and other benefits - Up to INR 50000 relocation assistance, one time - INR 1.5 lakh cash bonus at the end of the first year - INR 2 lakh cash bonus at the end of 2nd year - A stock option grant equivalent to INR 5 lakh at the valuation of the company as per the last funding round before your joining date. Stock option grants at our company vest equally in 48 months with a 12-month cliff.

# Selection Process

**Resume Shortlist:** Yes

**Written Test:** No

**Online Test:** No

**Group Discussion:** No

**Medical Test:** No

**Personal Interview:** Yes

**No. of Rounds:** 2

**No. of Offers:** 2

**Minimum CGPA:** 8.5

## Eligibility

**Recruiting PHDs:** No

**Eligible Departments:** M.Tech in Communications Engineering, M.Tech in Computer Technology, M.Tech in Control & Automation, M.Tech in Integrated Electronics & Circuits, M.Tech in Power Electronics, Electrical Machines & Drives, M.Tech in Power Systems, M.Tech in Radio Frequency Design & Technology, M.Tech in Telecommunication Technology & Management, M.Tech in VLSI Design Tools & Technology, M.S.(R) in Electrical Engineering, M.S.(R) in VLSI Design Tools and Technology, M.S.(R) in Telecommunication Technology and Management