Job Notification Form, IIT Delhi

Company Overview

Name:	HFCL Limited
Website:	https://www.hfcl.com/
Company Type:	Core (Technical)
Description:	? Leading manufacturer of OFC in India, ? State-of-the art Manufacturing and R&D facilities, ? 20+years of deploying telecommunication networks, ? Executing 8500Crs+ worth of Defense project, ? Exports to more than 60 countries

Job Details

Designation:	Graduate Engineer Trainee
Type:	Core (Technical)
Place of Posting:	Bangalore/Delhi NCR/Chennai/Goa/Hyderabad
Job Details:	Post Graduate Engineer Trainee - JD

Engineers hired from campus shall undergo 1 year training program, as a part of GET Spark Program. The 1 year training of graduate engineer trainees comprises of 2 weeks Induction, 1 week Behavioural/Functional Training program, 2 Weeks Plant visit, 1 week rotation in BU/Function and on-the-job training for approximately 11 months to take up specific role.

The objective of the one year training program is to groom young engineers into technical and business leaders by giving them an overview of the organization, actively involving in business, projects, sites, plants visits. On successful completion of 1 year training, PGETs shall be inducted as Manager.
Job Summary:
HFCL is developing 5G Radio Access Network (RAN) products based on open interfaces defined by 3GPP and O-RAN specifications.
Duties and Responsibilities:
• The job involves development of product requirements document and detailed system specifications for products in the area of 5G Radio Access Network (RAN).
• The job will involve design and development of hardware and software systems to realize the functionality of Radio Unit (RU), Distributed Unit (DU), Centralized Unit, Small cell, O-RAN Front haul, Mid-haul and Backhaul transport products.
☐ The hardware development will involve hardware board design, RTL logic design for FPGA/ASIC using Verilog, design of Digital and Analog RF frontend, Antenna design. The software development will involve development of 5G Physical layer functionalities including Baseband signal processing, digital pre-distortion (DPD) algorithms, Crest Factor Reduction (CFR) algorithms, MIMO beamforming, channel filters etc.
☐ The software development will also involve development of protocol features in MAC, RLC, PDCP, RRC and OAM layers, and development of management and orchestration functions.
• The job will require programming skills in C/C++, Python, Java, Verilog and familiarity with system simulations MATLAB, ADS, Signal generator, signal

analyzer, Telecom standards like 3GPP, O-RAN, IEEE etc.
• The ability to work in a team and taking ownership of the assigned tasks independently is essential.
• The job offers opportunity for creating patents, publishing research papers, contributing in standards and presenting in leading technical forums.

Eligibility Criteria:
B. Tech + M. Tech (Dual)
Internship+ Full time employment offer for final year student. Offers granted during the placement session will be final.

Key Desired Skills:
• C/C++,Python, Java, Verilog
• Enthusiastic, vibrant, energetic, youthful, hardworking
• Adaptable and flexible to travel
• Openness to learn
• Good analytical and logical skills
• Positive attitude/Forward looking attitude
• Good presentation and communication skills

Joining By: 15 July 2022

Salary Details

CTC:	1,500,000 INR Per Annum
Gross:	1,305,864 INR Per Annum
CTC Breakup:	Basic Salary ₹5,60,000 House Rent Allowance ₹2,80,000 Transport Allowance ₹0 Special Allowance ₹4,65,864 Medical Allowance ₹0 Gross Salary ₹13,05,864 Variable Components Gratuity ₹26,936 Provident Fund ₹67,200 Variable Component 1 ₹0 Variable Component 2 ₹0 Variable Component 3 ₹0 CTC ₹14,00,000 Retention Bonus ₹1,00,000
Perks / Bonus:	Internship Monthly Salary -Internship Stipend- 30,000/ per month Full Time Employment CTC Full Time offer- 15 LPA (14 Lakhs Fixed & 1 Lakh Retention Bonus) Retention Plan Summary- 1st Year: 15 LPA(14 Lakhs+ 1 Lakh Retention Bonus) 2nd Year: 17 Lakhs(15 Lakhs fixed + 2 Lakhs Retention Bonus) 3rd Year: 20 Lakhs (17 Lakhs + 3 Lakhs Retention Bonus)

Selection Process

Resume Shortlist:	Yes

Written Test:	Yes
Online Test:	Yes
Group Discussion:	No
Medical Test:	No
Personal Interview:	Yes
No. of Rounds:	1
No. of Offers:	2
Minimum CGPA:	60 percent above in all academics

Eligibility

Recruiting PHDs:	No
Eligible Departments:	B.Tech and M.Tech in Biochemical Engg & Biotechnology, B.Tech and M.Tech in Chemical Engineering, B.Tech and M.Tech in Computer Science & Engineering, B.Tech and M.Tech in Mathematics & Computing, B.Tech in Civil Engineering and M.Tech in Geotechnical and Geoenvironmental Engineering, B.Tech in Civil Engineering and M.Tech in Water Resources Engineering, B.Tech in Mechanical Engineering and M.Tech in Thermal Engineering, B.Tech in Mechanical Engineering and M.Tech in Computer Science & Engineering, B.Tech in Civil Engineering and M.Tech in Structural Engineering, B.Tech in Civil Engineering and M.Tech in Construction Engineering & Management, B.Tech in Textile Engineering and M.Tech in Computer Science & Engineering, B.Tech in Engineering Physics and M.Tech in Computer Science & Engineering, B.Tech in Chemical Engineering and M.Tech in Computer Science & Engineering, B.Tech in Production & Industrial Engineering and M.Tech in Computer Science & Engineering, B.Tech in Mechanical Engineering and M.Tech in Computer Science & Engineering, B.Tech in Mechanical Engineering and M.Tech in Computer Science & Engineering, B.Tech in Production & Industrial Engineering and M.Tech in Production Engineering