

# Job Notification Form, IIT Delhi

## Company Overview

**Name:** Taiwan Semiconductor Manufacturing Company

**Website:** <https://www.tsmc.com/>

**Company Type:** Core (Technical)

**Description:** Established in 1987 and headquartered in Hsinchu Science Park, Taiwan, TSMC pioneered the pure-play foundry business model with an exclusive focus on manufacturing customers' products. By choosing not to design, manufacture or market any semiconductor products under its own name, the Company ensures that it never competes with its customers. And so, the key to TSMC's success has always been to focus on its customers' success. TSMC's foundry business model has enabled the rise of the global fabless industry, and since its inception TSMC has been the world's leading semiconductor foundry. The Company manufactured 11,617 different products using 281 distinct technologies for 510 different customers in 2020.

TSMC-made semiconductors serve a global customer base that is large and diverse and includes a wide range of applications. These products are used in a variety of end markets including mobile devices, high performance computing, automotive electronics and the Internet of Things (IoT). Such strong diversification helps to smooth fluctuations in demand, which in turn allows TSMC to maintain higher levels of capacity utilization and profitability, and generate healthy returns for future investment.

The annual capacity of the manufacturing facilities managed by TSMC and its subsidiaries exceeded 12 million 12-inch equivalent wafers in 2020. These facilities include four 12-inch wafer GIGAFAB® fabs, four 8-inch wafer fabs, and one 6-inch wafer fab – all in Taiwan – as well as one 12-inch wafer fab at a wholly owned subsidiary, TSMC Nanjing Company Limited, and two 8-inch wafer fabs at wholly owned subsidiaries, WaferTech in the United States and TSMC China Company Limited.

In May 2020, TSMC announced its intention to build and operate an advanced semiconductor fab in the United States, in order to better support customers and partners there as well as to attract global talents. This facility, to be built in Arizona, will utilize TSMC's 5-nanometer technology for semiconductor wafer fabrication and will have a capacity of 20,000 semiconductor wafers per month. Construction is planned to start in 2021 with production targeted for 2024.

TSMC provides customer support, account management and engineering services through offices in North America, Europe, Japan, China, and South Korea. At the end of 2020, the Company and its subsidiaries employed more than 56,000 people worldwide.

The Company is listed on the Taiwan Stock Exchange (TWSE) under ticker number 2330, and its American Depositary Shares (ADSs) are traded on the New York Stock Exchange (NYSE) under the symbol TSM.

## Job Details

**Designation:** Chip Design Engineer

**Type:** Core (Technical)

**Place of Posting:** Hsinchu City, Taiwan

**Job Details:**

1. Block level and top level Digital Design impletmentaion from RTL to Physical Design, including Synthesis, DFT, DV, floorplan, CTS, P&R, Performance/Power/Area Optimization, Timing Closure, EM/IR and PV fixes, design signoff
2. design and sign-off methodology development and EDA flow enhancement
3. R&D, innovation and path-finding for most advanced process challenges
4. Quality infrastructure development and project/program management
5. Self-motivated, strong ownership and commitment, quick learner, and good teamwork

Requirements:

1. Familiar with process fabrication, CAD, algorithm, EDA tool, APR, timing, IR/EM, low power, physical verification, device modeling , VLSI chip implementation or 3DIC
2. Innovative and good problem solving skills
3. Willing to learn all aspects of chip design and come up with new solutions
4. Familiar with AI/ML technology is a plus
3. Familiar with Script languages (shell, python, perl, TCL)

**International:** Yes

**Joining By:** 31 August 2022

## Salary Details

**CTC:** 71,011 USD Per Annum

**Gross:** 59,176 USD Per Annum

**CTC Breakup:**

1. Including 12-month base pay, 2-month year-end bonus, profit sharing, 2-month relocation bonus, and 4-to-6-month sign on bonus. (\*Design incentive bonus is included in "profit sharing".)
2. Base Salary, year-end bonus are fixed and recurring; Relocation and sign on bonus are one shot paid after onboard.

\*Above salary is for Master students, PhD students will get higher pay.

**Perks / Bonus:** Mentioned as CTC breakup.

**Bond:** Yes

## Selection Process

**Resume Shortlist:** Yes

**Written Test:** No

**Online Test:** No

**Group Discussion:** No

**Medical Test:** No

<b>Personal Interview:</b>	Yes
<b>No. of Rounds:</b>	2
<b>No. of Offers:</b>	7
<b>Minimum CGPA:</b>	8

## Eligibility

<b>Recruiting PHDs:</b>	No
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<b>Eligible Departments:</b>	M.Tech in Communications Engineering, M.Tech in Computer Science & Engineering, M.Tech in Computer Technology, M.Tech in Control & Automation, M.Tech in Integrated Electronics & Circuits, M.Tech in Power Electronics, Electrical Machines & Drives, M.Tech in Power Systems, M.Tech in Radio Frequency Design & Technology, M.Tech in VLSI Design Tools & Technology, M.Sc in Mathematics, B.Tech in Engineering Physics and M.Tech in Computer Science & Engineering, B.Tech in Chemical Engineering and M.Tech in Computer Science & Engineering, B.Tech in Production & Industrial Engineering and M.Tech in Computer Science & Engineering, B.Tech in Mechanical Engineering and M.Tech in Computer Science & Engineering, M.S.(R) in Computer Science & Engineering, M.S.(R) in Electrical Engineering, M.S.(R) in VLSI Design Tools and Technology
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