

Job Notification Form, IIT Delhi

Company Overview

Name:	NVIDIA
Website:	www.nvidia.com
Company Type:	Core (Technical)
Description:	In 1999 sparked the growth of the PC gaming market, redefined modern computer graphics, and revolutionized parallel computing. More recently, GPU deep learning ignited modern AI — the next era of computing — with the GPU acting as the brain of computers, robots, and self-driving cars that can perceive and understand the world.

Job Details

Designation:	Formal Verification Engineer
Type:	Core (Technical)
Place of Posting:	Gurgaon
Job Details:	<p>NVIDIA is seeking Formal Verification Engineers for our Hardware Team.</p> <p>At NVIDIA, our employees are passionate about parallel and visual computing. We're united in our quest to transform the way graphics are used to tackle some of the most complex problems in computer science. It started out as an engine for simulating human imagination, conjuring up the amazing virtual worlds of video games and Hollywood films. Today, NVIDIA's GPU simulates human intelligence, running deep learning algorithms and acting as the brain of computers, robots, and self-driving cars that can perceive and understand the world. This is our life's work — to amplify human imagination and intelligence. And we have only scratched the surface of what we can accomplish when we apply our technology to it. We need passionate, hardworking, and creative people to help us take on some of these once-in-a-lifetime opportunities.</p> <p>This position offers the opportunity to have real impact in a progressive, technology-focused company impacting product lines ranging from consumer graphics to self-driving cars and the growing field of artificial intelligence. We have crafted a team of outstanding people stretching around the globe, whose mission is to push the frontiers of what is possible today and define the platform for the future of computing.</p> <p>What you'll be doing:</p>

- Learning state-of-the-art formal verification methodologies
- Responsible for applying best-in-class formal methodologies on complex digital designs, IP blocks and SoCs
- Taking ownership of verifying a design by
 - o Developing a robust test plan
 - o Executing it by working with the design team
 - o Coordinating with other verification team members for closure

What we need to see:

- B.Tech. / Dual Degree from any department and M.Tech. / Ph.D. from CS / EE departments
- Strong analytical skills to solve difficult problems

Ways to stand out from the crowd:

- Excellent problem-solving and debugging skills
- Great interpersonal skills and a team player
- Burning desire to learn new and diverse engineering areas

With competitive salaries and a generous benefits package, we are widely considered to be one of the technology world's most desirable employers. We have some of the most brilliant and talented people in the world working for us and, due to unprecedented growth, our elite engineering teams are rapidly growing. If you're a creative and autonomous engineer with a real passion for technology, we want to hear from you. As we highly value diversity in our current and future employees, we do not discriminate (including in our hiring and promotion practices) based on race, religion, color, national origin, gender, gender expression, sexual orientation, age, marital status, veteran status, disability status or any other characteristic protected by law.

Joining By: 25 July 2022

Salary Details

CTC:	2,406,248 INR Per Annum
Gross:	1,756,248 INR Per Annum
CTC Breakup:	Compensation Components Direct Campus B.Tech (IC1) Dual Degree / M.Tech (IC2) Base 1,550,000 1,725,000 Retirals 130,278 144,986 Base + Retirals 1,680,278 1,869,986 Sign on Bonus 300,000 300,000 Value of key benefits 75,970 78,689 Total Compensation 2,056,248 2,248,675 Equity Value - Overall 4 Years 1,400,000 1,700,000 Equity Value – Annual 350,000 425,000 Total Compensation + Equity of 1 year 2,406,248 2,673,675 Total CTC + Equity of 4 years 3,456,248 3,948,675
Perks / Bonus:	INR 300,000 joining bonus

Selection Process

Resume Shortlist: No

Written Test: No

Online Test: Yes

Group Discussion: No

Medical Test: No

Personal Interview: Yes

No. of Rounds: 2 to 3

No. of Offers: 7

Eligibility

Recruiting PHDs: No

Eligible Departments: B.Tech in Biochemical Engineering & Biotechnology, B.Tech in Chemical Engineering, B.Tech in Civil Engineering, B.Tech in Computer Science & Engineering, B.Tech in Electrical Engineering, B.Tech in Electrical Engineering (Power and Automation), B.Tech in Engineering Physics, B.Tech in Engineering and Computational Mechanics, B.Tech in Materials Engineering, B.Tech in Mathematics & Computing, B.Tech in Mechanical Engineering, B.Tech in Production & Industrial Engineering, B.Tech in Textile Engineering, B.Tech and M.Tech in Biochemical Engg & Biotechnology, B.Tech and M.Tech in Chemical Engineering, B.Tech and M.Tech in Computer Science & Engineering, B.Tech and M.Tech in Mathematics & Computing, M.Tech in Radio Frequency Design & Technology, M.Tech in VLSI Design Tools & Technology, B.Tech in Civil Engineering and M.Tech in Geotechnical and Geoenvironmental Engineering, B.Tech in Civil Engineering and M.Tech in Water Resources Engineering, B.Tech in Mechanical Engineering and M.Tech in Thermal Engineering, B.Tech in Mechanical Engineering and M.Tech in Computer Science & Engineering, B.Tech in Civil Engineering and M.Tech in Structural Engineering, B.Tech in Civil Engineering and M.Tech in Construction Engineering & Management, B.Tech in Textile Engineering and M.Tech in Computer Science & Engineering, B.Tech in Engineering Physics and M.Tech in Computer Science & Engineering, B.Tech in Chemical Engineering and M.Tech in Computer Science & Engineering, B.Tech in Production & Industrial Engineering and M.Tech in Computer Science & Engineering, B.Tech in Mechanical Engineering and M.Tech in Computer Science & Engineering, B.Tech in Mechanical Engineering and M.Tech in Computer Science & Engineering, B.Tech in Production & Industrial Engineering and M.Tech in Production Engineering, M.S.(R) in VLSI Design Tools and Technology