Job Notification Form, IIT Delhi

Company Overview

Name: Micron Technologies India Operations LLP

Website: www,micron.com

Company Type:

Core (Technical)

Description:

Micron is a world leader in innovative memory solutions that transform how the world uses information. For over 40 years, our company has been instrumental to the world's most significant technology advancements, delivering optimal memory and storage systems for a broad range of applications.

Job Details

Designation: Memory Circuit Design Engineer

Core (Technical) Type:

Place

of Hyderabad

Posting:

Job Details:

Micron Technology's vision is to transform how the world uses information to enrich life and our commitment to people, innovation, tenacity, collaboration, and customer focus allows us to fulfill our mission to be a global leader in memory and storage solutions. This means conducting business with integrity, accountability, and professionalism while supporting our global community.

As a Memory Circuit Design Engineer in our DRAM and Emerging memory Group (DEG) at Micron Technology, Inc., you will be responsible for designing and analyzing digital and analog circuits used in the development of memory products. This includes simulating, optimizing and floor planning DRAM circuits. In this position, you will collaborate with Micron's various design and verification teams all over the world and support the efforts of groups such as Product Engineering, Test, Probe, Process Integration, Assembly and Marketing to proactively design products that optimize all manufacturing functions and assure the best cost, quality, reliability, time-to-market, and customer satisfaction.

Responsibilities will include, but are not limited to:

- 1. Contributing to the development of new DRAM and emerging memory product opportunities by assisting with the overall design, layout, and optimization of Memory/Logic/Analog circuits
- 2. Parasitic modeling and assisting in design validation, reticle experiments and required tape-out revisions
- 3. Overseeing the layout process including floor-planning, placement, and routing
- 4. Performing verification processes with modeling and simulation using industry standard simulators
- 5. Contributing to cross group communication to work towards standardization and group success
- 6. Working with Marketing, Probe, Assembly, Test, Process Integration, and Product Engineering groups to ensure proper manufacturability of product
- 7. Proactively solicit inputs from Standards, CAD, modeling, and verification groups to ensure the design quality
- 8. Driving innovation into the future Memory generations within a dynamic work environment.

Successful candidates for this position will have:

- 1. Extensive knowledge of CMOS Circuit Design, Layout and Device Physics
- 2. Knowledge and exposure in Finesim and/or HSpice
- 3. Knowledge with power network analysis
- 4. Knowledge and exposure in verilog modeling and simulation tools
- 5. Knowledge of state machine logic / design verification techniques
- 6. Excellent problem-solving and analytical skills
- 7. A self-motivated, enthusiastic team player who enjoys working with others
- 8. Good communication skills with the ability to convey complex technical concepts to other design and verification peers in verbal and written form
- 9. Familiarity with a scripting language (Python, Tcl, Perl, etc)
- 10. Familiarity with memory design is a big plus

Qualifications:

Bachelors or Post Graduate Degree in Electronics Engineering or related engineering field.

Micron is an equal opportunity employer and values diversity at our company. We do not discriminate on the basis of race, religion, color, national origin, gender, sexual orientation, age, marital status, veteran status, or disability status.

Joining By: 25 July 2022

Salary Details

CTC: 2,065,000 INR Per Annum

Gross: 1,486,000 INR Per Annum

CTC Bachelors Masters

Breakup: Base Pay 1430000 Base Pay 1540000

IPP*** 143000 IPP*** 154000

Discretionary Allowance 56000 Discretionary Allowance 56000

Provident Fund 85824 Provident Fund 92400

Relocation amount** 200000 Relocation amount** 200000

Joining Bonus* 150000 Joining Bonus* 150000

Selection Process

Resume Shortlist: Yes

Written Test:

No

Online Test:

Yes

Group

No

Discussion:

Medical Test: No

Personal

Yes

Interview:

No. of 2

Rounds:

No. of 2 Offers:

Minimum 7 CGPA:

Eligibility

Recruiting PHDs:

No

Eligible Departments:

B.Tech in Electrical Engineering, B.Tech in Electrical Engineering (Power and Automation), B.Tech in Engineering Physics, M.Tech in Integrated Electronics & Circuits, M.Tech in Radio Frequency Design & Technology, M.Tech in VLSI Design Tools & Technology, M.S.(R) in VLSI Design Tools and Technology, M.S.(R) in Electrical Engineering