

# Job Notification Form, IIT Delhi

## Company Overview

<b>Name:</b>	Micron Technology Operations LLP
<b>Website:</b>	<a href="http://in.micron.com">in.micron.com</a>
<b>Company Type:</b>	Core (Technical)
<b>Description:</b>	Semiconductor

## Job Details

**Designation:** Singapore - CMOS Engineer - Phd Only

**Type:** Core (Technical)

**Place of Posting:** Singapore

**Job Details:** Process Integration (CMOS) (Evergreen)As a Process Integration Device Engineer, you will be responsible for advancing the CMOS and CELL performance on the future, current and legacy technology nodes.

Your primary responsibility will be to optimize and sustain CMOS and CELL performance to ensure good probe/backend yields and reliability.

Communication with Product Engineering and the Research and Development teams is a crucial and critical job function.

Responsibilities and Tasks Proactively engage with DOEs to optimize CMOS devices to meet ESPEC targets and ensure that devices are centered for optimum probe/backend yields, performance and reliability.

Proactively engage with DOEs to optimize CMOS/CELL performance to ensure products meet qual specifications and engage in continuous improvements to minimize probe/backend yield fallout.

Continuously audit probe, parametric and qual activities at regular frequencies and engage in CMOS/CELL related analysis, identify issues and drive towards root cause fix in collaboration with PI, YE, PEE teams.

Partner with R and D, QEM, PEE, YE and PE to improve cumulative yield, and reliability on all products across various technology nodes.

Collaborate with CMOS/CELL counterparts from R and D and other manufacturing sites to leverage lessons learnt and establish BKMs where possible.

Participate in World Wide focus teams established towards fixing issues and Fab matching.

Set challenging goals to support integration of new technology nodes into the Fab.

Maintain required training, as well as job critical knowledge and skills.

Proactively create and maintain critical CMOS/CELL documents targeted towards

self-learning which also serves as a reference for the fab.

Actively contribute to a safe and positive working environment.

**International:** Yes

**Joining By:** 6 January 2022

## Salary Details

**CTC:** 103,955 USD Per Annum

**Gross:** 89,050 USD Per Annum

**CTC Breakup:** Phd  
USD 89,050.00  
USD 8,905.00  
  
USD 6,000.00  
USD 103,955.00

## Selection Process

**Resume Shortlist:** Yes

**Written Test:** Yes

**Online Test:** Yes

**Group Discussion:** No

**Medical Test:** No

**Personal Interview:** Yes

**No. of Rounds:** 2

**No. of Offers:** 1

**Minimum CGPA:** 7

## Eligibility

**Recruiting PHDs:** Yes

**Eligible Departments:**

