## Addition or Subtraction

add 80, 37

First Operand	Bit <b>7</b>	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Hexa- Decimal Value				Decimal Value				
Second Operand	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Hexa- Decimal Value				Decimal Value				
Result	Bit <b>7</b>	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Hexa- Decimal Value				Decimal Value				
Control Status Register Alterations	I	Т	Н	S	V	N	Z	С
Reason for alterations								

## Sub 80, 37

First	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Operand								
Hexa- Decimal Value				Decimal Value				
Second Operand	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Hexa- Decimal Value		ı	ı	Decimal Value		ı		
Result	Bit <b>7</b>	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Hexa- Decimal Value				Decimal Value				
Control	I	T	Н	S	V	N	Z	С
Status Register								
Alterations								
Reason for alterations								

## add 80, -37

First Operand	Bit <b>7</b>	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Hexa- Decimal Value				Decimal Value				
Second Operand	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Hexa- Decimal Value				Decimal Value		I		
Result	Bit <b>7</b>	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Hexa- Decimal Value				Decimal Value				
Control Status Register	I	Т	Н	S	V	N	Z	С
Alterations								
Reason for alterations								

## add -80, 37

First	Bit <b>7</b>	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Operand								
Hexa- Decimal Value				Decimal Value				
Second Operand	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Hexa- Decimal Value			I	Decimal Value		I		
Result	Bit <b>7</b>	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Hexa- Decimal Value				Decimal Value				
Control	I	T	Н	S	V	N	Z	С
Status Register								
Alterations								
Reason for alterations								