Multiply 0x011 & 0x0AB

mul 0x011, 0x0AB

First Operand	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Hexa-Decimal Value				Decimal Value to mul				
Second Operand	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Hexa-Decimal Value				Decimal Value to mul				
R1	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
R0	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Hexa-Decimal Value				Decimal Value				
Control Status Register	I	T	H	S	V	N	Z	C
Alterations								
Reason for alterations								

muls 0x011, 0x0AB

First Operand	Bit **7**	Bit **6**	Bit **5**	Bit **4**	Bit **3**	Bit **2**	Bit **1**	Bit **0**
Hexa-Decimal Value				Decimal Value to mul				
Second Operand	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Hexa-Decimal Value				Decimal Value to mul				
R1	Bit **7**	Bit **6**	Bit **5**	Bit **4**	Bit **3**	Bit **2**	Bit **1**	Bit **0**
R0	Bit **7**	Bit **6**	Bit **5**	Bit **4**	Bit **3**	Bit **2**	Bit **1**	Bit **0**
Hexa-Decimal Value				Decimal Value				
Control Status Register	I	T	H	S	V	N	Z	C
Alterations								
Reason for alterations								

mulsu 0x011, 0x0AB

First Operand	Bit **7**	Bit **6**	Bit **5**	Bit **4**	Bit **3**	Bit **2**	Bit **1**	Bit **0**
Hexa-Decimal Value				Decimal Value to mul				
Second Operand	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Hexa-Decimal Value				Decimal Value to mul				
R1	Bit **7**	Bit **6**	Bit **5**	Bit **4**	Bit **3**	Bit **2**	Bit **1**	Bit **0**
R0	Bit **7**	Bit **6**	Bit **5**	Bit **4**	Bit **3**	Bit **2**	Bit **1**	Bit **0**
Hexa-Decimal Value				Decimal Value				
Control Status Register	I	T	H	S	V	N	Z	C
Alterations								
Reason for alterations								